

Cooperative Groups: a flexible model for synchronization and communication within groups of threads.

At a glance

Scalable Cooperation among groups of threads

Flexible parallel decompositions

Composition across software boundaries

Deploy Everywhere

Benefits <u>all</u> applications

Examples include:
Persistent RNNs
Physics
Search Algorithms
Sorting

LEVELS OF COOPERATION: TODAY

Warp X Warp X __syncthreads(): block level synchronization barrier in CUDA GPU X Multi-GPU X



LEVELS OF COOPERATION: CUDA 9.0

```
For current coalesced set of threads:
  auto g = coalesced_threads();
For warp-sized group of threads:
 auto block = this_thread_block();
                                                             Warp
 auto g = tiled_partition<32>(block)
For CUDA thread blocks:
  auto g = this thread block();
For device-spanning grid:
  auto g = this grid();
                                                                             GPU
                                                                          Multi-GPU
For multiple grids spanning GPUs:
  auto g = this multi grid();
  All Cooperative Groups functionality is
```

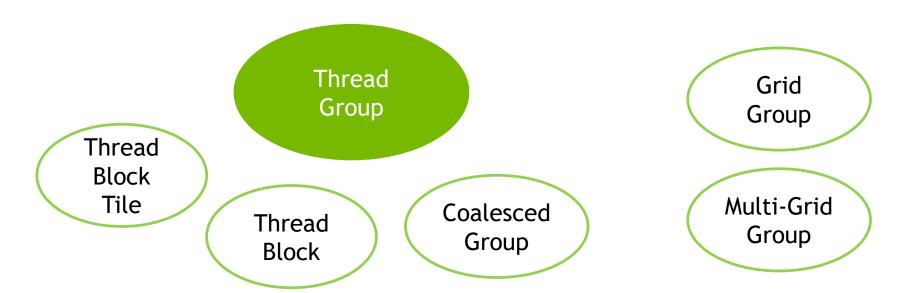
within a cooperative groups:: namespace

THREAD GROUP

Base type, the implementation depends on its construction.

Unifies the various group types into one general, collective, thread group.

We need to extend the CUDA programming model with handles that can represent the groups of threads that can communicate/synchronize





THREAD BLOCK

Implicit group of all the threads in the launched thread block

Implements the same interface as thread_group:

```
// Synchronize the threads in the group
void sync();
unsigned size();
                          // Total number of threads in the group
unsigned thread_rank();
                          // Rank of the calling thread within [0, size]
bool is_valid();
                          // Whether the group violated any API constraints
And additional thread_block specific functions:
dim3 group_index();
                          // 3-dimensional block index within the grid
                          // 3-dimensional thread index within the block
dim3 thread_index();
```



PROGRAM DEFINED DECOMPOSITION

CUDA KERNEL



All threads launched

thread_block g = this_thread_block();

foobar(thread_block g)



All threads in thread block

thread_group tile32 = tiled_partition(g, 32);



thread_group tile4 = tiled_partition(tile32, 4);

















Restricted to powers of two, and <= 32 in initial release



GENERIC PARALLEL ALGORITHMS

Per-Block

Per-Warp

```
g = this_thread_block();
reduce(g, ptr, myVal);
g = tiled_partition(this_thread_block(), 32);
reduce(g, ptr, myVal);
```

THREAD BLOCK TILE

A subset of threads of a thread block, divided into tiles in row-major order

```
thread block tile<32> tile32 = tiled partition<32>(this thread block());
thread block tile<4> tile4 = tiled partition<4>(this thread block());
                                                         .any()
                                          .shfl()
Exposes additional functionality:
                                          .shfl down()
                                                         .all()
                                                         .ballot()
                                          .shfl up()
                                                         .match any()
                                          .shfl xor()
Size known at compile time = fast!
                                                         .match all()
```



STATIC TILE REDUCE

Per-Tile of 16 threads

```
g = tiled_partition<16>(this_thread_block());
tile_reduce(g, myVal);
```



```
template <unsigned size>
__device__ int tile_reduce(thread_block_tile<size> g, int val) {
  for (int i = g.size()/2; i > 0; i /= 2) {
    val += g.shfl_down(val, i);
  }
  return val;
}
```

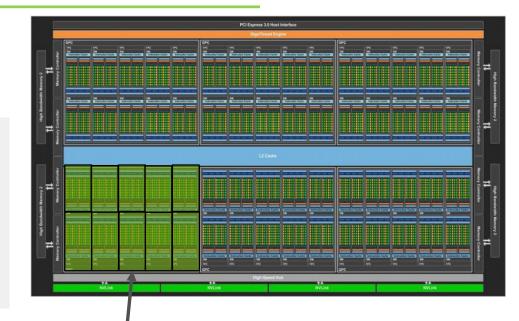
GRID GROUP

A set of threads within the same grid, guaranteed to be resident on the device

New CUDA Launch API to opt-in:

cudaLaunchCooperativeKernel(...)

```
__global__ kernel() {
    grid_group grid = this_grid();
    // load data
    // loop - compute, share data
        grid.sync();
        // devices are now synced
}
```



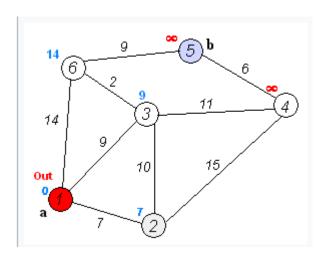
Device needs to support the cooperativeLaunch property.

cudaOccupancyMaxActiveBlocksPerMultiprocessor(&numBlocksPerSm, kernel, numThreads, 0));

GRID GROUP

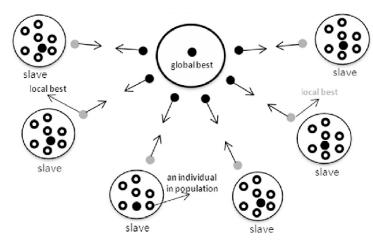
The goal: keep as much state as possible resident

Shortest Path / Search



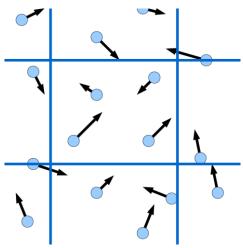
Weight array perfect for persistence Iteration over vertices? Fuse!

Genetic Algorithms / Master driven algorithms



Synchronization between a master block and slaves

Particle Simulations

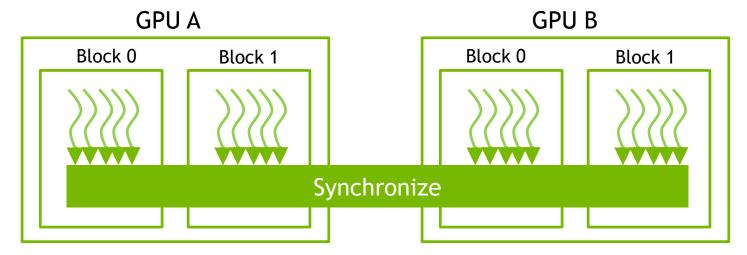


Synchronization between update and collision simulation

MULTI GRID GROUP

A set of threads guaranteed to be resident on the same system, on multiple devices

```
__global___ void kernel() {
    multi_grid_group multi_grid = this_multi_grid();
    // load data
    // loop - compute, share data
        multi_grid.sync();
        // devices are now synced, keep on computing
}
```



MULTI GRID GROUP

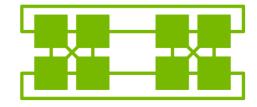
Launch on multiple devices at once

New CUDA Launch API to opt-in:

cudaLaunchCooperativeKernelMultiDevice(...)

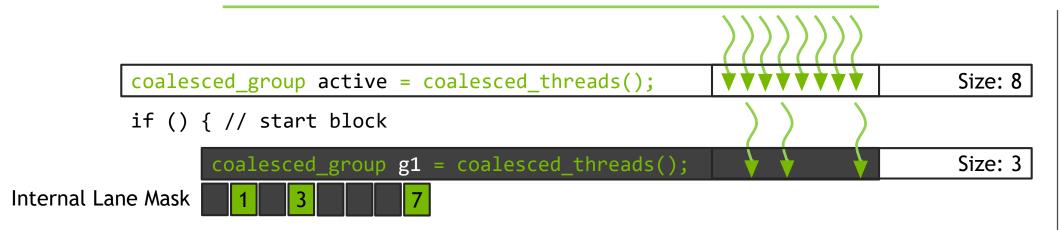
Devices need to support the cooperativeMultiDeviceLaunch property.

```
struct cudaLaunchParams params[numDevices];
for (int i = 0; i < numDevices; i++) {
    params[i].func = (void *)kernel;
    params[i].gridDim = dim3(...); // Use occupancy calculator
    params[i].blockDim = dim3(...);
    params[i].sharedMem = ...;
    params[i].stream = ...; // Cannot use the NULL stream
    params[i].args = ...;
}
cudaLaunchCooperativeKernelMultiDevice(params, numDevices);</pre>
```

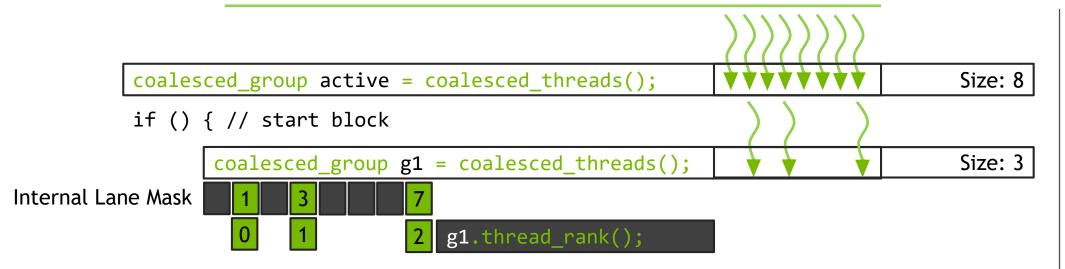


Discover the set of coalesced threads, i.e. a group of converged threads executing in SIMD

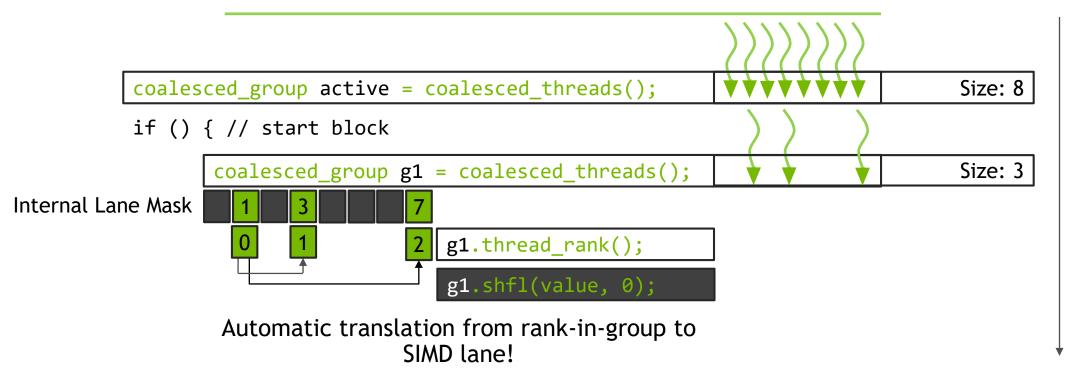
coalesced_group active = coalesced_threads();
Size: 8



Discover the set of coalesced threads, i.e. a group of converged threads executing in SIMD



Automatic translation to rank-in-group!



```
coalesced_group active = coalesced_threads();
                                                                                      Size: 8
          if () { // start block
                                                                                      Size: 3
                  coalesced_group g1 = coalesced_threads();
Internal Lane Mask
                                       g1.thread_rank();
                                       g1.shfl(value, 0);
                                       g2 = tiled partition(g1, 2);
                                                                                Size: 2 and 1
```

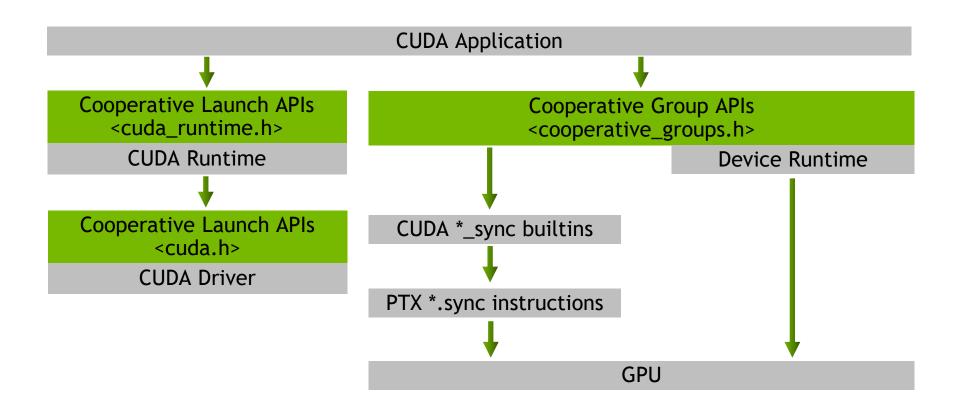
```
coalesced_group active = coalesced_threads();
                                                                                      Size: 8
          if () { // start block
                                                                                      Size: 3
                  coalesced_group g1 = coalesced_threads();
Internal Lane Mask
                                       g1.thread_rank();
                                       g1.shfl(value, 0);
                                       g2 = tiled partition(g1, 2);
                                                                                Size: 2 and 1
          } // end block
          active.sync()
```

ATOMIC AGGREGATION

Opportunistic cooperation within a warp

```
inline __device__ int atomicAggInc(int *p)
{
    coalesced_group g = coalesced_threads();
    int prev;
    if (g.thread_rank() == 0) {
        prev = atomicAdd(p, g.size());
    }
    prev = g.thread_rank() + g.shfl(prev, 0);
    return prev;
}
```

ARCHITECTURE



WARP SYNCHRONOUS PROGRAMMING IN CUDA 9.0

About 17,200 results (0.27 seconds)

[PDF] Warp-synchronous programming - Irisa

www.irisa.fr/alf/downloads/collange/cours/gpuprog_ufmg.../gpu_ufmg_2015_5.pdf ▼ Mar 9, 2016 - Lane ID exists in PTX, not in C for CUDA. Can be recovered using If you know warp-synchronous programming in CUDA, you know SIMD ...

Warp synchronous programming - NVIDIA Developer Forums

https://devtalk.nvidia.com/default/topic/807699/warp-synchronous-programming/ ▼ Jan 30, 2015 - http://docs.nvidia.com/cuda/cuda-c-programming-guide/index.html#volatile-qualifier regarding this: "If dynamically allocated shared memory of ...

Is syncthreads required within a warp? - NVIDIA Developer Forums

https://devtalk.nvidia.com/default/topic/.../is-syncthreads-required-within-a-warp-/ ▼ Nov 6, 2013 - cuda threads fence applied on share memory has the same effect only that it ... Warp synchronous programming is also safe across all current ...

warp synchronous programming is a lie \cdot Issue #167 \cdot AccelerateHS ...

https://github.com/AccelerateHS/accelerate/issues/167 ▼
May 6, 2014 - Until at least CUDA 4, warp synchronous programming was the advertised and recommended way to get the best performance from a GPU.

cuda - What is warp-level-programming (racecheck) - Stack Overflow stackoverflow.com/questions/19011826/what-is-warp-level-programming-racecheck ▼ Sep 25, 2013 - Its true that all processing is handled in warps. Warp level programming also called warp synchronous programming depends on this to ensure ...

parallel processing - CUDA __syncthreads() usage within a warp ... stackoverflow.com/questions/10205245/cuda-syncthreads-usage-within-a-warp ▼

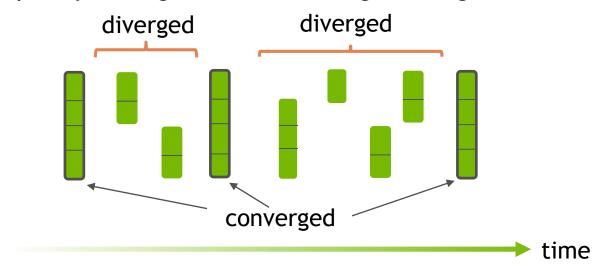
Apr 18, 2012 - Updated with more information about using volatile. Presumably you want all threads ... On the face of it this means you can omit the __syncthreads() , a practice known as "warp-synchronous programming". However, there are ...

cuda - Struggling with intuition regarding how warp-synchronous ... stackoverflow.com/.../struggling-with-intuition-regarding-how-warp-synchronous-thr... ▼ Dec 4, 2013 - Let's look at the code in blocks, and answer your questions along the way: int sum Now we are finally getting into some warp-synchronous programming. This line of code depends on the fact that 32 threads are executing in ...

CUDA WARP THREADING MODEL

NVIDIA GPU multiprocessors create, manage, schedule and execute threads in warps (32 parallel threads).

Threads in a warp may diverge and re-converge during execution.



Full efficiency may be realized when all 32 threads of a warp are converged.

WARP SYNCHRONOUS PROGRAMMING

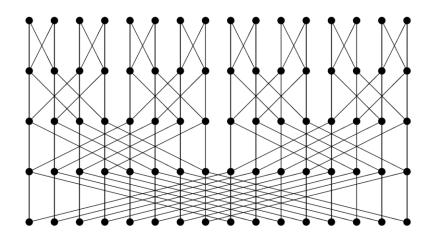
Warp synchronous programming is a CUDA programming technique that leverages warp execution for efficient inter-thread communication.

e.g. reduction, scan, aggregated atomic operation, etc.

CUDA C++ supports warp synchronous programming by providing warp synchronous built-in functions and cooperative group collectives.

EXAMPLE: SUM ACROSS A WARP

```
\label{eq:val} $$ val = input[lane_id]; $$ val += __shfl_xor_sync(0xffffffff, val, 1); $$ val += __shfl_xor_sync(0xffffffff, val, 2); $$ val += __shfl_xor_sync(0xffffffff, val, 4); $$ val += __shfl_xor_sync(0xffffffff, val, 8); $$ val += __shfl_xor_sync(0xffffffff, val, 16); $$
```

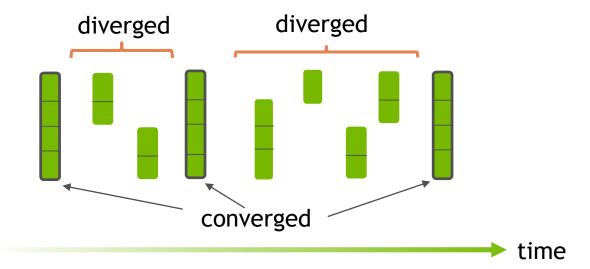


HOW TO WRITE WARP SYNCHRONOUS PROGRAMMING

Make Sync Explicit

Thread re-convergence

- Use built-in functions to converge threads explicitly
- Do not rely on implicit thread reconvergence.



HOW TO WRITE WARP SYNCHRONOUS PROGRAMMING

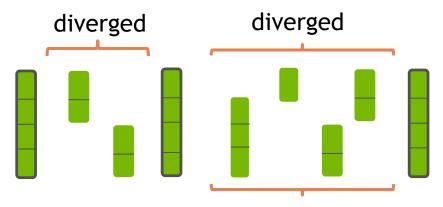
Make Sync Explicit

Thread re-convergence

- Use built-in functions to converge threads explicitly
- Do not rely on implicit thread reconvergence.

Data exchange between threads

- Use built-in functions to sync threads and exchange data in one step.
- When using shared memory, avoid data races between convergence points.



Reading and writing the same memory location by different threads may cause data races.

WARP SYNCHRONOUS BUILT-IN FUNCTIONS

Three Categories (New in CUDA 9.0)

Active-mask query: which threads in a warp are active

__activemask

Synchronized data exchange: exchange data between threads in warp

- __all_sync, __any_sync, __uni_sync, __ballot_sync
- __shfl_sync, __shfl_up_sync, __shfl_down_sync, __shfl_xor_sync
- __match_any_sync, __match_all_sync

Threads synchronization: synchronize threads in a warp and provide a memory fence

__syncwarp

EXAMPLE: ALIGNED MEMORY COPY

__activemask __all_sync

```
// pick the optimal memory copy based on the alignment
device void memorycopy(char *tptr, char *sptr, size t size) {
  unsigned mask = activemask();
  if ( all sync(mask, is all aligned(tptr, sptr, 16))
      return memcpy_aligned_16(tptr, sptr, size);
  if ( all sync(mask, is all aligned(tptr, sptr, 8))
      return memcpy aligned 8(tptr, sptr, size);
```

EXAMPLE: ALIGNED MEMORY COPY

__activemask __all_sync

```
// pick the optimal memory copy based on the alignment
                                                                Find the active threads
device void memorycopy(char *tptr, char *sptr, size t size) {
   unsigned mask = activemask();
  if ( all sync(mask, is all aligned(tptr, sptr, 16))
       return memcpy_aligned_16(tptr, sptr, size);
  if ( all sync(mask, is all aligned(tptr, sptr, 8))
       return memcpy_aligned_8(tptr, sptr, size);
```

EXAMPLE: ALIGNED MEMORY COPY

__activemask __all_sync

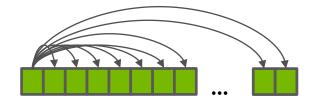
```
// pick the optimal memory copy based on the alignment
                                                                  Find the active threads
 device void memorycopy(char *tptr, char *sptr, size t size) {
   unsigned mask = activemask();
                                                           Returns true when all threads in 'mask'
                                                              have the same predicate value
   if (__all_sync(mask, is_all_aligned(tptr, sptr, 16))
       return memcpy aligned 16(tptr, sptr, size);
   if ( all sync(mask, is all aligned(tptr, sptr, 8))
       return memcpy_aligned_8(tptr, sptr, size);
```

EXAMPLE: SHUFFLE

__shfl_sync, __shfl_down_sync

Broadcast: all threads get the value of 'x' from lane id 0

$$y = _-shfl_sync(0xffffffff, x, 0);$$

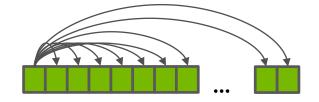


EXAMPLE: SHUFFLE

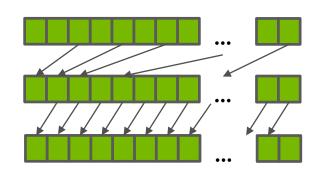
__shfl_sync, __shfl_down_sync

Broadcast: all threads get the value of 'x' from lane id 0

$$y = __shfl_sync(0xffffffff, x, 0);$$



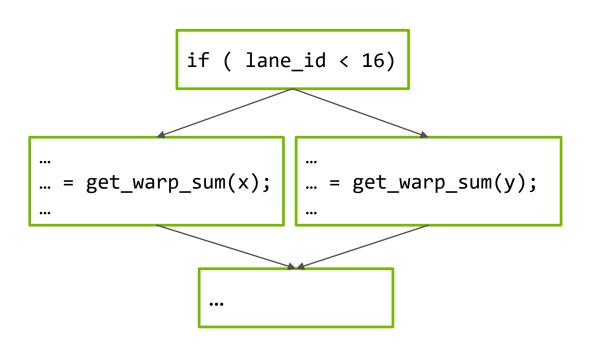
Reduction:





EXAMPLE: DIVERGENT BRANCHES

All *_sync built-in functions can be used in divergent branches on Volta

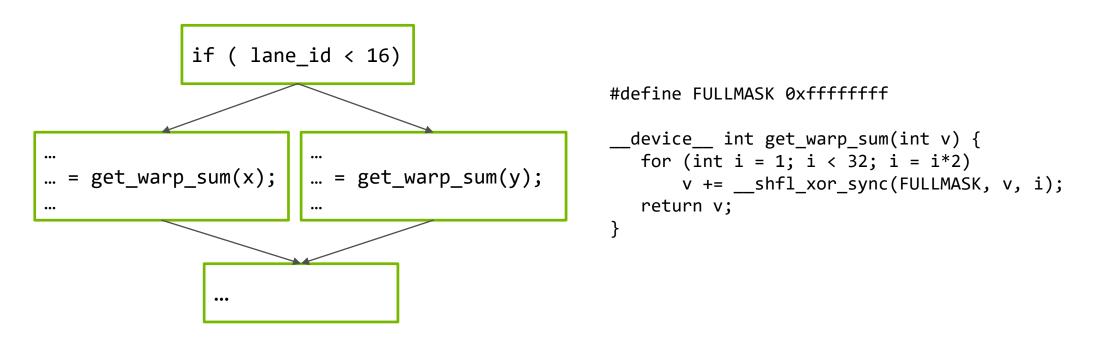


```
#define FULLMASK 0xffffffff

__device__ int get_warp_sum(int v) {
   for (int i = 1; i < 32; i = i*2)
      v += __shfl_xor_sync(FULLMASK, v, i);
   return v;
}</pre>
```

EXAMPLE: DIVERGENT BRANCHES

All *_sync built-in functions can be used in divergent branches on Volta



Possible to write a library function that performs warp synchronous programming w/o requiring it to be called convergently.

EXAMPLE: REDUCTION VIA SHARED MEMORY

__syncwarp

Re-converge threads and perform memory fence

BUT WHAT'S WRONG WITH THIS CODE?

```
v += shmem[tid+16];
shmem[tid] = v;
v += shmem[tid+8];
shmem[tid] = v;
v += shmem[tid+4];
shmem[tid] = v;
v += shmem[tid+2];
shmem[tid] = v;
v += shmem[tid+1];
shmem[tid] = v;
```

IMPLICIT WARP SYNCHRONOUS PROGRAMMING

Unsafe and Unsupported

Implicit warp synchronous programming builds upon two unreliable assumptions,

- implicit thread re-convergence points, and
- Implicit lock-step execution of threads in a warp.

Implicit warp synchronous programming is unsafe and unsupported.

Make warp synchronous programming safe by making synchronizations explicit.

IMPLICIT THREAD RE-CONVERGENCE

Unreliable Assumption 1

Example 1:

```
if (lane_id < 16)
   A;
else
   B;
assert(__activemask() == 0xffffffff);</pre>
```

IMPLICIT THREAD RE-CONVERGENCE

Unreliable Assumption 1

Example 1:

```
if (lane_id < 16)
    A;
else
    B;
assert(__activemask() == 0xfffffffff); not guaranteed to be true</pre>
```

Solution

- Do not reply on implicit thread re-convergence
- Use warp synchronous built-in functions to ensure convergence



Unreliable Assumption 2

Example 2

```
if (__activemask() == 0xffffffff) {
   assert(__activemask() == 0xffffffff);
}
```

Unreliable Assumption 2

Example 2

```
if (__activemask() == 0xffffffff) {
   assert(__activemask() == 0xffffffff); not guaranteed to be true
}
```

Solution

- Do not reply on implicit lock-step execution
- Use warp synchronous built-in functions to ensure convergence

Unreliable Assumption 2

```
shmem[tid] += shmem[tid+16];
shmem[tid] += shmem[tid+8];
shmem[tid] += shmem[tid+4];
shmem[tid] += shmem[tid+2];
shmem[tid] += shmem[tid+1];
```

Unreliable Assumption 2

```
Example 3
            shmem[tid] += shmem[tid+16];
            shmem[tid] += shmem[tid+8];
            shmem[tid] += shmem[tid+4];
                                          data race
            shmem[tid] += shmem[tid+2];
            shmem[tid] += shmem[tid+1];
Solution
                      v += shmem[tid+16]; syncwarp();
                      shmem[tid] = v; __syncwarp();
  Make sync explicit
                      v += shmem[tid+8]; syncwarp();
                      shmem[tid] = v; __syncwarp();
                      v += shmem[tid+4]; __syncwarp();
                      shmem[tid] = v; __syncwarp();
                      v += shmem[tid+2]; syncwarp();
                      shmem[tid] = v; __syncwarp();
                      v += shmem[tid+1]; __syncwarp();
                      shmem[tid] = v;
```

LEGACY WARP-LEVEL BUILT-IN FUNCTIONS

Deprecated in CUDA 9.0

Legacy built-in functions

__all(), __any(), __ballot(), __shfl(), __shfl_up(), __shfl_down(), __shfl_xor()

These legacy warp-level built-in functions can perform data exchange between the active threads in a warp.

They do not ensure which threads are active.

They are deprecated in CUDA 9.0 on all architectures.

COOPERATIVE GROUPS VS BUILT-IN FUNCTIONS

Example: warp aggregated atomic

```
// increment the value at ptr by 1 and return the old value
             device int atomicAggInc(int *p);
                                          int mask = activemask();
coalesced_group g = coalesced_threads();
                                          int rank = __popc(mask & __lanemask_lt());
                                          int leader_lane = __ffs(mask) - 1;
                                          int res;
int res;
                                          if (rank == 0)
if (g.thread_rank() == 0)
                                             res = atomicAdd(p, __popc(mask));
  res = atomicAdd(p, g.size());
                                          res = __shfl_sync(mask, res, leader_lane);
res = g.shfl(res, 0);
                                          return rank + res;
return g.thread rank() + res;
```

WARP SYNCHRONOUS PROGRAMMING IN CUDA 9.0

New warp synchronous built-in functions ensure reliable synchronizations.

New warp synchronous built-in functions can be used divergently on Volta.

Legacy warp built-in functions are deprecated.

Cooperative groups offers

- Higher-level abstraction of thread groups
- Four levels of thread grouping
- More scalable code and better software decomposition

BETTER COMPOSITION

Barrier synchronization hidden within functions

```
device int sum(int *x, int n)
                                                 All threads in thread block
    _syncthreads();
                                                 must arrive at this barrier.
  return total;
_global___ void parallel_kernel(float *x)
                                                 Hidden constraint on
                                                 caller due to
  // Entire thread block must call sum
                                                 implementation of sum.
  sum(x, n);
```

BETTER COMPOSITION

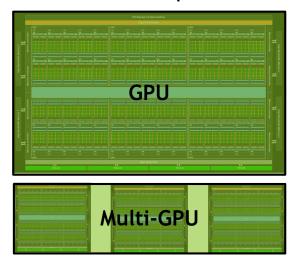
Explicit cooperative interfaces

```
_device__ int sum(<mark>thread_group</mark> g, int *x, int n)
                                                     Participating thread group
    g.sync()
                                                     provided by caller.
    return total;
__global__ void parallel_kernel(...)
    // Entire thread block must call sum
                                                     The need to synchronize
    sum(this_thread_block(), x, n);
                                                     in sum is visible in code.
```

FUTURE ROADMAP

Partition by label or predicate, more complex scopes

At all scopes!





FUTURE ROADMAP

Library of collectives (sort, reduce, etc.)

On a simpler note:

```
// Collective key-value sort, default allocator
cooperative_groups::sort(this_thread_block(), myValues, myKeys);
```

HONORABLE MENTION

The ones that didn't make it into their own slide

_CG_DEBUG: Define to enable various runtime safety checks. This helps debug incorrect API usage, incorrect synchronization, or similar issues (Automatically turned on with -G).

Tools help detect incorrect warp-synchronization with the racecheck tool.

Match is a new Volta instruction that is able to return who in your warp has the same 32 or 64 bit value

Developers now have a flexible model for synchronization and communication between groups of threads.

Shipping in CUDA 9.0

Provides safety, composability, and high performance

Flexibility to synchronize at various architecture and program defined scopes.

Deploy everywhere from Kepler to Volta