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Hardware Reference
Library*

**Technical
Reference**

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*Personal Computer
Hardware Reference
Library*

**Technical
Reference**

Revised Edition (March 1986)

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CAUTION

This product described herein is equipped with a grounded plug for the user's safety. It is to be used in conjunction with a properly grounded receptacle to avoid electrical shock.

Notes:

Preface

This publication describes the various components of the IBM Personal Computer XT and IBM Portable Personal Computer; and the interaction of each.

The information in this publication is for reference, and is intended for hardware and program designers, programmers, engineers, and anyone else with a knowledge of electronics and/or programming who needs to understand the design and operation of the IBM Personal Computer XT or IBM Portable Personal Computer.

This publication consists of two parts: a system manual and an options and adapters manual.

The system manual is divided into the following sections:

Section 1, "System Board", discusses the component layout, circuitry, and function of the system board.

Section 2, "Coprocessor", describes the Intel 8087 coprocessor and provides programming and hardware interface information.

Section 3, "Power Supply", provides electrical input/output specifications as well as theory of operation for both the IBM Personal Computer XT power supply and the IBM Portable Personal Computer power supply.

Section 4, "Keyboard", discusses the hardware makeup, function, and layouts of the IBM Personal Computer XT 83-key and 101/102-key keyboards and the IBM Portable Personal Computer keyboard. In addition, keyboard encoding and usage is discussed.

Section 5, "System Bios", describes the basic input/output system and its use. This section also contains the software

interrupt listing, a BIOS memory map, descriptions of vectors with special meanings, and a set of low memory maps.

Section 6, "Instruction Set", provides a quick reference for the 8088 and 8087 assembly instruction set.

Section 7, "Characters, Keystrokes, and Colors", supplies the decimal and hexadecimal values for characters and text attributes.

A glossary, bibliography, and index are also provided.

The *Technical Reference Options and Adapters* manual provides information, logic diagrams, and specifications pertaining to the options and adapters available for the IBM Personal Computer family of products. The manual is modular in format, with each module providing information about a specific option or adapter. Modules having a large amount of text contain individual indexes. The modules are grouped by type of device into the following categories:

- Expansion Unit
- Displays
- Printers
- Storage Devices
- Memory Expansion
- Adapters
- Miscellaneous
- Cables and Connectors.

Full-length hard-tab pages with the above category descriptions, separate the groups of modules.

The term "*Technical Reference* manual" in the Options and Adapters manual, refers to the:

- IBM Personal Computer XT/IBM Portable Personal Computer *Technical Reference* manual
- IBM Personal Computer *Technical Reference* manual
- IBM Personal Computer AT *Technical Reference* manual.

The term "*Guide to Operations* manual" in the Options and Adapters manual, refers to the:

- IBM Personal Computer *Guide to Operations* manual
- IBM Personal Computer XT *Guide to Operations* manual
- IBM Portable Personal Computer *Guide to Operations* manual
- IBM Personal Computer AT *Guide to Operations* manual.

Prerequisite Publications

- IBM Personal Computer XT *Guide to Operations*
- IBM Portable Personal Computer *Guide to Operations*.

Suggested Reading

- *BASIC for the IBM Personal Computer*
- *Disk Operating System (DOS)*
- *Hardware Maintenance Service* manual
- *Hardware Maintenance Reference* manual
- *Macro Assembler for the IBM Personal Computer*.

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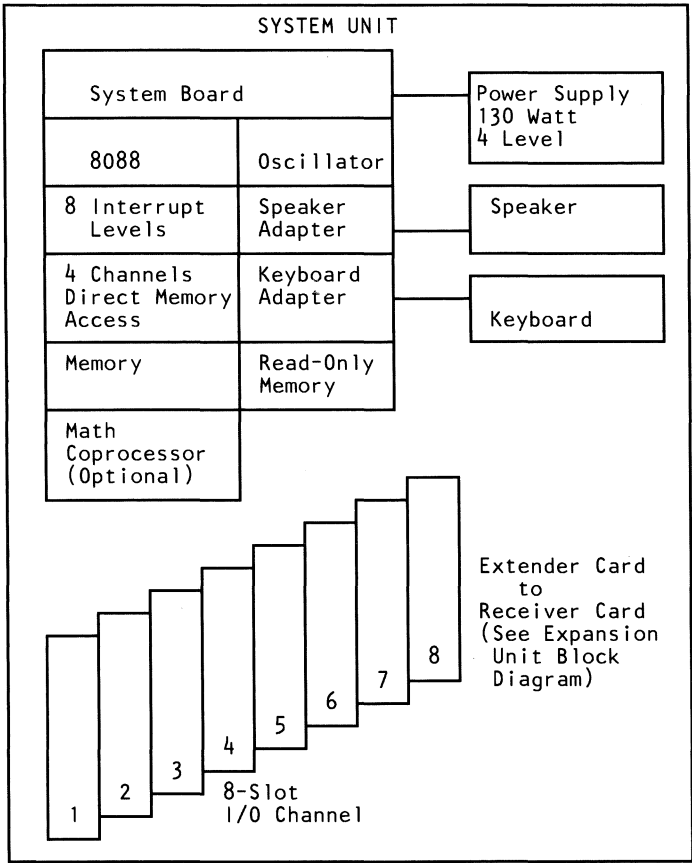
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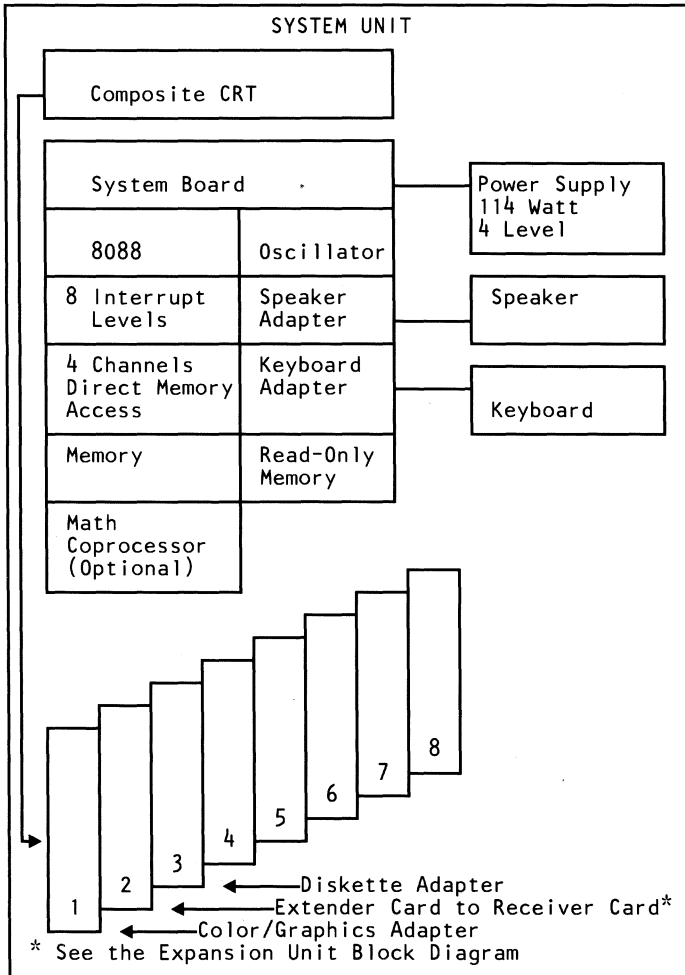
The following is a system block diagram of the IBM Personal Computer XT.



Note: A “System to Adapter Compatibility Chart,” to identify the adapters supported by each system, and an “Option to Adapter Compatibility Chart,” to identify the options supported by each adapter, can be found in the front matter of the *Technical Reference Options and Adapters* manual, Volume 1.

System Block Diagram (Portable)

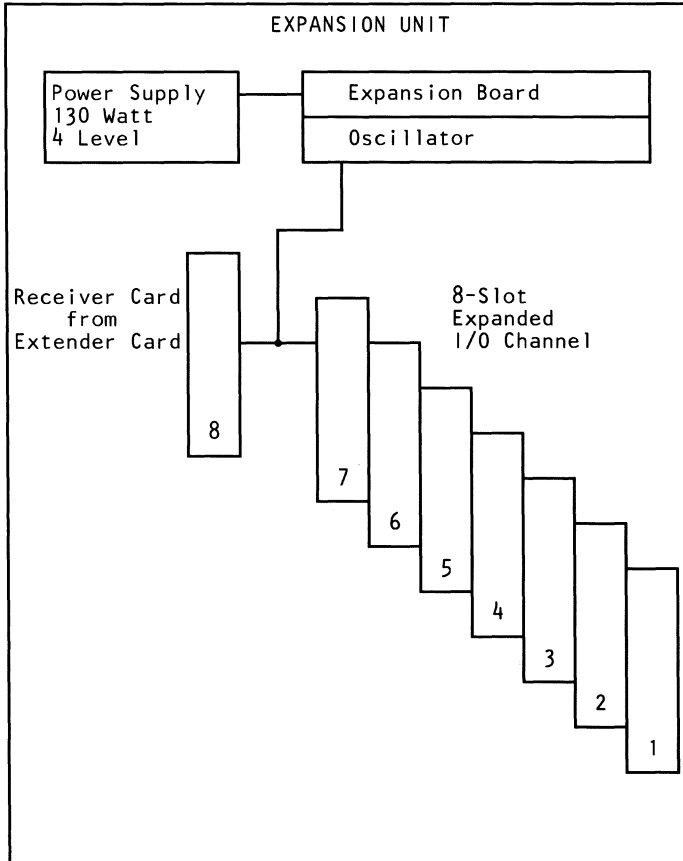
The following is a system block diagram of the IBM Portable Personal Computer.



Note: A "System to Adapter Compatibility Chart," to identify the adapters supported by each system, and an "Option to Adapter Compatibility Chart," to identify the options supported by each adapter, can be found in the front matter of the *Technical Reference Options and Adapters manual, Volume 1*.

Expansion Unit Block Diagram

The following is an expansion unit block diagram for the IBM Portable Personal Computer and IBM Personal Computer XT with the 64/256K system board.



Note: A "System to Adapter Compatibility Chart," to identify the adapters supported by each system, and an "Option to Adapter Compatibility Chart," to identify the options supported by each adapter, can be found in the front matter of the *Technical Reference Options and Adapters* manual, Volume 1.

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Description

The system board fits horizontally in the base of the system unit of the Personal Computer XT and Portable Personal Computer and is approximately 215 mm by 304 mm (8-1/2 x 12 in.). It is a multilayer, single-land-per-channel design with ground and internal planes provided. DC power and a signal from the power supply enter the board through two 6-pin connectors. Other connectors on the board are for attaching the keyboard and speaker. Eight 62-pin card-edge sockets are also mounted on the board. The I/O channel is bussed across these eight I/O slots. Slot J8 is slightly different from the others in that any card placed in it is expected to respond with a 'card selected' signal whenever the card is selected.

A dual in-line package (DIP) switch (one 8-switch pack) is mounted on the board and can be read under program control. The DIP switch provides the system programs with information about the installed options, how much storage the system board has, what type of display adapter is installed, whether or not the coprocessor is installed, what operational modes are desired when power is switched on (color or black-and-white, 80- or 40-character lines), and the number of diskette drives attached.

The system board contains the adapter circuits for attaching the serial interface from the keyboard. These circuits generate an interrupt to the microprocessor when a complete scan code is received. The interface can request execution of a diagnostic test in the keyboard.

The system board consists of five functional areas: the processor subsystem and its support elements, the ROM subsystem, the read/write (R/W) memory subsystem, integrated I/O adapters, and the I/O channel. All are described in this section.

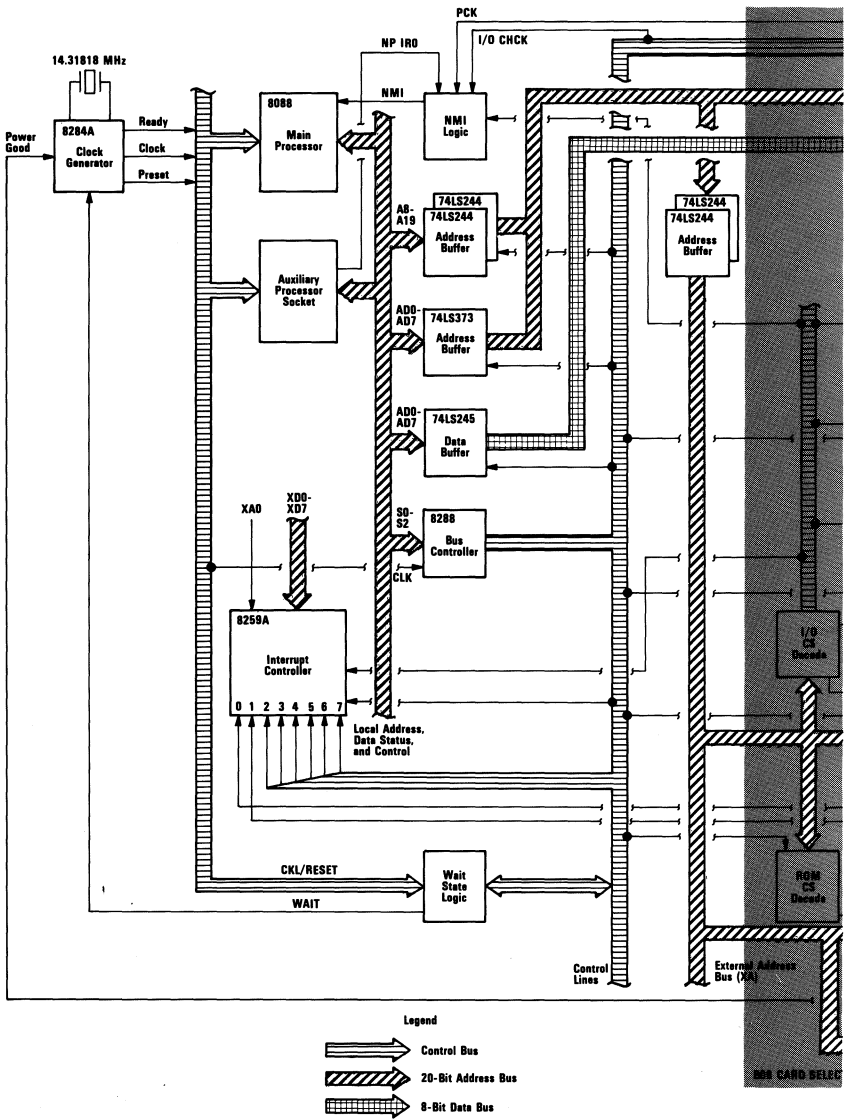
Microprocessor

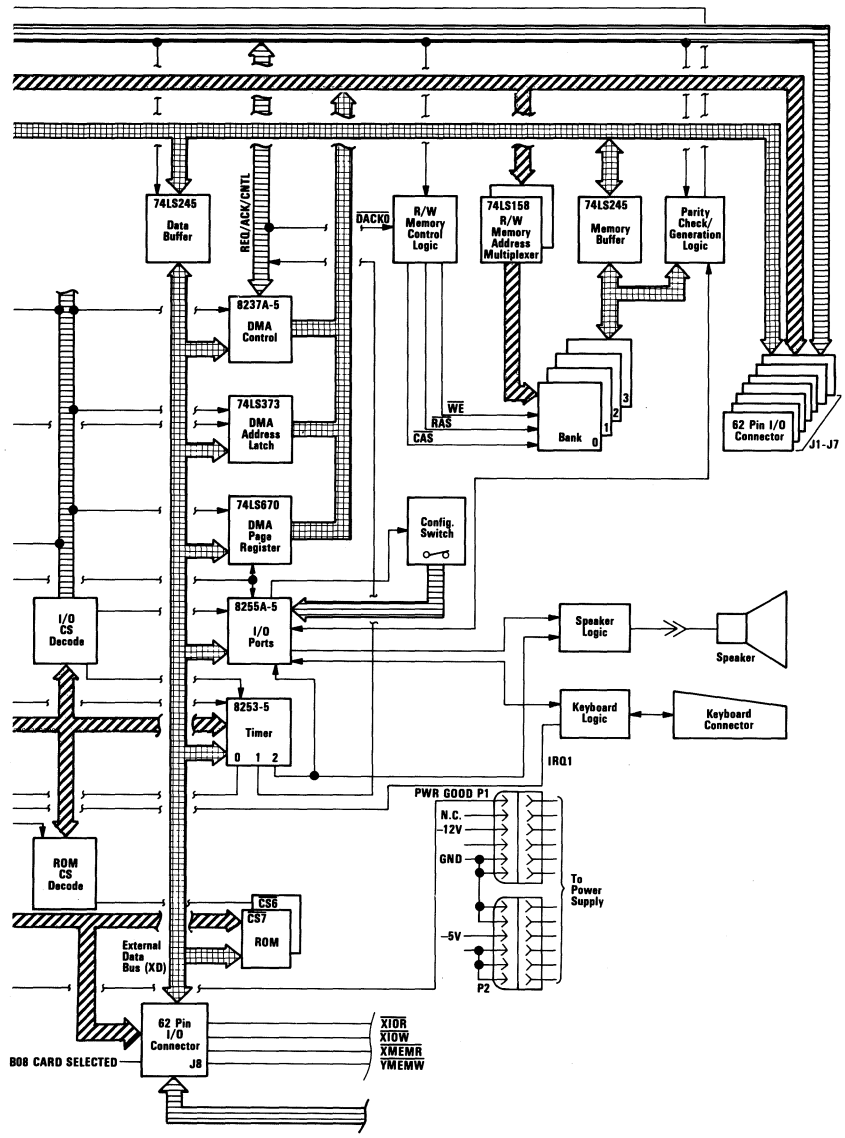
The heart of the system board is the Intel 8088 Microprocessor. This is an 8-bit external-bus version of Intel's 16-bit 8086 Microprocessor, and is software-compatible with the 8086. Thus, the 8088 supports 16-bit operations, including multiply and divide, and 20 bits of addressing (1M byte of storage). It also operates in maximum mode, so a coprocessor can be added as a feature. The microprocessor operates at 4.77MHz. This frequency is derived from a 14.31818MHz crystal, the frequency of which is divided by 3 for the microprocessor clock, and divided by 4 to obtain the 3.58MHz color-burst signal required for color televisions.

At the 4.77MHz clock rate, the 8088 bus cycles are four clocks of 210 nanoseconds (ns) each, or 840ns total. Some I/O cycles take five 210ns clocks or 1.05 microseconds (μ s).

Data Flow Diagrams

The system board data flow diagram starts on the next page.





System Memory Map

Start Address		Function			
Decimal	Hex	64/256K	256/640K		
0K	00000				
16K	04000				
32K	08000				
48K	0C000				
64K	10000				
80K	14000				
86K	18000				
112K	1C000				
128K	20000			128-256K Read/Write Memory on the System Board	256-640K Read/Write Memory on the System Board
144K	24000				
160K	28000				
176K	2C000				
192K	30000				
208K	34000				
224K	38000				
240K	3C000				
256K	40000	384K R/W Memory Expansion in the I/O Channel			
272K	44000				
288K	48000				
304K	4C000				
320K	50000				
336K	54000				
352K	58000				
368K	5C000				
384K	60000				
400K	64000				
416K	68000				
432K	6C000				
448K	70000				
464K	74000				
480K	78000				
496K	7C000				
512K	80000				
528K	84000				
544K	88000				
560K	8C000				
576K	90000				
592K	94000				
608K	98000				
624K	9C000				

System Memory Map (Part 1 of 2)

Start Address		Function
Decimal	Hex	64/256K & 256/640K
640K	A0000	128K Reserved
656K	A4000	
672K	A8000	
688K	AC000	
704K	B0000	Monochrome
736K	B8000	Color/Graphics
752K	BC000	
768K	C0000	Enhanced Graphics
784K	C6000	Professional Graphics
800K	C8000	Fixed Disk Control
816K	CC000	PC Network
832K	D0000	Cluster
848K	D4000	192K Read Only Memory Expansion and Control
864K	D8000	
880K	DC000	
896K	E0000	
912K	E4000	
928K	E8000	
944K	EC000	
960K	F0000	64K Base system BIOS and BASIC ROM
976K	F4000	
992K	F8000	
1008K	FC000	

System Memory Map (Part 2 of 2)

System Timers

Three programmable timer/counters are used by the system as follows: Channel 0 is used as a general-purpose timer providing a constant time base for implementing a time-of-day clock.

Channel 0 System Timer

GATE 0	Tied on
CLK IN 0	1.193182 MHz OSC
CLK OUT 0	8259A IRQ 0

Channel 1 is used to time and request refresh cycles from the DMA channel.

Channel 1 Refresh Request Generator

GATE 1	Tied on
CLK IN 1	1.193182 MHz OSC
CLK OUT 1	Request refresh cycle

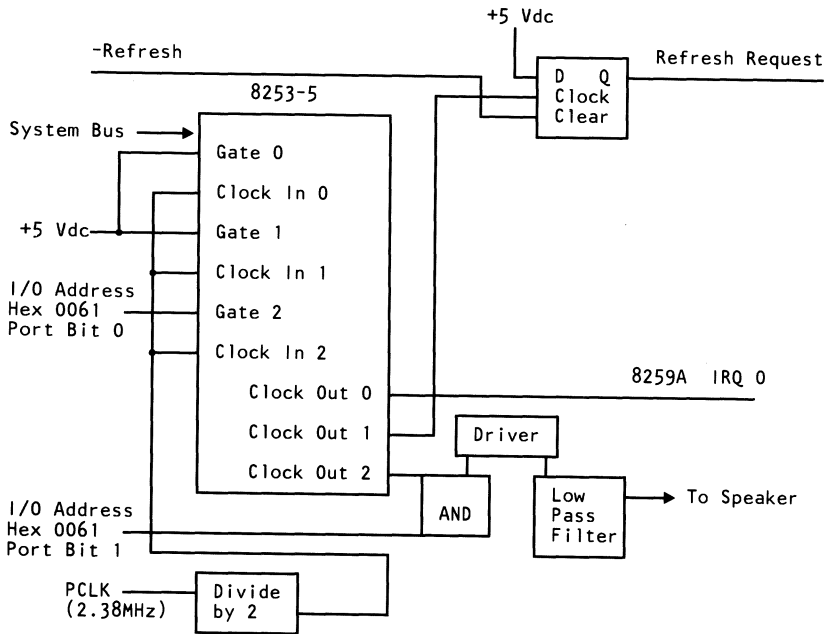
Note: Channel 1 is programmed as a rate generator to produce a 15-microsecond period signal.

Channel 2 is used to support the tone generation for the audio speaker. Each channel has a minimum timing resolution of 1.05 μ s.

Channel 2 Tone Generation for Speaker

GATE 2	Controlled by bit 0 of port hex 61, PPI bit
CLK IN 2	1.193182 MHz OSC
CLK OUT 2	Used to drive the speaker

The 8254-2 Timer/Counter is a programmable interval timer/counter that system programs treat as an arrangement of four external I/O ports. Three ports are treated as counters; the fourth is a control register for mode programming. The following is a system-timer block diagram.



System-Timer Block Diagram

System Interrupts

Of the eight prioritized levels of interrupt, six are bussed to the system expansion slots for use by feature cards. Two levels are used on the system board. Level 0, the higher priority, is attached to Channel 0 of the timer/counter and provides a periodic interrupt for the time-of-day clock. Level 1 is attached to the keyboard adapter circuits and receives an interrupt for each scan code sent by the keyboard.

The non-maskable interrupt (NMI) of the 8088 is used to report memory parity errors.

The following diagram contains the System Interrupt Listing.

Number	Usage
NMI	Parity 8087
0	Timer
1	Keyboard
2	EGA Display, PC Net, 3278/79
3	Asynchronous Communications (Alternate) PC Net(Alternate) 3278/79(Alternate) SDLC Communications BSC Communications Cluster (Primary)
4	Asynchronous Communications (Primary) SDLC Communications BSC Communications Voice Communications Adapter *
5	Fixed Disk
6	Diskette
7	Printer Cluster (Alternate)

* Jumper selectable to 2, 3, 4, 7.

8088 Hardware Interrupt Listing

System Boards

There are two types of system boards, 64/256K and 256/640K.

RAM

64/256K System Board

The 64/256K system board has either 128K or 256K of R/W memory. Memory greater than the system board's maximum of 256K is obtained by adding memory cards in the expansion slots. The memory consists of dynamic 64K by 1 bit chips with an access time of 200ns and a cycle time of 345ns. All R/W memory is parity-checked.

256/640K System Board

The 256/640K system board has either 256K, 512K or 640K of R/W memory. The memory consists of dynamic 64K by 1 bit chips in Banks 2 and 3 and dynamic 256K by 1 bit chips in Banks 0 and 1 with an access time of 200ns and a cycle time of 345ns. All R/W memory is parity-checked.

System Board	Minimum Storage	Maximum Storage	Memory Modules	Pluggable (Banks 0-1)	Pluggable (Banks 2-3)
64/256K	64K	256K	64K by 1 bit	2 Banks of 9	2 Banks of 9
256/640K	256K	640K	256K by 1 bit and 64K by 1 bit	2 Banks of 9	2 Banks of 9

ROM

The system board supports both read only memory (ROM) and R/W memory. It has space for 64K by 8 of ROM or erasable programmable read-only memory (EPROM). Two module sockets are provided, each of which can accept a 32K or 8K device. On the 64/256K system board, one socket has 32K by 8 bits of ROM, the other 8K by 8 bits. On the 256/640K system board, both sockets have 32K by 8 bits of ROM installed. This ROM contains the power-on self test, I/O drivers, dot patterns for 128 characters in graphics mode, and a diskette bootstrap loader. The ROM is packaged in 28-pin modules and has an access time and a cycle time of 250ns each.

DMA

The microprocessor is supported by a set of high-function support devices providing four channels of 20-bit direct-memory access (DMA), three 16-bit timer/counter channels, and eight prioritized interrupt levels.

Three of the four DMA channels are available on the I/O bus and support high-speed data transfers between I/O devices and memory without microprocessor intervention. The fourth DMA channel is programmed to refresh the system's dynamic memory. This is done by programming a channel of the timer/counter device to periodically request a dummy DMA transfer. This action creates a memory-read cycle, which is available to refresh dynamic memory both on the system board and in the system expansion slots. DMA data transfers take five clock cycles of 210ns, or 1.05 μ s. (See I/O CH RDY on page 1-22.) Refresh cycles occur once every 72 clocks (approximately 15 μ s) and require four clocks or approximately 5.6% of the bus bandwidth.

The following formula determines the percentage of bandwidth used for refresh.

64K X 1

$$\begin{array}{l} \% \text{ Bandwidth used} \\ \text{for Refresh} \end{array} = \frac{4 \text{ cycles} \times 128}{1.93\text{ms}/210\text{ns}} = \frac{512}{9190} = 5.6\%$$

256K X 1

$$\begin{array}{l} \% \text{ Bandwidth used} \\ \text{for Refresh} \end{array} = \frac{4 \text{ cycles} \times 256}{3.86\text{ms}/210\text{ns}} = \frac{1024}{19048} = 5.6\%$$

I/O Channel

The I/O channel is an extension of the 8088 microprocessor bus. It is, however, demultiplexed, repowered, and enhanced by the addition of interrupts and direct memory access (DMA) functions.

The I/O channel contains an 8-bit, bidirectional data bus, 20 address lines, 6 levels of interrupt, control lines for memory and I/O read or write, clock and timing lines, 3 channels of DMA control lines, memory refresh-timing control lines, a 'channel check' line, and power and ground for the adapters. Four voltage levels are provided for I/O cards: $+5 \text{ Vdc} \pm 5\%$, $-5 \text{ Vdc} \pm 10\%$, $+12 \text{ Vdc} \pm 5\%$, and $-12 \text{ Vdc} \pm 10\%$. These functions are provided in a 62-pin connector with 100-mil card tab spacing.

An 'I/O channel ready' line (I/O CH RDY) is available on the I/O channel to allow operation with slow I/O or memory devices. These devices can pull I/O CH RDY low to add wait states to the following operations:

- Normal memory read and write cycles take four 210ns clocks for a cycle time of 840ns/byte.
- Microprocessor-generated I/O read and write cycles require five clocks for a cycle time of $1.05 \mu\text{s}/\text{byte}$.
- DMA transfers require five clocks for a cycle time of $1.05 \mu\text{s}/\text{byte}$.

I/O devices are addressed using I/O mapped address space. The channel is designed so that 768 I/O device addresses are available to the I/O channel cards.

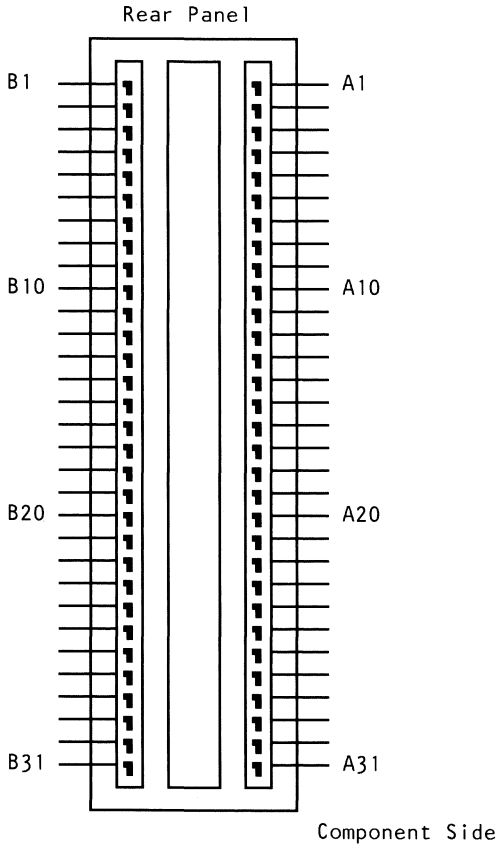
A 'channel check' line exists for reporting error conditions to the microprocessor. Activating this line results in a non-maskable interrupt (NMI) to the 8088 microprocessor. Memory expansion options use this line to report parity errors.

The I/O channel is repowered to provide sufficient drive to power all eight (J1 through J8) expansion slots, assuming two low-power

Schottky (LS) loads per slot. The IBM I/O adapters typically use only one load.

Timing requirements on slot J8 are much stricter than those on slots J1 through J7. Slot J8 also requires the card to provide a signal designating when the card is selected.

The following figure shows the pin numbering for I/O channel connectors J1 through J8.



I/O Channel Pin Numbering (J1-J8)

The following figures show signals and voltages for the I/O channel connectors.

I/O Pin	Signal Name	I/O
A1	-I/O CH CK	1
A2	SD7	I/O
A3	SD6	I/O
A4	SD5	I/O
A5	SD4	I/O
A6	SD3	I/O
A7	SD2	I/O
A8	SD1	I/O
A9	SD0	I/O
A10	I/O CH RDY	1
A11	AEN	0
A12	SA19	I/O
A13	SA18	I/O
A14	SA17	I/O
A15	SA16	I/O
A16	SA15	I/O
A17	SA14	I/O
A18	SA13	I/O
A19	SA12	I/O
A20	SA11	I/O
A21	SA10	I/O
A22	SA9	I/O
A23	SA8	I/O
A24	SA7	I/O
A25	SA6	I/O
A26	SA5	I/O
A27	SA4	I/O
A28	SA3	I/O
A29	SA2	I/O
A30	SA1	I/O
A31	SA0	I/O

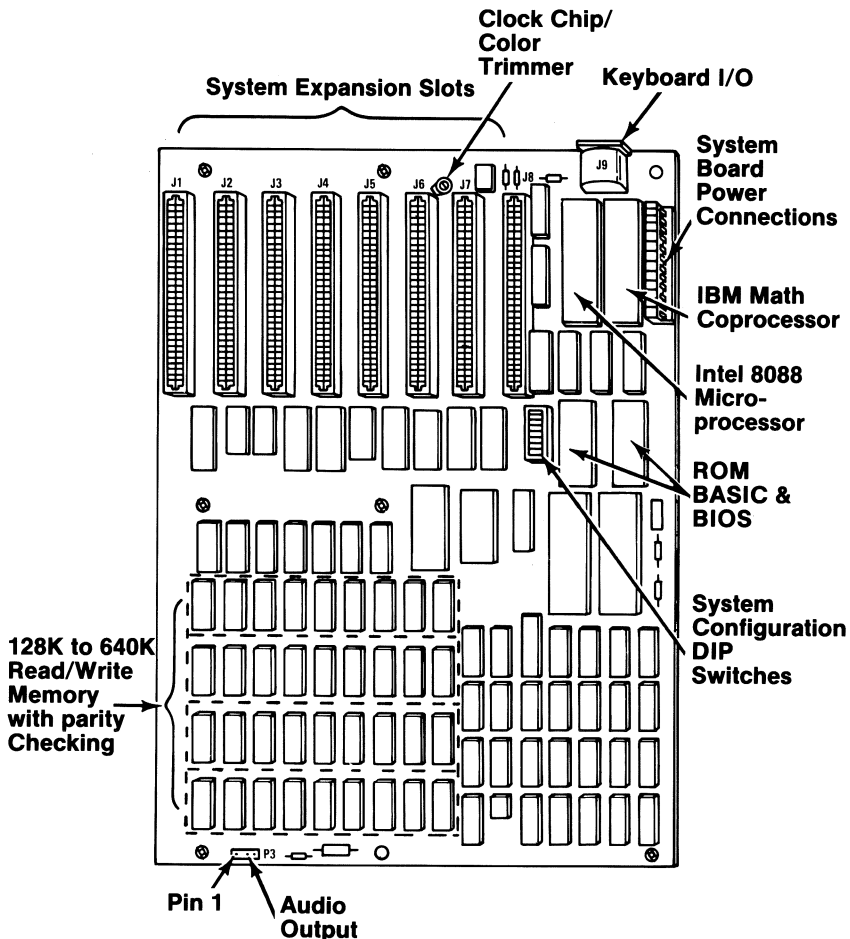
I/O Channel (A-Side, J1 through J8)

I/O Pin	Signal Name	I/O
B1	GND	Ground
B2	RESET DRV	0
B3	+5 Vdc	Power
B4	IRQ 2	I
B5	-5 Vdc	Power
B6	DRQ2	I
B7	-12 Vdc	Power
B8	-CARD SLCTD	I
B9	+12 Vdc	Power
B10	GND	Ground
B11	-MEMW	0
B12	-MEMR	0
B13	-IOW	I/O
B14	-IOR	I/O
B15	-DACK3	0
B16	DRQ3	I
B17	-DACK1	0
B18	DRQ1	I
B19	-DACK0	I/O
B20	CLK	0
B21	IRQ7	I
B22	IRQ6	I
B23	IRQ5	I
B24	IRQ4	I
B25	IRQ3	I
B26	-DACK2	0
B27	T/C	0
B28	ALE	0
B29	+5Vdc	Power
B30	OSC	0
B31	GND	Ground

I/O Channel (B-Side, J1 through J8)

System Board Diagram

The following diagram shows the component layout for the system board. All system board switch settings for total system memory, number of diskette drives, and types of display adapters are shown on page 1-27.



System Board Component Diagram

I/O Channel Description

The following is a description of the I/O Channel. All lines are TTL-compatible.

A0–A19 (O)

Address bits 0 to 19: These lines are used to address memory and I/O devices within the system. The 20 address lines allow access of up to 1M byte of memory. A0 is the least significant bit (LSB) and A19 is the most significant bit (MSB). These lines are generated by either the microprocessor or DMA controller. They are active high.

AEN (O)

Address Enable: This line is used to de-gate the microprocessor and other devices from the I/O channel to allow DMA transfers to take place. When this line is active (high), the DMA controller has control of the address bus, data bus, Read command lines (memory and I/O), and the Write command lines (memory and I/O).

ALE (O)

Address Latch Enable: This line is provided by the 8288 Bus Controller and is used on the system board to latch valid addresses from the microprocessor. It is available to the I/O channel as an indicator of a valid microprocessor address (when used with AEN). Microprocessor addresses are latched with the falling edge of ALE.

-CARD SLCTD (I)

-Card Selected: This line is activated by cards in expansion slot J8. It signals the system board that the card has been selected and that appropriate drivers on the system board should be directed to either read from, or write to, expansion slot J8. Connectors J1 through J8 are tied together at this pin, but the system board does not use their signal. This line should be driven by an open collector device.

CLK (O)

System clock: It is a divide-by-3 of the oscillator and has a period of 210ns (4.77MHz). The clock has a 33% duty cycle.

D0—D7 I/O

Data Bits 0 to 7: These lines provide data bus bits 0 to 7 for the microprocessor, memory, and I/O devices. D0 is the LSB and D7 is the MSB. These lines are active high.

-DACK0 to -DACK3 (O)

-DMA Acknowledge 0 to 3: These lines are used to acknowledge DMA requests (DRQ1—DRQ3) and refresh system dynamic memory (-DACK0). They are active low.

DRQ1—DRQ3 (I)

DMA Request 1 to 3: These lines are asynchronous channel requests used by peripheral devices to gain DMA service. They are prioritized with DRQ3 being the lowest and DRQ1 being the highest. A request is generated by bringing a DRQ line to an active level (high). A DRQ line must be held high until the corresponding DACK line goes active.

-I/O CH CK (I)

-I/O Channel Check: This line provides the microprocessor with parity (error) information on memory or devices in the I/O channel. When this signal is active low, a parity error is indicated.

I/O CH RDY (I)

I/O Channel Ready: This line, normally high (ready), is pulled low (not ready) by a memory or I/O device to lengthen I/O or memory cycles. It allows slower devices to attach to the I/O channel with a minimum of difficulty. Any slow device using this line should drive it low immediately upon detecting a valid address and a Read or Write command. This line should never be held low longer than 10 clock cycles. Machine cycles (I/O or memory) are extended by an integral number of clock cycles (210ns).

-IOR (O)

-I/O Read Command: This command line instructs an I/O device to drive its data onto the data bus. It may be driven by the microprocessor or the DMA controller. This signal is active low.

-IOW (O)

-I/O Write Command: This command line instructs an I/O device to read the data on the data bus. It may be driven by the microprocessor or the DMA controller. This signal is active low.

IRQ2—IRQ7 (I)

Interrupt Request 2 to 7: These lines are used to signal the microprocessor that an I/O device requires attention. They are prioritized with IRQ2 as the highest priority and IRQ7 as the lowest. An interrupt request is generated by raising an IRQ line (low to high) and holding it high until it is acknowledged by the microprocessor (interrupt service routine).

-MEMR (O)

-Memory Read: This command line instructs the memory to drive its data onto the data bus. It may be driven by the microprocessor or the DMA controller. This signal is active low.

-MEMW (O)

-Memory Write: This command line instructs the memory to store the data present on the data bus. It may be driven by the microprocessor or the DMA controller. This signal is active low.

OSC (O)

Oscillator: High-speed clock with a 70ns period (14.31818MHz). It has a 50% duty cycle.

RESET DRV (O)

Reset Drive: This line is used to reset or initialize system logic upon power-up or during a low line-voltage outage. This signal is synchronized to the falling edge of CLK and is active high.

T/C (O)

Terminal Count: This line provides a pulse when the terminal count for any DMA channel is reached. This signal is active high.

I/O Address Map

The following pages contain the planar and channel I/O Address Maps.

Hex Range*	Device
000-01F	DMA controller, 8237A-5
020-03F	Interrupt controller, 8259A
040-05F	Timer, 8253-5
060-06F	PPI 8255A-5
080-09F	DMA page registers
0AX**	NMI Mask Registers

Note: I/O Addresses, hex 000 to 0FF, are reserved for the system board I/O. Hex 100 to 3FF are available on the I/O channel.

* These are the addresses decoded by the current set of adapter cards. IBM may use any of the unlisted addresses for future use.

** At power-on-time, the Non Mask Interrupt into the 8088 is masked off. This mask bit can be set and reset through system software as follows:
Set mask: Write hex 80 to I/O Address
 hex A0(enable NMI)
Clear mask: Write hex 00 to I/O Address
 hex A0(disable NMI)

Planar I/O Address Map

Hex Range*	Device
200-20F	Game Control
201	Game I/O
20C-20D	Reserved
210-217	Expansion Unit
21F	Reserved
278-27F	Parallel printer port 2
280-2DF	Alternate Enhanced Graphics Adapter
2E1	GPiB (Adapter 0)
2E2 & 2E3	Data Acquisition (Adapter 0)
2F8-2FF	Serial port 2
300-31F	Prototype card
320-32F	Fixed Disk
348-357	DCA 3278
360-367	PC Network (low address)
368-36F	PC Network (high address)
378-37F	Parallel printer port 1
380-38F***	SDLC, bisynchronous 2
390-393	Cluster
3A0-3AF	Bisynchronous 1
3B0-3BF	Monochrome Display and Printer Adapter
3C0-3CF	Enhanced Graphics Adapter
3D0-3DF	Color/Graphics Monitor Adapter
3F0-3F7	Diskette controller
3F8-3FF	Serial port 1
6E2 & 6E3	Data Acquisition (Adapter 1)
790-793	Cluster (Adapter 1)
AE2 & AE3	Data Acquisition (Adapter 2)
B90-B93	Cluster (Adapter 2)
EE2 & EE3	Data Acquisition (Adapter 3)
1390-1393	Cluster (Adapter 3)
22E1	GPiB (Adapter 1)
2390-2393	Cluster (Adapter 4)
42E1	GPiB (Adapter 2)
62E1	GPiB (Adapter 3)
82E1	GPiB (Adapter 4)
A2E1	GPiB (Adapter 5)
C2E1	GPiB (Adapter 6)
E2E1	GPiB (Adapter 7)

Note: I/O Addresses, hex 000 to 0FF, are reserved for the system board I/O. Hex 100 to 3FF are available on the I/O channel.

* These are the addresses decoded by the current set of adapter cards. IBM may use any of the unlisted addresses for future use.

*** SDLC Communication and Secondary Binary Synchronous Communications cannot be used together because their hex addresses overlap.

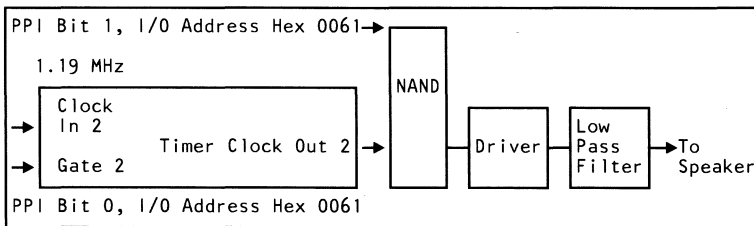
Channel I/O Address Map

Other Circuits

Speaker Circuit

The system unit has a 57.15 mm (2-1/4 in.) audio speaker. The speaker's control circuits and driver are on the system board. The speaker connects through a 2-wire interface that attaches to a 3-pin connector on the system board.

The speaker drive circuit is capable of approximately 1/2 watt of power. The control circuits allow the speaker to be driven three different ways: 1.) a direct program control register bit may be toggled to generate a pulse train; 2.) the output from Channel 2 of the timer/counter may be programmed to generate a waveform to the speaker; 3.) the clock input to the timer/counter can be modulated with a program-controlled I/O register bit. All three methods may be performed simultaneously.



Speaker Drive System Block Diagram

Channel 2 (Tone generation for speaker)
Gate 2 -- Controlled by 8255A-5 PPI Bit (See 8255 Map)
Clock In 2 -- 1.19318 MHz OSC
Clock Out 2 -- Used to drive speaker

Speaker Tone Generation

The speaker connection is a 4-pin Berg connector.

	Pin	Function
P3	1	Data out
	2	Key
	3	Ground
	4	+5 Vdc

Speaker Connector (P3)

8255A I/O Bit Map

The 8255A I/O Bit Map shows the inputs and outputs for the Command/Mode register on the system board. Also shown are the switch settings for the memory, display, and number of diskette drives. The following page contains the I/O bit map.

Hex Port Number	I N P U T	PA0	+ Keyboard Scan Code	0 1 2 3 4 5 6 7	Or	Diagnostic Outputs	0 1 2 3 4 5 6 7																
		0060																					
0061	O U T P U T	PB0	+ Timer 2 Gate Speaker + Speaker Data Spare Read High Switches or Read Low Switches - Enable RAM Parity Check - Enable I/O Channel Check - Hold Keyboard Clock Low - (Enable Keyboard or + (Clear Keyboard)																				
		0062																					
0063	I N P U T	PC0	Loop on POST + CoProcessor Installed + Planar RAM Size 0 + Planar RAM Size 1 Spare + Timer Channel 2 Out + I/O Channel Check + RAM Parity Check	Sw-1 Sw-2 * Sw-3 * Sw-4	Or	Display 0 Display 1 # Drives # Drives 1	**Sw-5 **Sw-6 ***Sw-7 ***Sw-8																
		0063																					
Command/Mode Register			Hex 99																				
Mode Register Value			<table border="1" style="margin-left: auto; margin-right: auto;"> <tr><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr> <tr><td>1</td><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td></tr> </table>					7	6	5	4	3	2	1	0	1	0	0	1	1	0	0	1
7	6	5	4	3	2	1	0																
1	0	0	1	1	0	0	1																

* Sw-4	Sw-3	Amount of Memory on System Board - 64/256K
0	0	64K
0	1	128K
1	0	192K
1	1	256K
* Sw-4	Sw-3	Amount of Memory on System Board - 256/640K
0	0	256K
0	1	512K
1	0	576K
1	1	640K
** Sw-6	Sw-5	Display at Power-Up Mode
0	0	Reserved
0	1	Color 40 X 25 (BW Mode)
1	0	Color 80 X 25 (BW Mode)
1	1	IBM Monochrome 80 X 25
*** Sw-8	Sw-7	Number of Diskette Drives in System
0	0	1
0	1	2
1	0	3
1	1	4

Notes:
PA Bit = 0 implies switch "ON". PA Bit = 1 implies switch "OFF".
A plus (+) indicates a bit value of 1 performs the specified function.
A minus (-) indicates a bit value of 0 performs the specified function.

8255A I/O Bit Map

Specifications

System Unit

Size

- Length: 498 millimeters (19.6 inches)
- Depth: 411 millimeters (16.2 inches)
- Height: 147 millimeters (5.8 inches)

Weight

- 14.2 kilograms (31.6 pounds)

Power Cable

- Length: 1.8 meters (6 feet)

Environment

- Air Temperature
 - System On: 15.6 to 32.2 degrees C (60 to 90 degrees F)
 - System Off: 10 to 43 degrees C (50 to 110 degrees F)
- Wet Bulb Temperature
 - System On: 22.8 degrees C (73 degrees F)
 - System Off: 26.7 degrees C (80 degrees F)

- Humidity
 - System On: 8% to 80%
 - System Off: 20% to 80%
- Altitude
 - Maximum altitude: 2133.6 meters (7000 feet)

Heat Output

- 1229 British Thermal Units (BTU) per hour

Noise Level

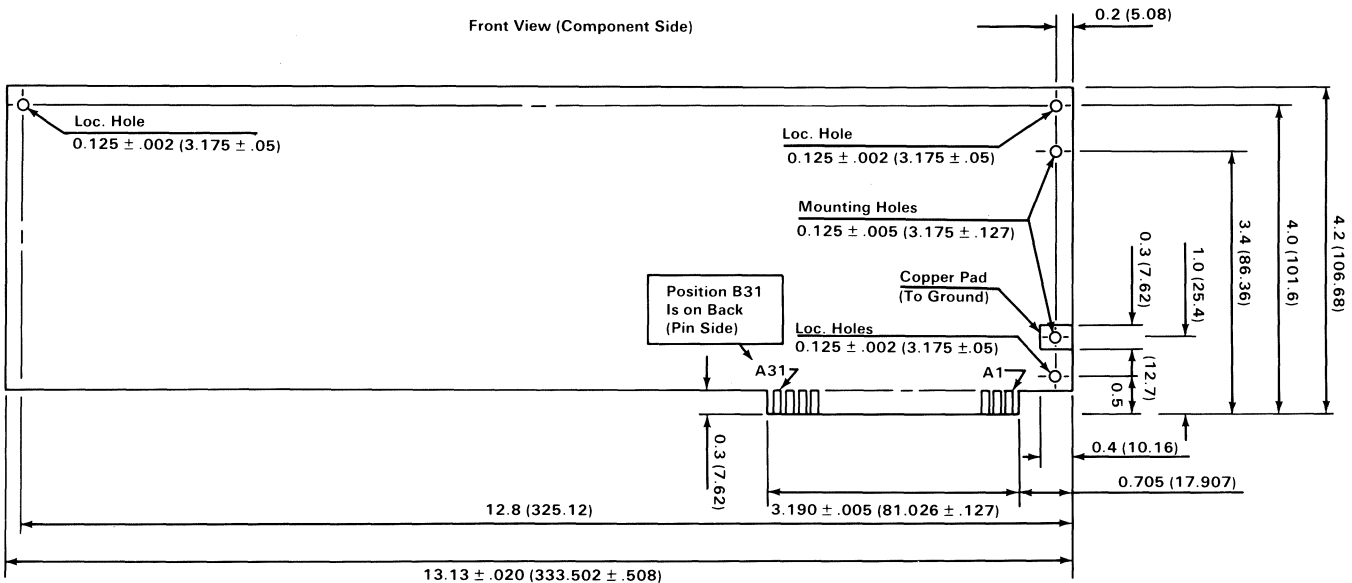
- 43 decibels average-noise rating (without printer)

Electrical

- Power: 450 VA
- Input
 - Nominal: 115 Vac
 - Minimum: 100 Vac
 - Maximum: 125 Vac

Card Specifications

The specifications for option cards follow.



Notes:

1. All Card Dimensions are $\pm .010$ (.254) Tolerance (With Exceptions Indicated on Drawing or in Notes).
2. Max. Card Length is 13.15 (334.01) Smaller Length is Permissible.
3. Loc. and Mounting Holes are Non-Plated Thru. (Loc. 3X, Mtg. 2X).
4. 31 Gold Tabs Each Side, $0.100 \pm .0005$ (2.54 \pm .0127) Center to Center, $0.06 \pm .0005$ (1.524 \pm .0127) Width.
5. Numbers in Parentheses are in Millimeters. All Others are in Inches.

Connectors

The system board has the following additional connectors:

- Two power-supply connectors (P1 and P2)
- Speaker connector (J19)
- Keyboard connector (J22)

The pin assignments for the power-supply connectors, P1 and P2, are as follows. The pins are numbered 1 through 6 from the rear of the system.

Connector	Pin	Assignments
P1	1	Power Good
	2	Key
	3	+12 Vdc
	4	-12 Vdc
	5	Ground
	6	Ground
P2	1	Ground
	2	Ground
	3	-5 Vdc
	4	+5 Vdc
	5	+5 Vdc
	6	+5 Vdc

Power Supply Connectors (P1, P2)

The speaker connector, J19, is a 4-pin, keyed, Berg strip. The pins are numbered 1 through 4 from the front of the system. The pin assignments are as follows:

Connector	Pin	Function
J19	1	Data out
	2	Key
	3	Ground
	4	+5 Vdc

Speaker Connector (J19)

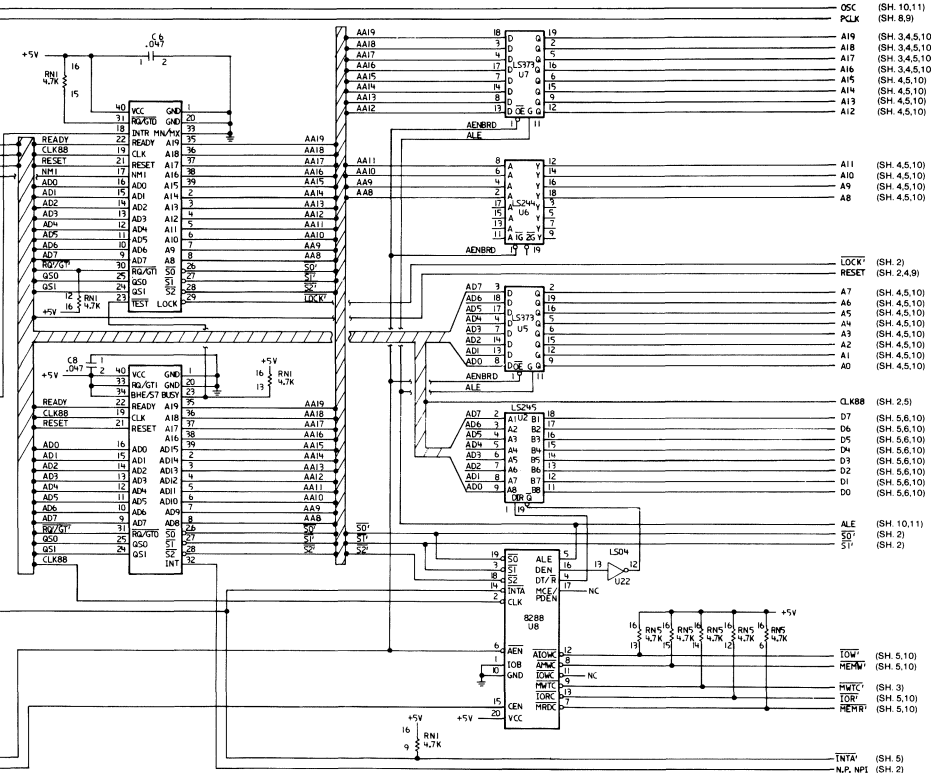
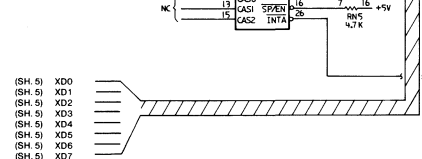
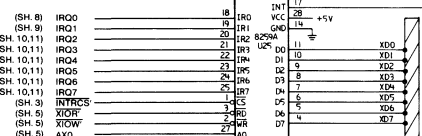
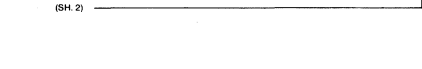
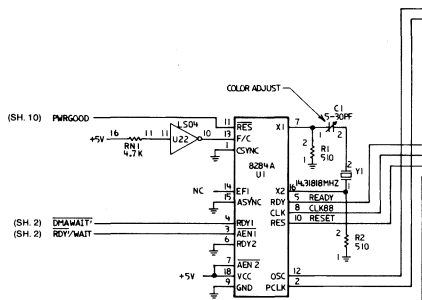
The keyboard connector, J22, is a 5-pin, 90-degree printed circuit board (PCB) mounting, DIN connector. For pin numbering, see the “Keyboard” section. The pin assignments are as follows:

Connector	Pin	Assignments
J22	1	Keyboard Clock
	2	Keyboard Data
	3	Reserved
	4	Ground
	5	+5 Vdc

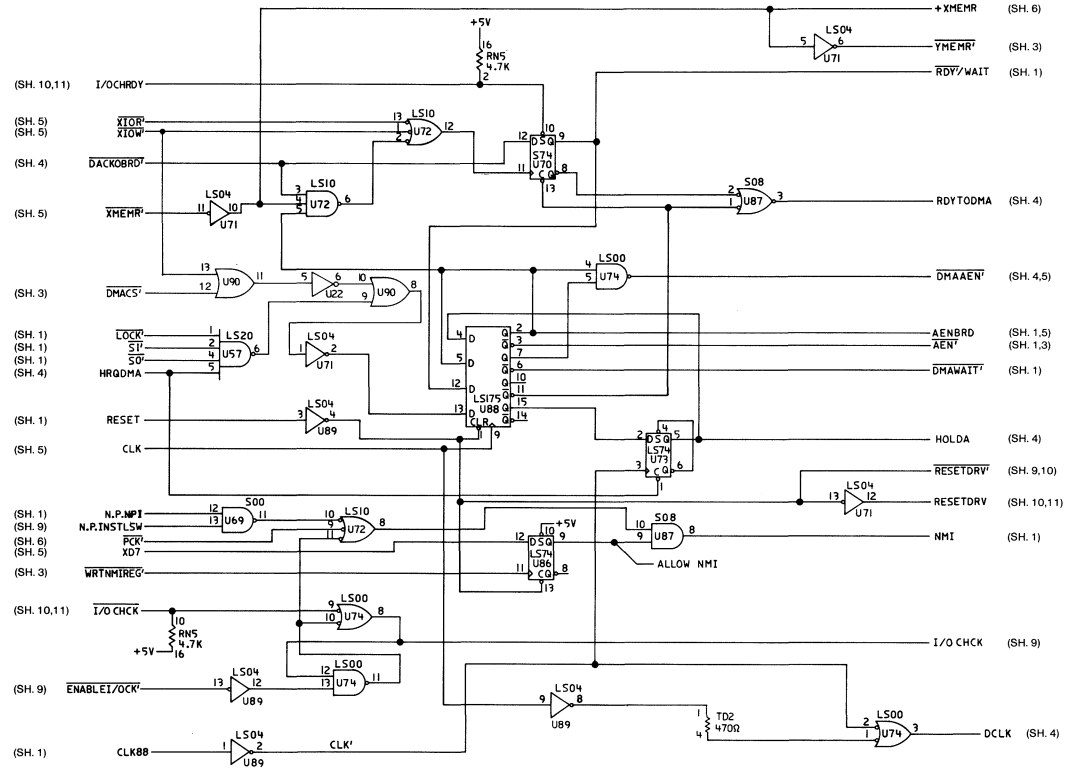
Keyboard Connector (J22)

Logic Diagrams - 64/256K

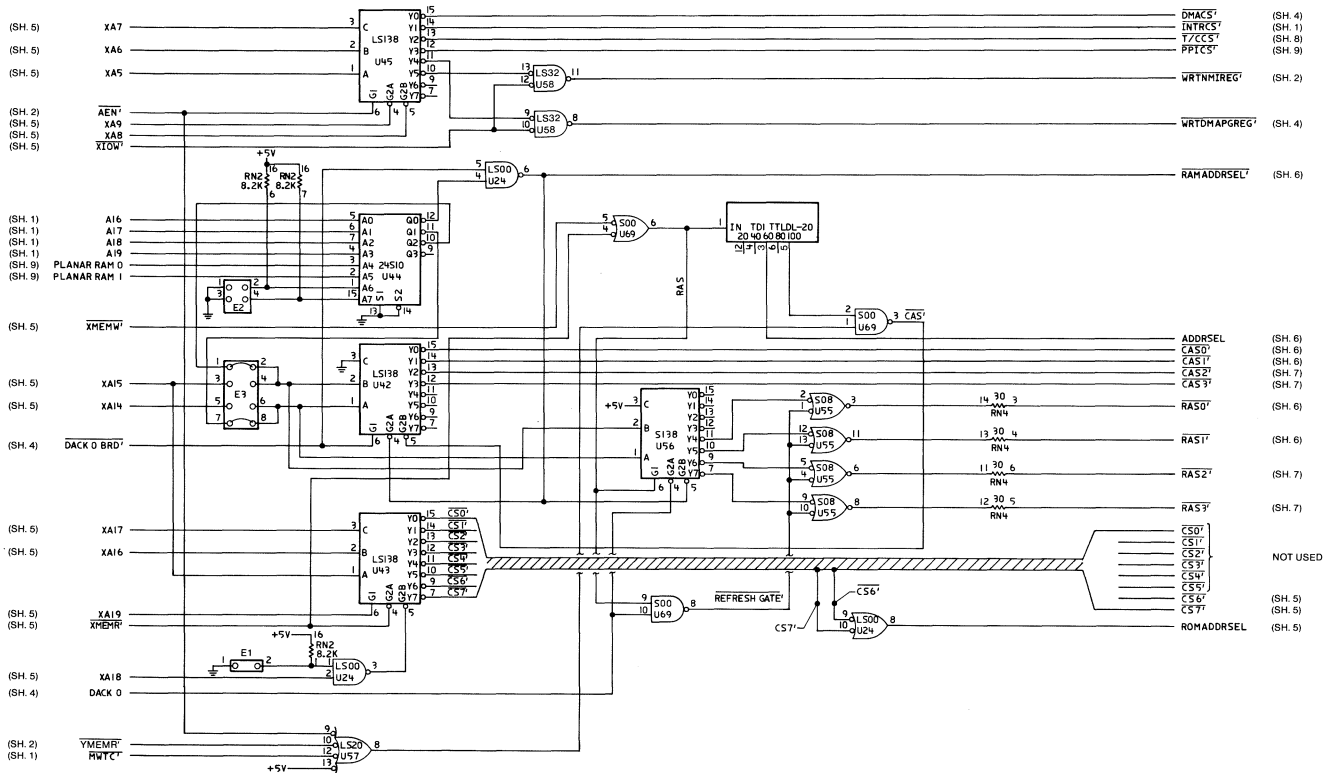
The following pages contain the logic diagrams for the 64/256K system board.



64/256K System Board (Sheet 1 of 11)

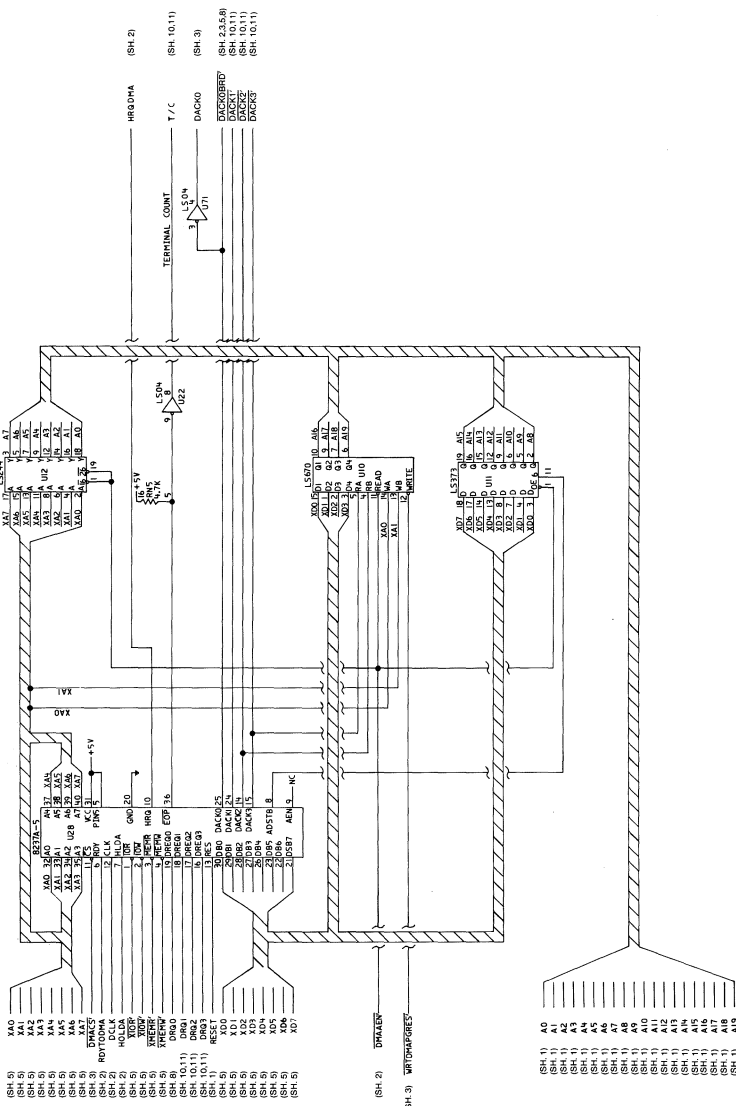


64/256K System Board (Sheet 2 of 11)

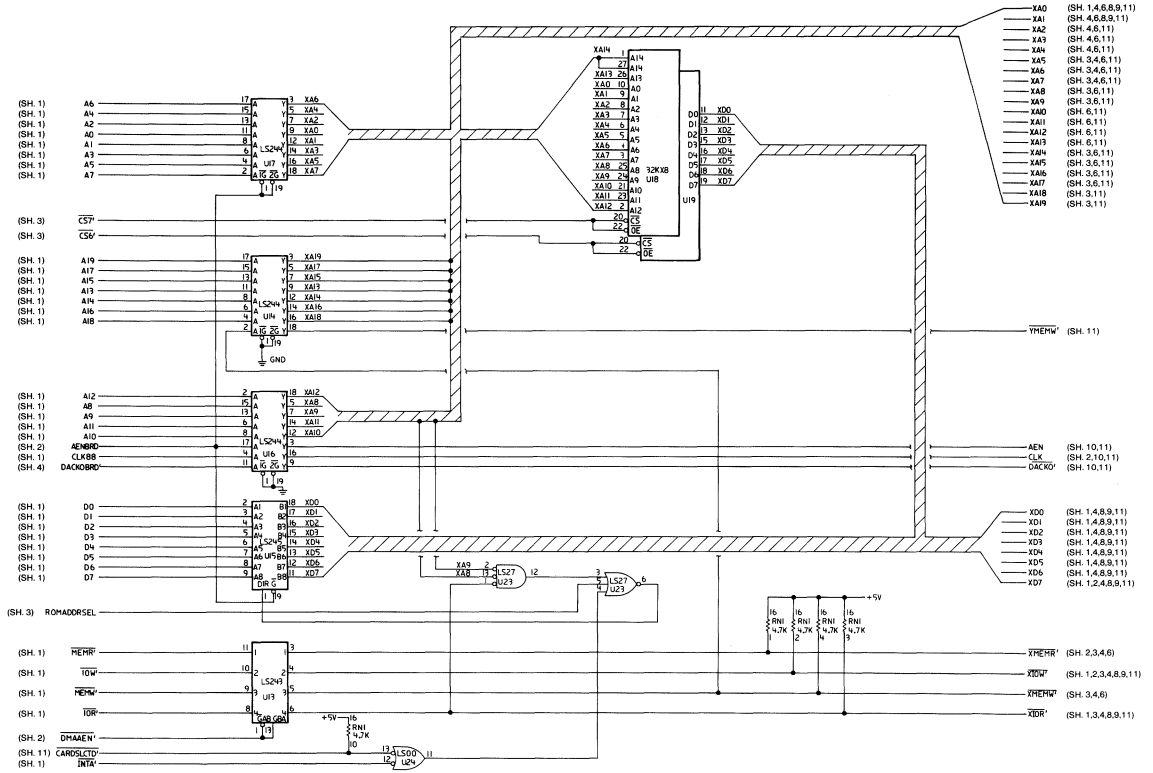


64/256K System Board (Sheet 3 of 11)

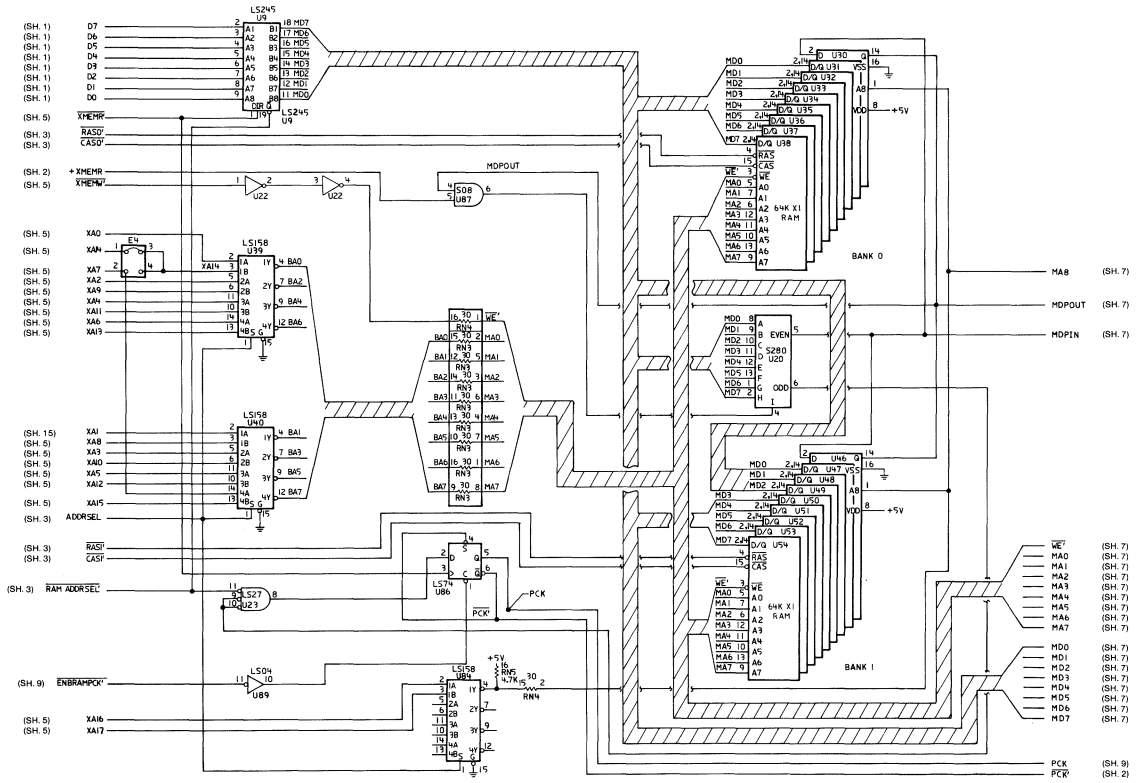




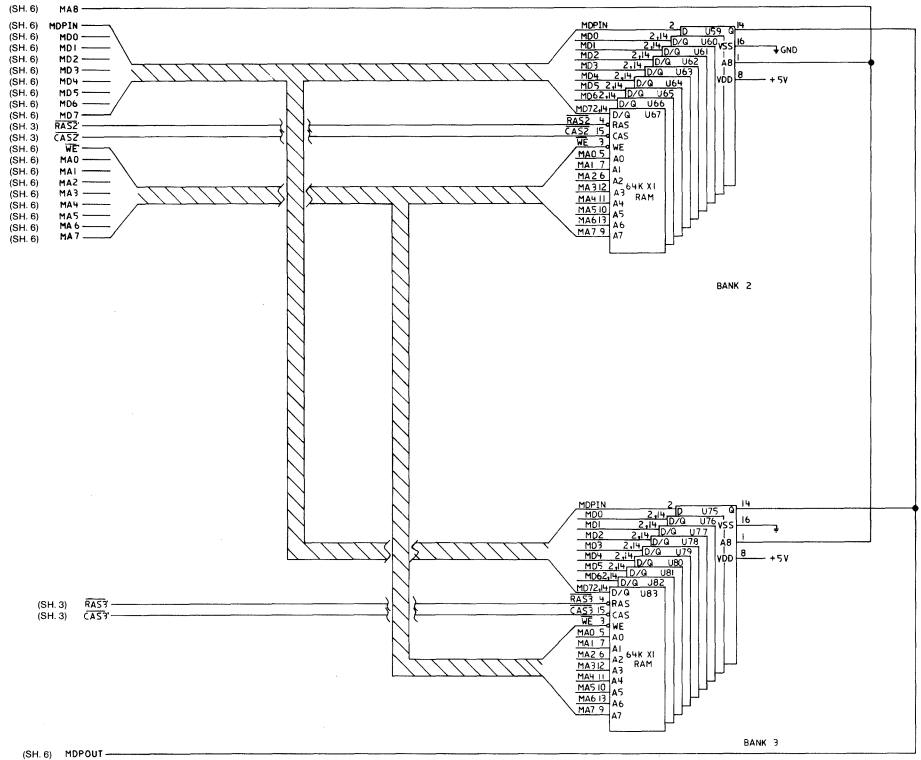
64/256K System Board (Sheet 4 of 11)



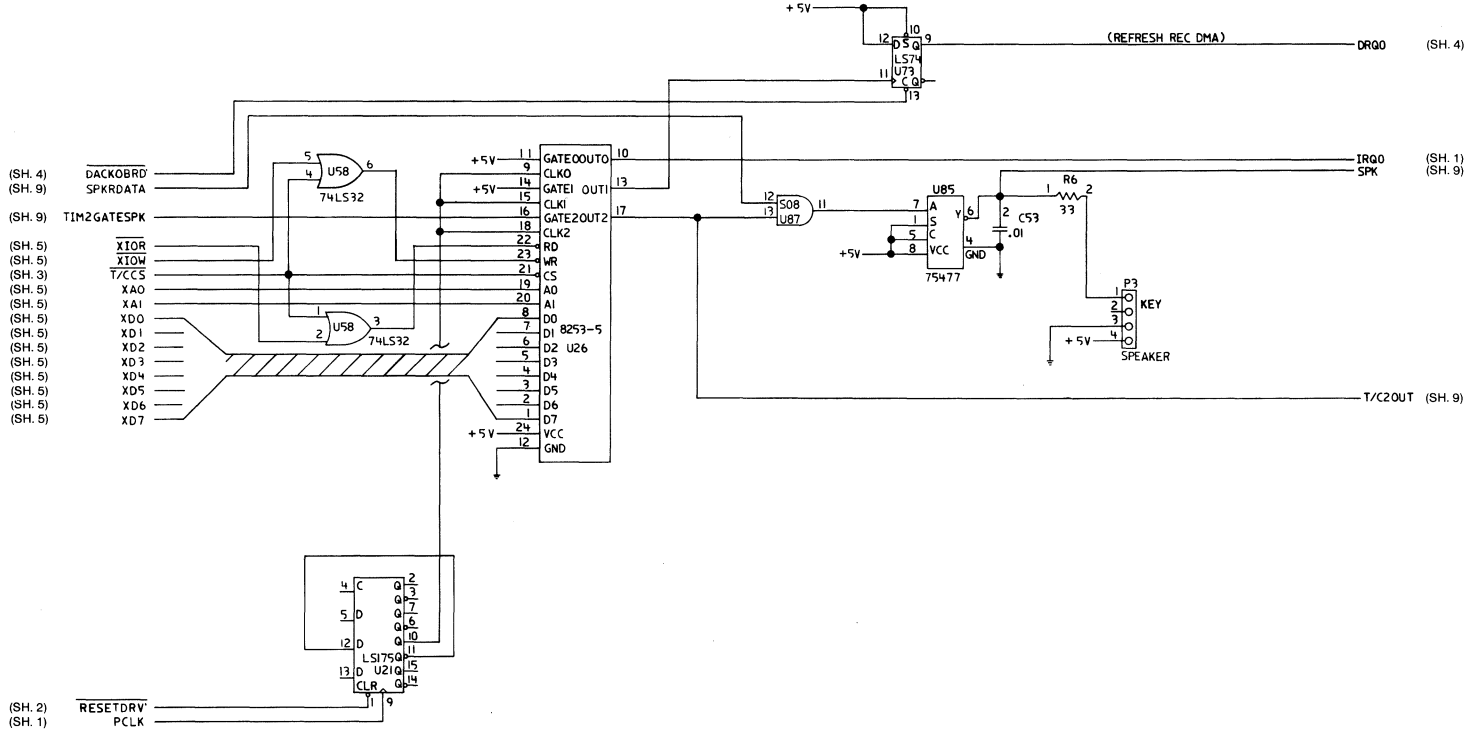
64/256K System Board (Sheet 5 of 11)



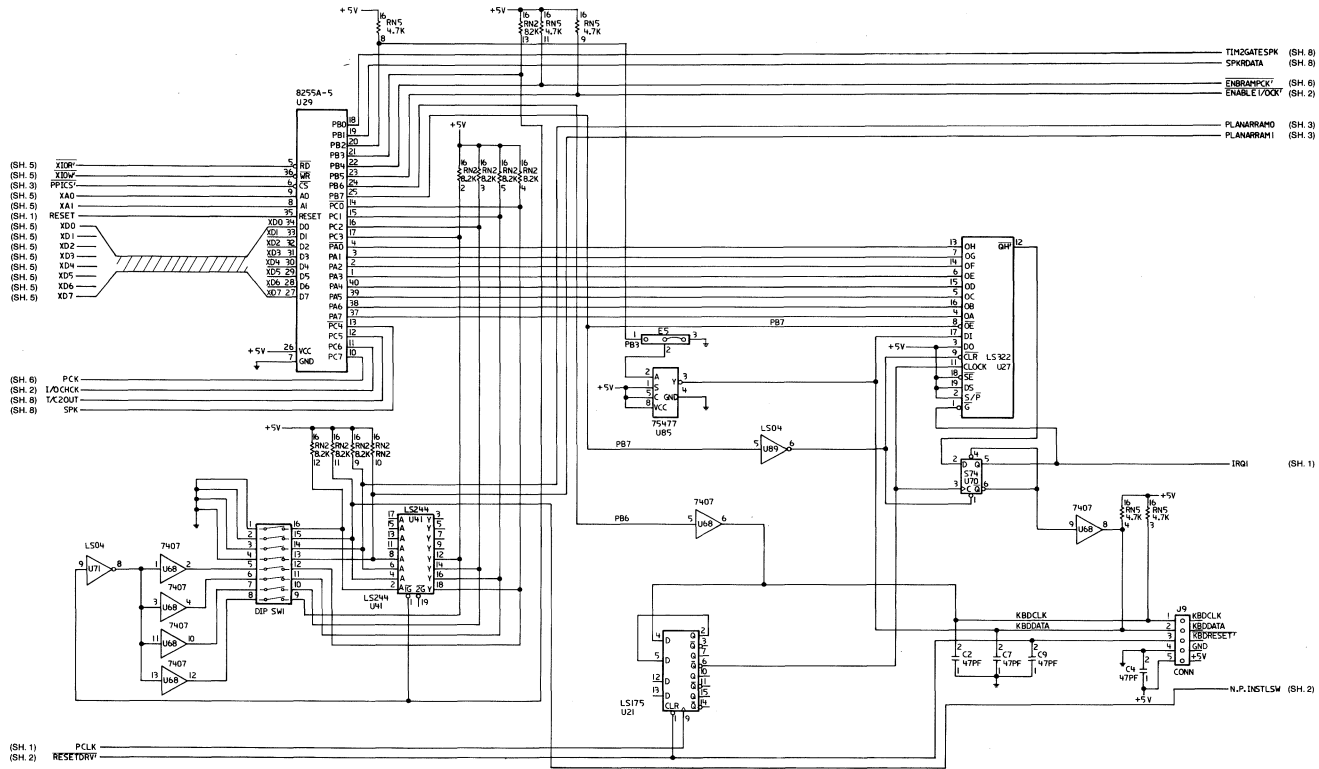
64/256K System Board (Sheet 6 of 11)



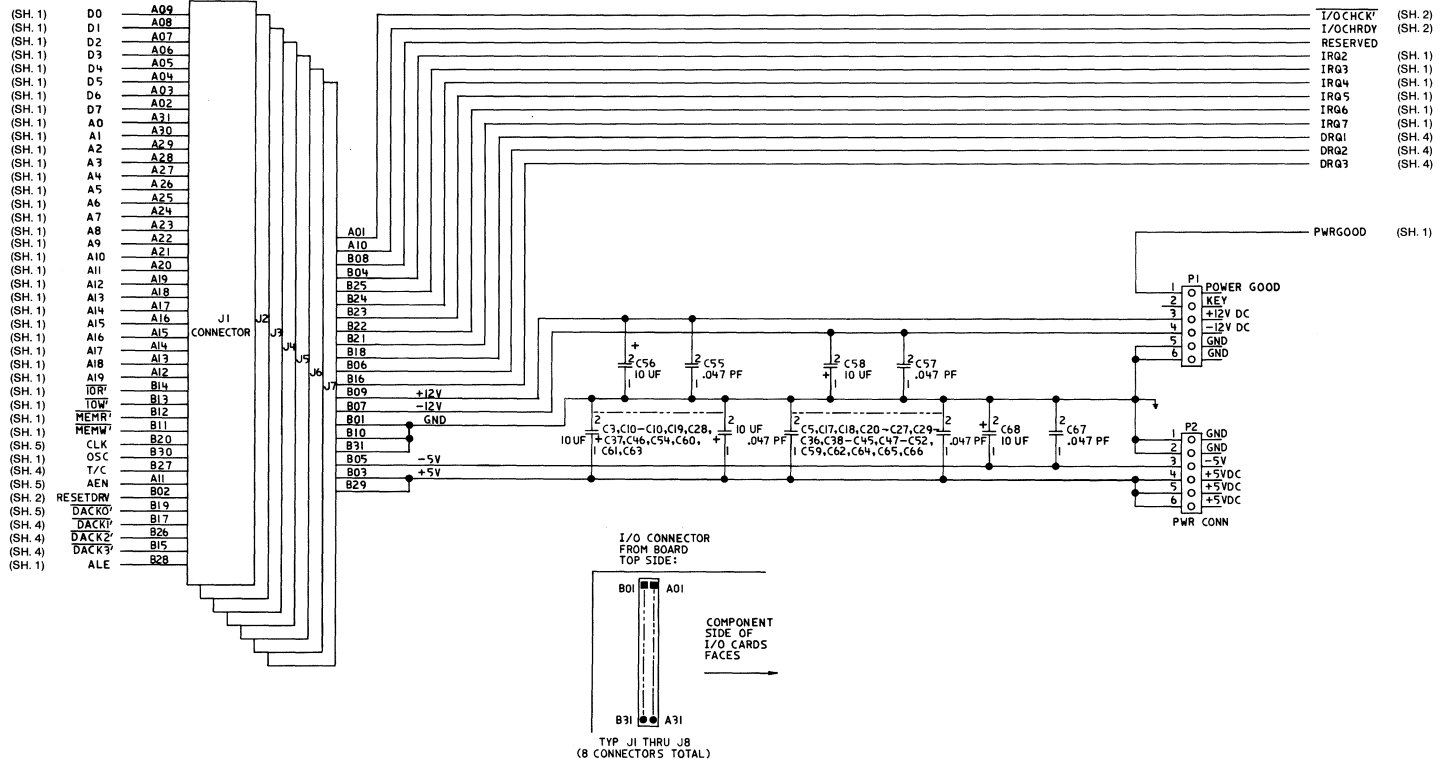
64/256K System Board (Sheet 7 of 11)



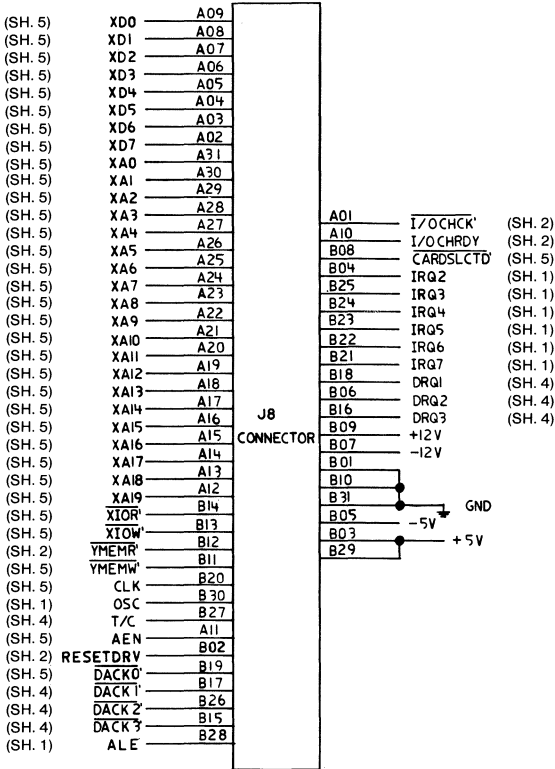
64/256K System Board (Sheet 8 of 11)



64/256K System Board (Sheet 9 of 11)



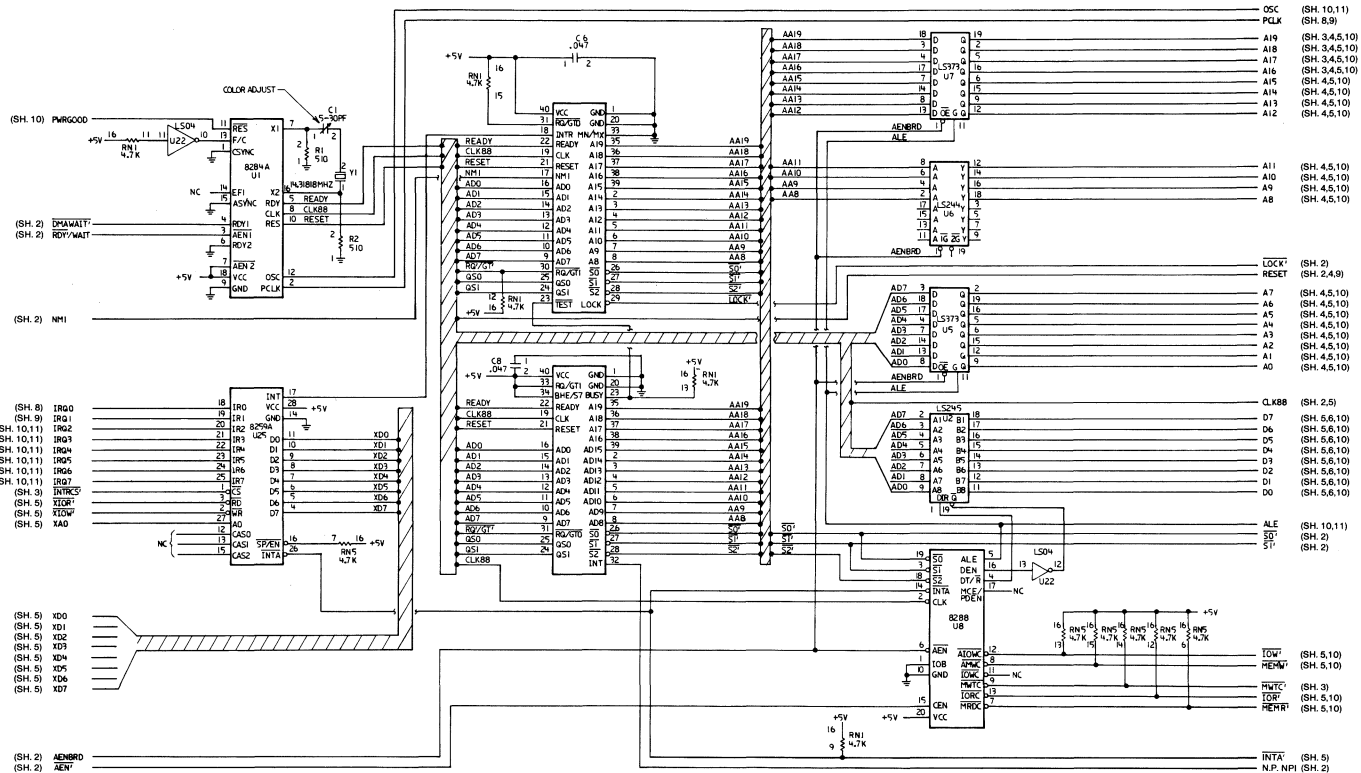
64/256K System Board (Sheet 10 of 11)



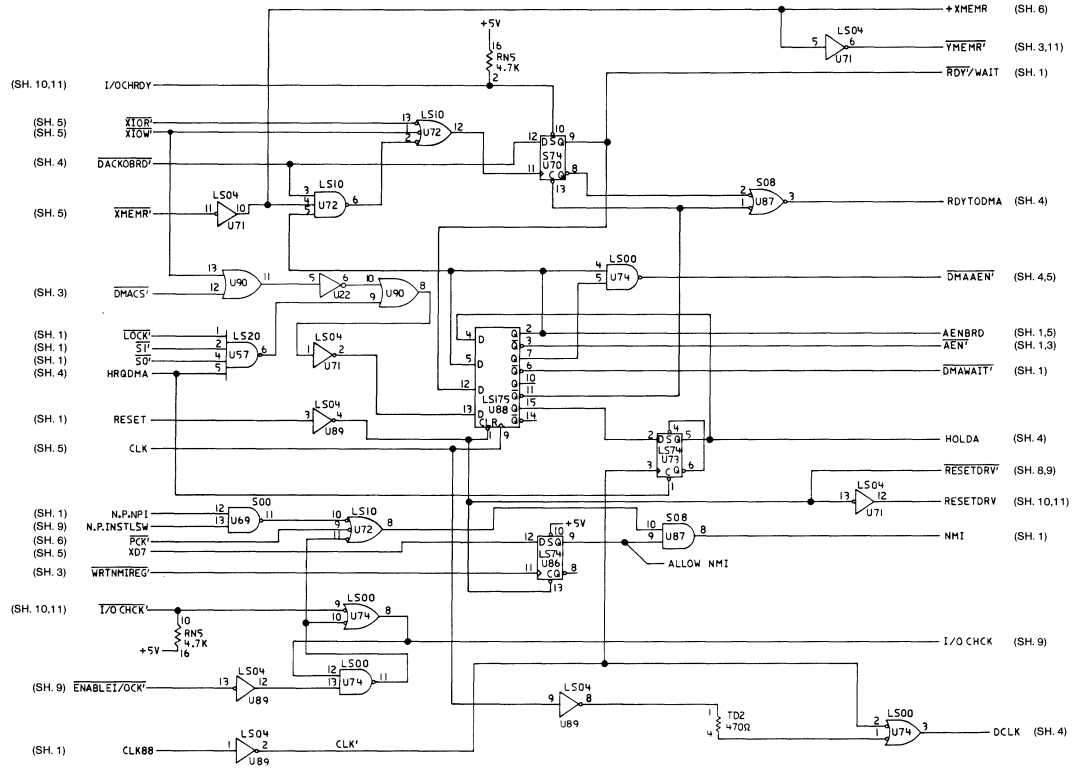
64/256K System Board (Sheet 11 of 11)

Logic Diagrams - 256/640K

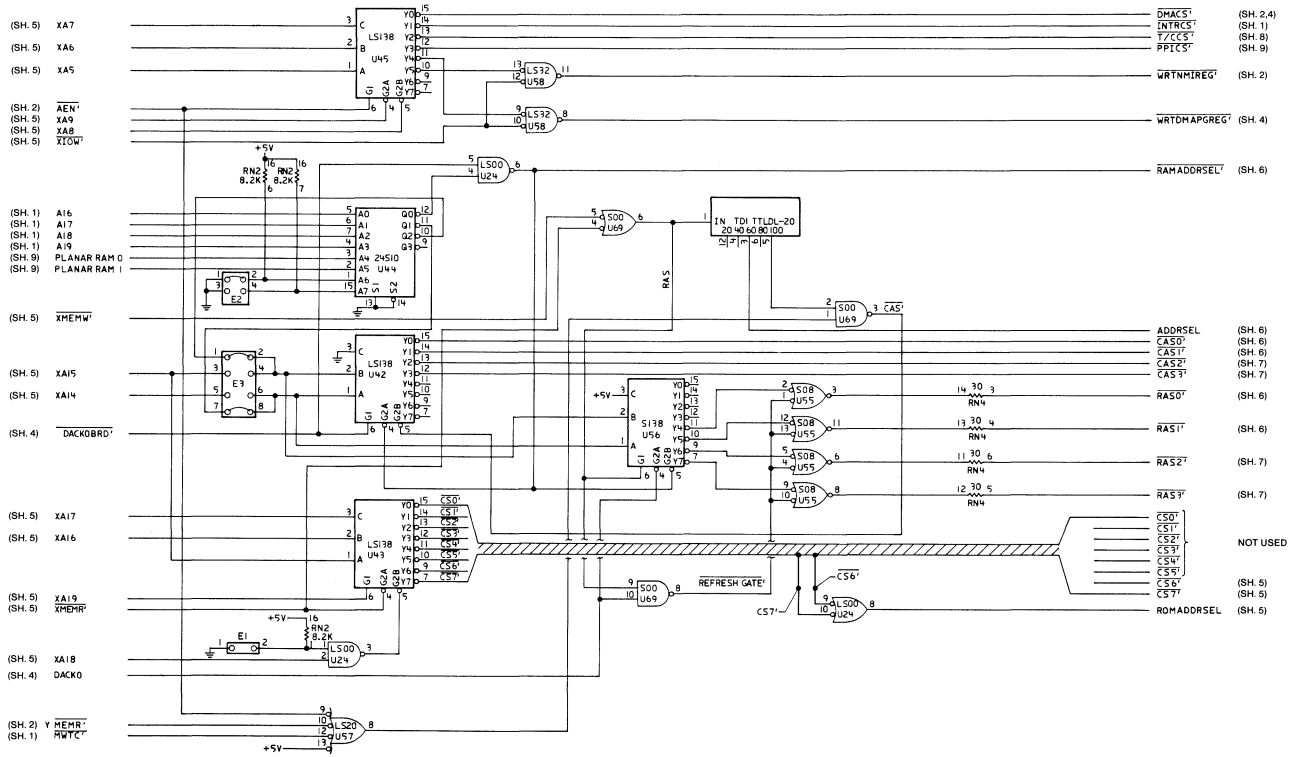
The following pages contain the logic diagrams for the 256/640K system board.



256/640K System Board (Sheet 1 of 11)

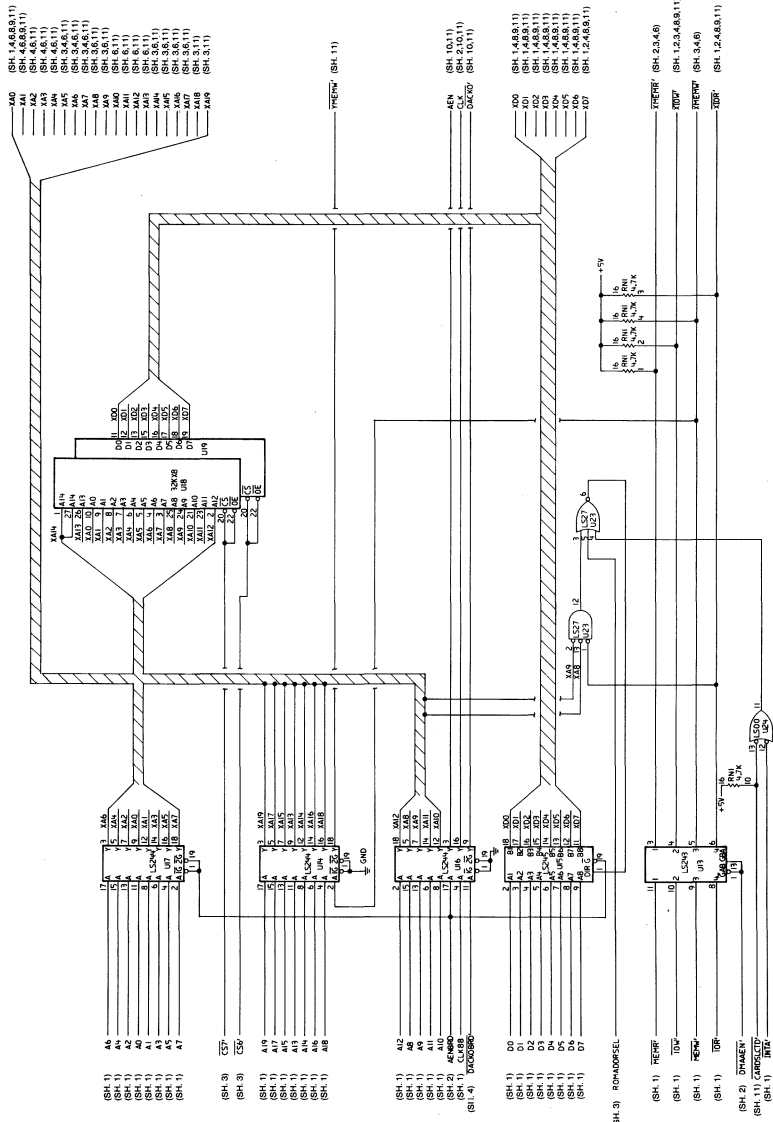


256/640K System Board (Sheet 2 of 11)

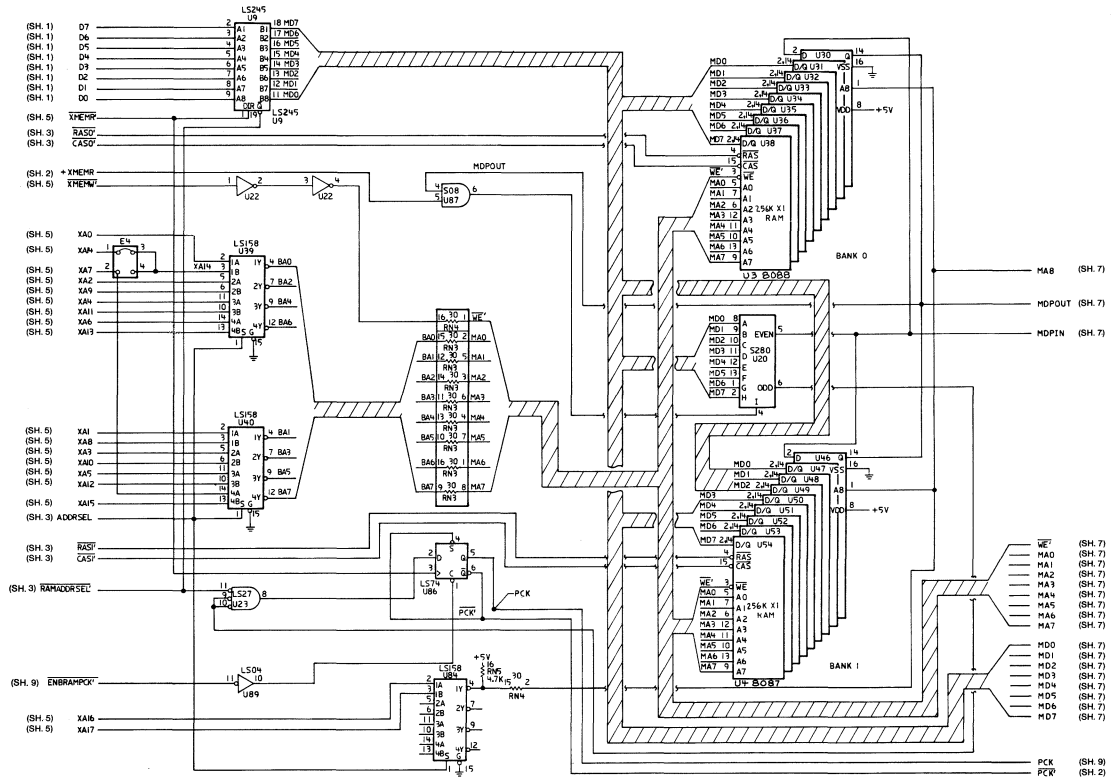


256/640K System Board (Sheet 3 of 11)

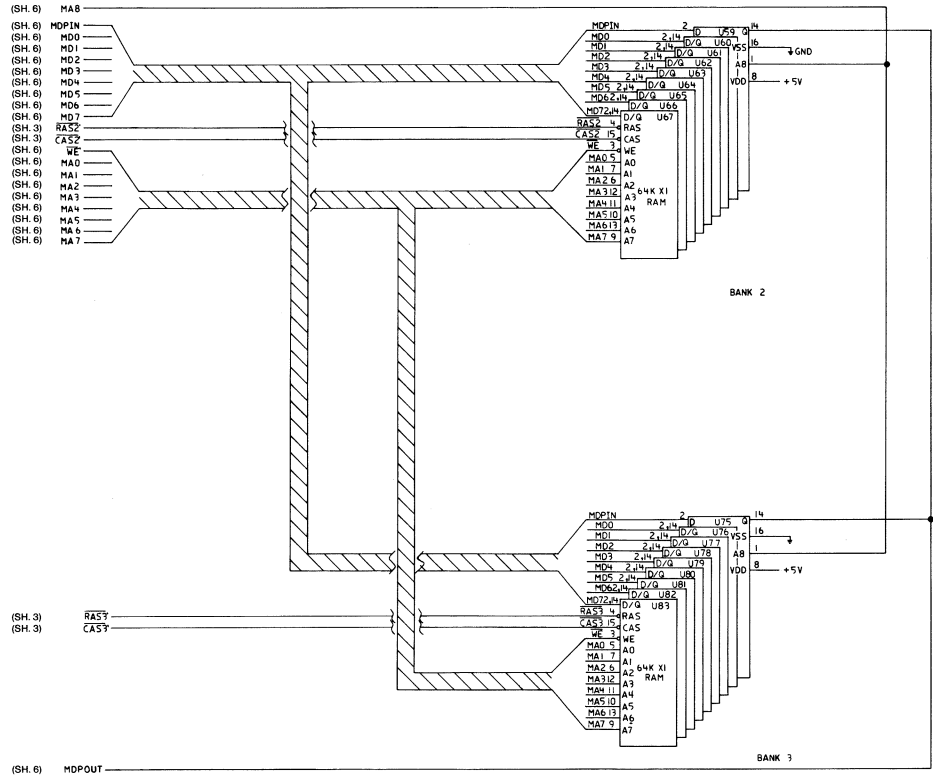




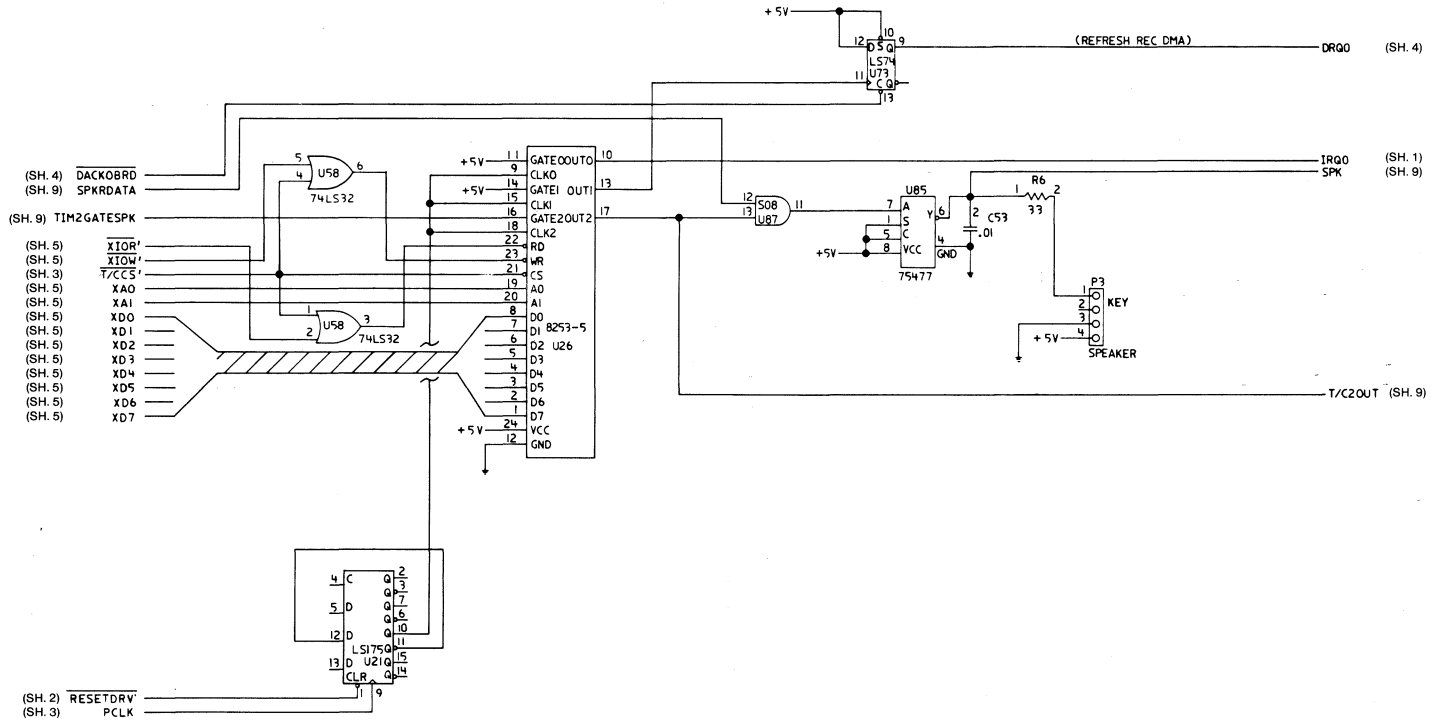
256/640K System Board (Sheet 5 of 11)



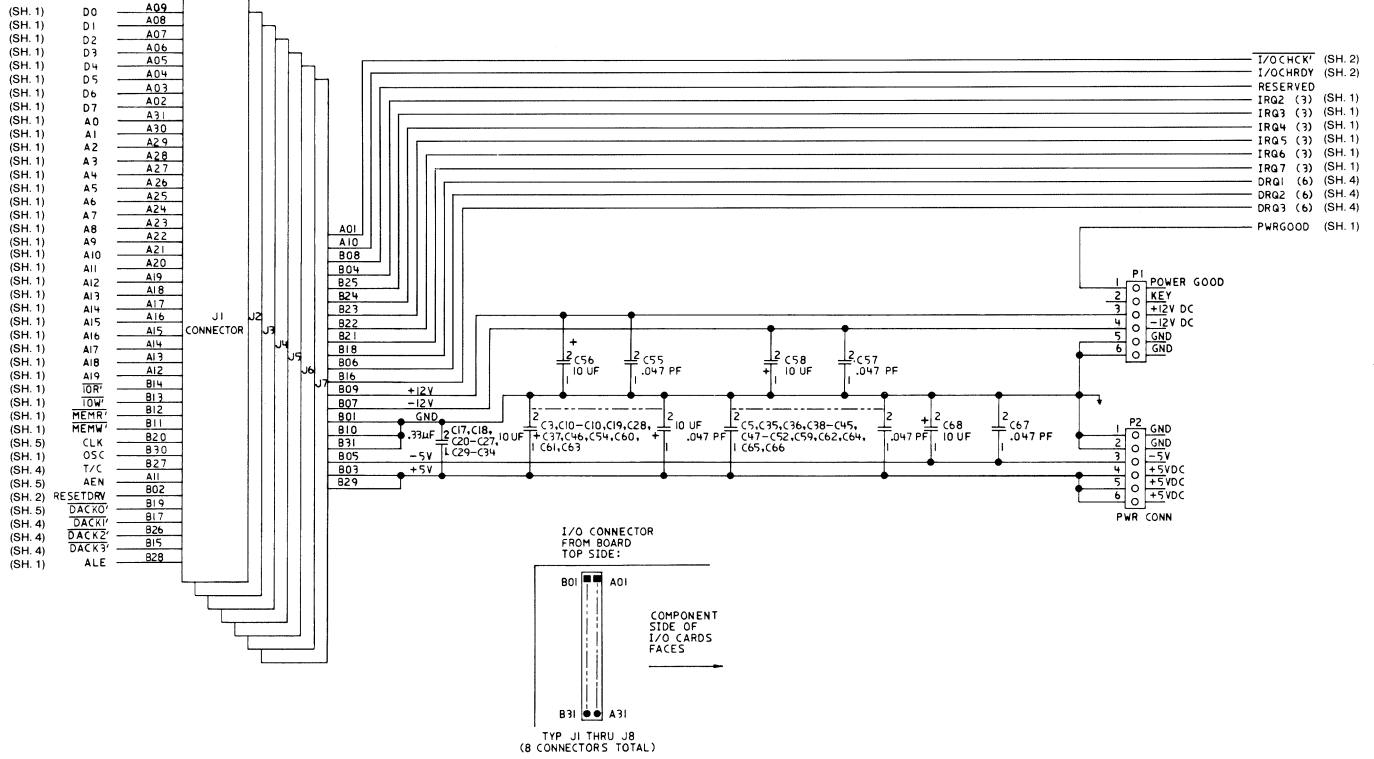
256/640K System Board (Sheet 6 of 11)



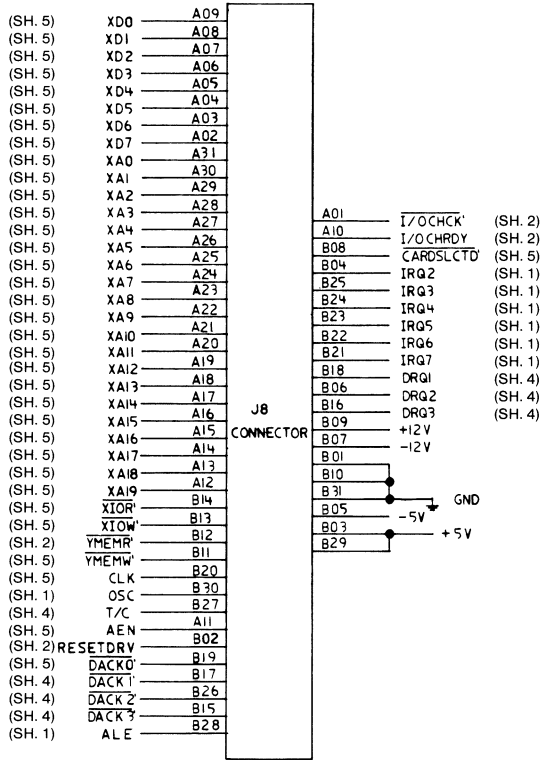
256/640K System Board (Sheet 7 of 11)



256/640K System Board (Sheet 8 of 11)



256/640K System Board (Sheet 10 of 11)



256/640K System Board (Sheet 11 of 11)

Notes:

SECTION 2. COPROCESSOR

Description	2-3
Programming Interface	2-4
Hardware Interface	2-4

Notes:

Description

The Math Coprocessor (8087) enables the IBM Personal Computer to perform high-speed arithmetic, logarithmic functions, and trigonometric operations with extreme accuracy.

The 8087 coprocessor works in parallel with the microprocessor. The parallel operation decreases operating time by allowing the coprocessor to do mathematical calculations while the microprocessor continues to do other functions.

The first five bits of every instruction's operation code for the coprocessor are identical (binary 11011). When the microprocessor and the coprocessor see this operation code, the microprocessor calculates the address of any variables in memory, while the coprocessor checks the instruction. The coprocessor takes the memory address from the microprocessor if necessary. To gain access to locations in memory, the coprocessor takes the local bus from the microprocessor when the microprocessor finishes its current instruction. When the coprocessor is finished with the memory transfer, it returns the local bus to the microprocessor.

The IBM Math Coprocessor works with seven numeric data types divided into the three classes listed below.

- Binary integers (3 types)
- Decimal integers (1 type)
- Real numbers (3 types).

Programming Interface

The coprocessor extends the data types, registers, and instructions to the microprocessor.

The coprocessor has eight 80-bit registers, which provide the equivalent capacity of the 40 16-bit registers found in the microprocessor. This register space allows constants and temporary results to be held in registers during calculations, thus reducing memory access and improving speed as well as bus availability. The register space can be used as a stack or as a fixed register set. When used as a stack, only the top two stack elements are operated on. The figure below shows representations of large and small numbers in each data type.

Data Type	Bits	Significant Digits (Decimal)	Approximate Range (Decimal)
Word Integer	16	4	$-32,768 \leq X \leq +32,767$
Short Integer	32	9	$-2 \times 10^9 \leq X \leq +2 \times 10^9$
Long Integer	64	18	$-9 \times 10^{18} \leq X \leq +9 \times 10^{18}$
Packed Decimal	80	18	$-9.99 \leq X \leq +9.99$ (18 digits)
Short Real *	32	6-7	$8.43 \times 10^{-37} \leq X \leq 3.37 \times 10^{38}$
Long Real *	64	15-16	$4.19 \times 10^{-307} \leq X \leq 1.67 \times 10^{308}$
Temporary Real	80	19	$3.4 \times 10^{-4932} \leq X \leq 1.2 \times 10^{4932}$

* The Short Real and Long Real data types correspond to the single and double precision data types.

Data Types

Hardware Interface

The coprocessor uses the same clock generator and system bus interface components as the microprocessor. The microprocessor's queue status lines (QS0 and QS1) enable the coprocessor to obtain and decode instructions simultaneously with the microprocessor. The coprocessor's 'busy' signal informs the microprocessor that it is executing; the microprocessor's WAIT

instruction forces the microprocessor to wait until the coprocessor is finished executing (WAIT FOR NOT BUSY).

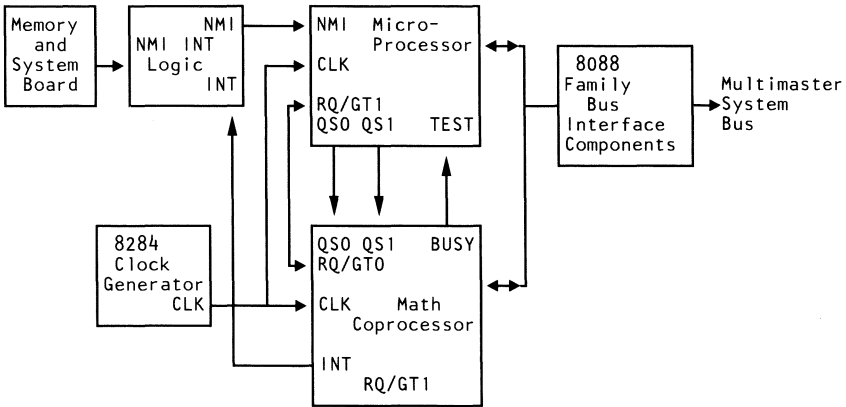
When an incorrect instruction is sent to the coprocessor (for example, divide by 0 or load a full register), the coprocessor can signal the microprocessor with an interrupt. There are three conditions that will disable the coprocessor interrupt to the microprocessor:

1. Exception and interrupt-enable bits of the control word are set to 1's
2. System-board switch-block 1, switch 2, set in the On position
3. Non-maskable interrupt register (NMI REG) is set to zero.

At power-on time, the NMI REG is cleared to disable the NMI. Any program using the coprocessor's interrupt capability must ensure that conditions 2 and 3 are never met during the operation of the software or an "Endless WAIT" will occur. An "Endless WAIT" will have the microprocessor waiting for the 'not busy' signal from the coprocessor while the coprocessor is waiting for the microprocessor to interrupt.

Because a memory parity error may also cause an interrupt to the microprocessor NMI line, the program should check the coprocessor status for an exception condition. If a coprocessor exception condition is not found, control should be passed to the normal NMI handler. If an 8087 exception condition is found, the program may clear the exception by executing the FNSAVE or the FNCLEX instruction, and the exception can be identified and acted upon.

The NMI REG and the coprocessor's interrupt are tied to the NMI line through the NMI interrupt logic. Minor modifications to programs designed for use with a coprocessor must be made before the programs will be compatible with the IBM Personal Computer Math Coprocessor.



Coprocessor Interconnection

Detailed information for the internal functions of the Intel 8087 Coprocessor can be found in the books listed in the Bibliography.

SECTION 3. POWER SUPPLIES

IBM Personal Computer XT Power Supply	3-3
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IBM Portable Personal Computer Power Supply	3-7
Description	3-7
Voltage and Current Requirements	3-7
Power Good Signal	3-8
Connector Specifications and Pin Assignments	3-9

Notes:

IBM Personal Computer XT Power Supply

Description

The system dc power supply is a 130-watt, 4 voltage-level switching regulator. It is integrated into the system unit and supplies power for the system unit, its options, and the keyboard. The supply provides 15 A of +5 Vdc, plus or minus 5%, 4.2 A of +12 Vdc, plus or minus 5%, 300 mA of -5 Vdc, plus or minus 10%, and 250 mA of -12 Vdc, plus or minus 10%. All power levels are regulated with overvoltage and overcurrent protection. There are two power supplies, 120 Vac and 220/240 Vac. Both are fused. If dc overcurrent or overvoltage conditions exist, the supply automatically shuts down until the condition is corrected. The supply is designed for continuous operation at 130 watts.

The system board takes approximately 2 to 4 A of +5 Vdc, thus allowing approximately 11 A of +5 Vdc for the adapters in the system expansion slots. The +12 Vdc power level is designed to power the internal diskette drives and the 10M or 20M fixed disk drive. The -5 Vdc level is used for analog circuits in the diskette adapter's phase-lock loop. The +12 Vdc and -12 Vdc are used for powering the Electronic Industries Association (EIA) drivers for the communications adapters. All four power levels are bussed across the eight system expansion slots.

The IBM Monochrome Display has its own power supply, receiving its ac power from the system unit's power system. The ac output for the display is switched on and off with the Power switch and is a nonstandard connector.

Input Requirements

The nominal power requirements and output voltages are listed in the following tables.

Voltage @ 50/60. Hz \pm 3 Hz		
Nominal Vac	Minimum Vac	Maximum Vac
110 220/240	90 180	137 259
Current: 4.1 A max at 90 Vac		

Input Requirements

Outputs

Nominal Output (Vdc)	Load Current (A)		Regulation Tolerance
	Min	Max	
+5 Vdc	2.3	15.0	+5% to -4%
-5 Vdc	0.0	0.3	+10% to -8%
+12 Vdc	0.4	4.2	+5% to -4%
-12 Vdc	0.0	0.25	+10% to -9%

Vdc Output

Nominal Output (Vac)	Load Current (A)		Voltage Limits	
	Min	Max	Min	Max
120 220/240	0.0 0.0	1.0 0.5	90 180	137 259

Vac Output

The sense levels of the dc outputs are:

Output (Vdc)	Minimum (Vdc)	Sense Voltage Nominal (Vdc)	Maximum (Vdc)
+5 Vdc	+4.5	+5.0	+5.5
-5 Vdc	-4.3	-5.0	-5.5
+12 Vdc	+10.8	+12.0	+13.2
-12 Vdc	-10.2	-12.0	-13.2

Vdc Sense Levels

Overvoltage/Overcurrent Protection

Voltage Nominal(Vac)	Type Protection	Rating Amps
110	Fuse	5.0
220/240	Fuse	3.5

Voltage and Current Protection

Power Good Signal

When the supply is switched off for a minimum of 1.0 second, and then switched on, the 'power good' signal will be regenerated.

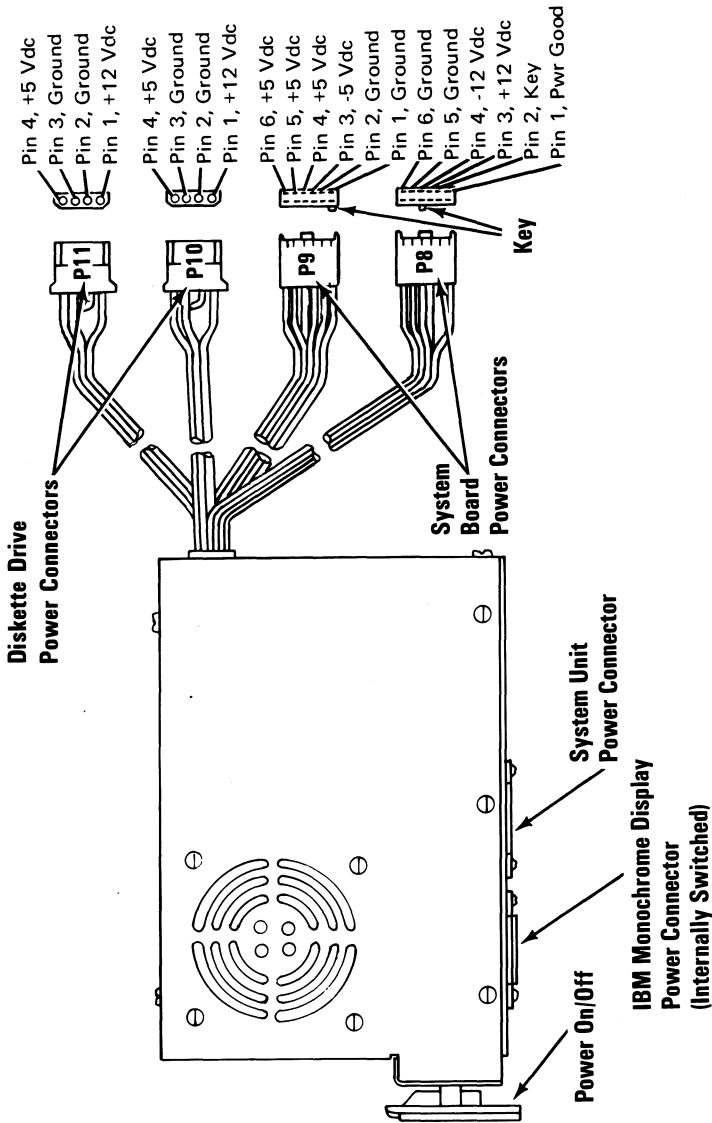
The 'power good' signal indicates that there is adequate power to continue processing. If the power goes below the specified levels, the 'power good' signal triggers a system shutdown.

This signal is the logical AND of the dc output-voltage 'sense' signal and the ac input-voltage 'fail' signal. This signal is TTL-compatible up-level for normal operation or down-level for fault conditions. The ac 'fail' signal causes 'power good' to go to a down level when any output voltage falls below the regulation limits.

The dc output-voltage 'sense' signal holds the 'power good' signal at a down level (during power-on) until all output voltages have reached their respective minimum sense levels. The 'power good' signal has a turn-on delay of at least 100 ms but no greater than 500 ms.

Connector Specifications and Pin Assignments

The power connector on the system board is a 12-pin male connector that plugs into the power-supply connectors. The pin assignments and locations are shown below.



Power Supply and Connectors

IBM Portable Personal Computer Power Supply

Description

The system unit's power supply is a 114-watt, switching regulator that provides five outputs. It supplies power for the system unit and its options, the power supply fan, the diskette drive, the composite display, and the keyboard. All power levels are protected against overvoltage and overcurrent conditions. The input voltage selector switch has 115 Vac and 230 Vac positions. If a dc overload or overvoltage condition exists, the power supply automatically shuts down until the condition is corrected, and the power supply is switched off and then on.

The internal 5-1/4 inch diskette drive uses the +5 Vdc and the +12 Vdc power levels. Both the +12 Vdc and -12 Vdc power levels are used in the drivers and receivers of the optional communications adapters. The display uses a separate +12 Vdc power level. The +5 Vdc, -5 Vdc, +12 Vdc, and -12 Vdc power levels are bussed across the system expansion slots.

Voltage and Current Requirements

Voltage @ 50/60 Hz \pm 3 Hz		
Nominal Vac	Minimum Vac	Maximum Vac
110 220/240	90 180	137 259
Current: 3.5 A max at 90 Vac		

Note: Input voltage to be 50 or 60 hertz, \pm 3 hertz.

Nominal Output (Vdc)	Load Current (A)		Regulation Tolerance
	Min	Max	
+5 Vdc	2.3	11.2	+5% to -4%
-5 Vdc	0.0	0.3	+10% to -8%
+12 Vdc	0.04	2.9	+5% to -4%
-12 Vdc	0.0	0.25	+10% to -9%
+12 Vdc (display)	0.5	1.5	+10% to -9%

Vdc Output

Output (Vdc)	Minimum (Vdc)	Sense Voltage Nominal (Vdc)	Maximum (Vdc)
+5 Vdc	+4.5	+5.0	+6.5
-5 Vdc	-4.3	-5.0	-6.5
+12 Vdc	+10.8	+12.0	+15.6
-12 Vdc	-10.2	-12.0	-15.6
+12 Vdc (display)	+10.8	+12.0	+15.6

Vdc Sense Levels

Voltage Nominal (Vac)	Type Protection	Rating Amps
110	Fuse	5.0
220/240	Fuse	2.5

Voltage and Current Protection

Power Good Signal

When the power supply is switched off for a minimum of 1 second and then switched on, the 'power good' signal is regenerated.

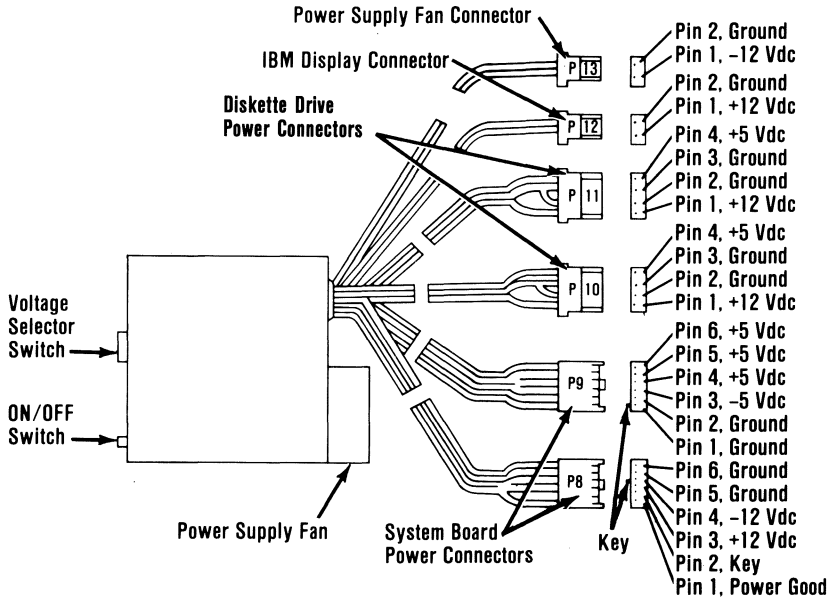
This signal is the logical **AND** of the dc output-voltage sense signal and the ac input-voltage fail signal. This signal is **TTL-compatible up-level** for normal operation or down-level for fault conditions. The ac 'fail' signal causes 'power good' to go to a down-level when any output voltage falls below the sense voltage limits.

When power is switched on, the dc output-voltage sense signal holds the 'power good' signal at a down level until all output

voltages reach their minimum sense levels. The 'power good' signal has a turn-on delay of 100 to 500 milliseconds.

Connector Specifications and Pin Assignments

The power connector on the system board is a 12-pin connector that plugs into the power supply connectors, P8 and P9. The Input Voltage Selector switch and the pin assignment locations follow.



Power Supply and Connectors

Notes:

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Introduction

Three keyboards are discussed in this section. The 83-key keyboard information for the Personal Computer XT and Portable Personal Computer begins below. Information about the IBM Enhanced Personal Computer Keyboard, hereafter referred to as the 101/102-Key Keyboard, begins on page 4-22.

83-Key Keyboard Description

The Personal Computer XT keyboard has a permanently attached cable that connects to a DIN connector at the rear of the system unit. This shielded 5-wire cable has power (+5 Vdc), ground, and two bidirectional signal lines. The cable is approximately 183 cm (6 ft) long and is coiled, like that of a telephone handset.

The IBM Portable Personal Computer keyboard cable is a detachable, 4-wire, shielded cable that connects to a modular connector in the front panel of the system unit. The cable has power (+5 Vdc), ground, and two bidirectional signal lines in it. It is 762 mm (30 in.) long and is coiled.

Both keyboards use a capacitive technology with a microprocessor (Intel 8048) performing the keyboard scan function. The keyboard has two tilt positions for operator comfort (5- or 15-degree tilt orientations for the Personal Computer XT and 5- or 12-degree tilt orientations for the IBM Portable Personal Computer).

Note: The following descriptions are common to both the Personal Computer XT and IBM Portable Personal Computer.

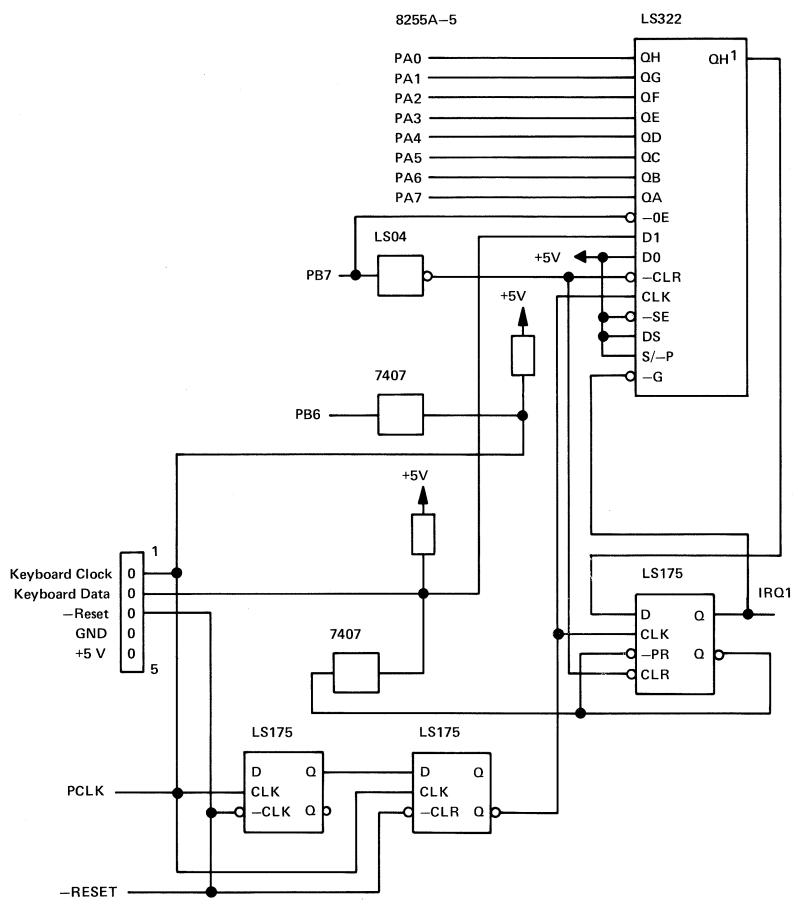
The keyboard has 83 keys arranged in three major groupings. The central portion of the keyboard is a standard typewriter keyboard layout. On the left side are 10 function keys. These keys are user-defined by the software. On the right is a 15-key keypad. These keys are also defined by the software, but have legends for the functions of numeric entry, cursor control, calculator pad, and screen edit.

The keyboard interface is defined so that system software has maximum flexibility in defining certain keyboard operations. This is accomplished by having the keyboard return scan codes rather than American Standard Code for Information Interchange (ASCII) codes. In addition, all keys are typematic (if held down, they will repeat) and generate both a make and a break scan code. For example, key 1 produces scan code hex 01 on make and code hex 81 on break. Break codes are formed by adding hex 80 to make codes. The keyboard I/O driver can define keyboard keys as shift keys or typematic, as required by the application.

The keyboard microprocessor (Intel 8048) performs several functions, including a power-on self test when requested by the system unit. This test checks the keyboard's ROM, tests memory, and checks for stuck keys. Additional functions are keyboard scanning, buffering of up to 16 key scan codes, maintaining bidirectional serial communications with the system unit, and executing the handshake protocol required by each scan-code transfer.

Several different keyboard arrangements are available. These are illustrated on the following pages. For information about the keyboard routines required to implement non-US keyboards, refer to the *Guide to Operations* and *DOS* manuals.

Block Diagram



Keyboard Interface Block Diagram

SECTION 4

Keyboard Encoding and Usage

Encoding

The keyboard routine provided by IBM in the ROM BIOS is responsible for converting the keyboard scan codes into what will be termed "Extended ASCII."

Extended ASCII encompasses 1-byte character codes with possible values of 0 to 255, an extended code for certain extended keyboard functions, and functions handled within the keyboard routine or through interrupts.

Character Codes

The following character codes are passed through the BIOS keyboard routine to the system or application program. A '-1' means the combination is suppressed in the keyboard routine. The codes are returned in AL.

Key	Base Case	Uppercase	Ctrl	Alt
1	Esc	Esc	-1	-1
2	1	!	-1	(*)
3	2	@	Nul(000) (*)	(*)
4	3	#	-1	(*)
5	4	\$	-1	(*)
6	5	%	-1	(*)
7	6	^	RS(030)	(*)
8	7	&	-1	(*)
9	8	*	-1	(*)
10	9	(-1	(*)
11	0)	-1	(*)
12	-		US(031)	(*)
13	=	+	-1	(*)
14	Backspace (008)	Backspace (008)	Del(127)	-1
15	→ (009)	← (*)	-1	-1
16	q	Q	DC1(017)	(*)
17	w	W	ETB(023)	(*)
18	e	E	ENQ(005)	(*)
19	r	R	DC2(018)	(*)
20	t	T	DC4(020)	(*)
21	y	Y	EM(025)	(*)
22	u	U	NAK(021)	(*)
23	i	I	HT(009)	(*)
24	o	O	SI(015)	(*)
25	p	P	DLE(016)	(*)
26	[{	Esc(027)	(*)
27]	}	GS(029)	-1
28	CR	CR	LF(010)	-1
29 Ctrl	-1	-1	-1	-1
30	a	A	SOH(001)	(*)
31	s	S	DC3(019)	(*)
32	d	D	EOT(004)	(*)
33	f	F	ACK(006)	(*)
34	g	G	BEL(007)	(*)
35	h	H	BS(008)	(*)
36	j	J	LF(010)	(*)
37	k	K	VT(011)	(*)
38	l	L	FF(012)	(*)
39	;	;	-1	-1
40	'	'	-1	-1
41	~	~	FS(028)	-1
42 Shift (Left)	-1	-1	-1	-1
43	\		FS(028)	-1
44	z	Z	SUB(026)	(*)
45	x	X	CAN(024)	(*)
46	c	C	ETX(003)	(*)

Notes:
 (*) Refer to "Extended Functions" in this section.

Character Codes (Part 1 of 2)

Key	Base Case	Uppercase	Ctrl	Alt
47	v	V	SYN(022)	(*)
48	b	B	STX(002)	(*)
49	n	N	SO(014)	(*)
50	m	M	CR(013)	(*)
51	,	<	-1	-1
52	.	>	-1	-1
53	/	?	-1	-1
54 Shift (Right)	-1	-1	-1	-1
55	*	PrtSc	?	?
56 Alt	-1	-1	-1	-1
57	Space	Space	Space	Space
58 Caps Lock	-1	-1	-1	-1
69 Num Lock	-1	-1 (*)	Pause (**)	-1
70 Scroll Lock	-1	-1	Break (**)	-1
107	-	-	(*)	(*)
108	Enter	Enter	-1	-1
112	Null (*)	Null (*)	Null (*)	Null (*)
113	Null (*)	Null (*)	Null (*)	Null (*)
114	Null (*)	Null (*)	Null (*)	Null (*)
115	Null (*)	Null (*)	Null (*)	Null (*)
116	Null (*)	Null (*)	Null (*)	Null (*)
117	Null (*)	Null (*)	Null (*)	Null (*)
118	Null (*)	Null (*)	Null (*)	Null (*)

Notes:

(*) Refer to "Extended Functions" in this section.

(**) Refer to "Special Handling" in this section.

Character Codes (Part 2 of 2)

Keys 71 through 83 have meaning only in base case, in Num Lock (or shifted) states, or in Ctrl state. Note that the Shift key temporarily reverses the current Num Lock state.

Extended Codes

Extended Functions

For certain functions that cannot be represented in the standard ASCII code, an extended code is used. A character code of 000 (Null) is returned in AL. This indicates that the system or application program should examine a second code that will indicate the actual function. Usually, but not always, this second code is the scan code of the primary key that was pressed. This code is returned in AH.

Shift States

Most shift states are handled within the keyboard routine and are not apparent to the system or application program. In any case, the current set of active shift states is available by calling an entry point in the ROM keyboard routine. The key numbers are shown on the keyboard diagrams beginning on page 4-12. The following keys result in altered shift states:

Shift

This key temporarily shifts keys 2–13, 15–27, 30–41, 43–53, 55, 59–68 to uppercase (base case if in Caps Lock state). Also, the Shift key temporarily reverses the Num Lock or non-Num-Lock state of keys 71–73, 75, 77, and 79–83.

Ctrl

This key temporarily shifts keys 3, 7, 12, 14, 16–28, 30–38, 43–50, 55, 59–71, 73, 75, 77, 79, and 81 to the Ctrl state. Also, the Ctrl key used with the Alt and Del keys causes the system reset function; with the Scroll Lock key, the break function; and with the Num Lock key, the pause function. The system reset, break, and pause functions are described in “Special Handling” on the following pages.

Alt

This key temporarily shifts keys 2–13, 16–25, 30–38, 44–50, and 59–68 to the Alt state. Also, the Alt key is used with the Ctrl and Del keys to cause the system reset function described in “Special Handling” on the following pages.

The Alt key has another use. This key allows the user to enter any ASCII character code from 1 to 255 into the system from the keyboard. The user holds down the Alt key and types the decimal value of the characters desired using the numeric keypad (keys 71–73, 75–77, and 79–82). The Alt key is then released. If more than three digits are typed, a modulo-256 result is created. These three digits are interpreted as a character code and are transmitted through the keyboard routine to the system or application program. Alt is handled within the keyboard routine.

Caps Lock

This key shifts keys 16–25, 30–38, and 44–50 to uppercase. Pressing the Caps Lock key a second time reverses the action. Caps Lock is handled within the keyboard routine.

Scroll Lock

This key is interpreted by appropriate application programs as indicating that use of the cursor-control keys should cause windowing over the text rather than cursor movement. Pressing the Scroll Lock key a second time reverses the action. The keyboard routine simply records the current shift state of the Scroll Lock key. It is the responsibility of the system or application program to perform the function.

Shift Key Priorities and Combinations

If combinations of the Alt, Ctrl, and Shift keys are pressed and only one is valid, the precedence is as follows: the Alt key is first, the Ctrl key is second, and the Shift key is third. The only valid combination is Alt and Ctrl, which is used in the system reset function.

Special Handling

System Reset

The combination of the Alt, Ctrl, and Del keys will result in the keyboard routine initiating the equivalent of a system reset. System reset is handled within the keyboard routine.

Break

The combination of the Ctrl and Break keys will result in the keyboard routine signaling interrupt hex 1B. Also the extended characters (AL = hex 00, AH = hex 00) will be returned.

Pause

The combination of the Ctrl and Num Lock keys will cause the keyboard interrupt routine to loop, waiting for any key except the Num Lock key to be pressed. This provides a system- or application-transparent method of temporarily suspending list, print, and so on, and then resuming the operation. The “unpause” key is thrown away. Pause is handled within the keyboard routine.

Print Screen

The combination of the Shift and PrtSc keys will result in an interrupt invoking the print screen routine. This routine works in the alphameric or graphics mode, with unrecognizable characters printing as blanks.

Extended Functions

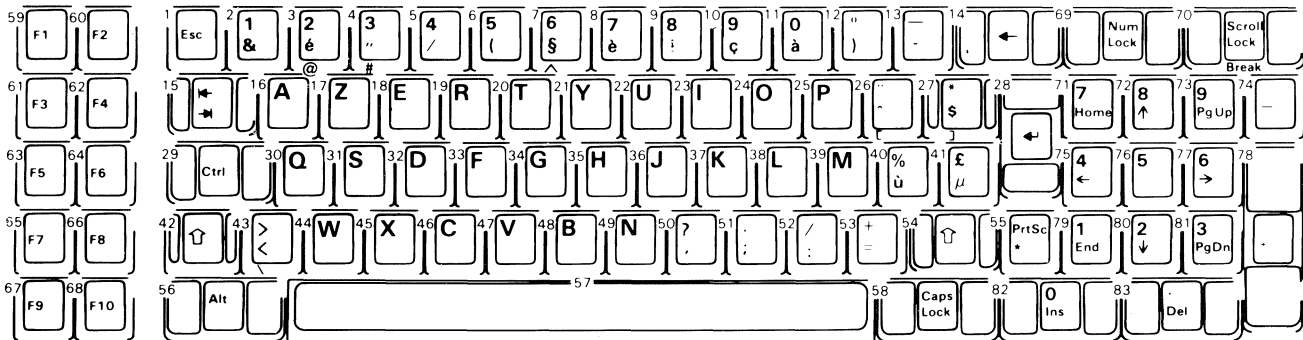
The keyboard routine does its own buffering. The keyboard buffer is large enough that few typists will ever fill it. However, if a key is pressed when the buffer is full, the key will be ignored and the "bell" will sound.

Also, the keyboard routine suppresses the typematic action of the following keys: Ctrl, Shift, Alt, Num Lock, Scroll Lock, Caps Lock, and Ins.

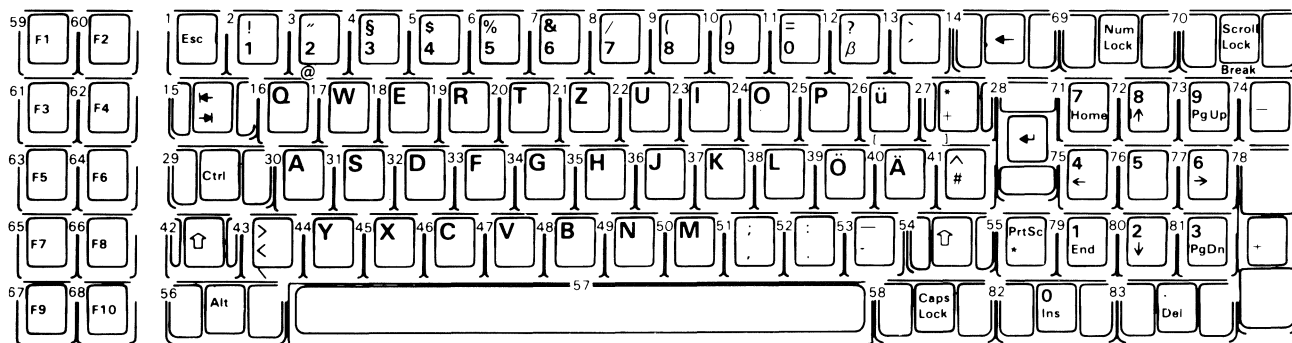
Keyboard Layouts

The IBM Personal Computer keyboard is available in six different layouts as shown on the following pages:

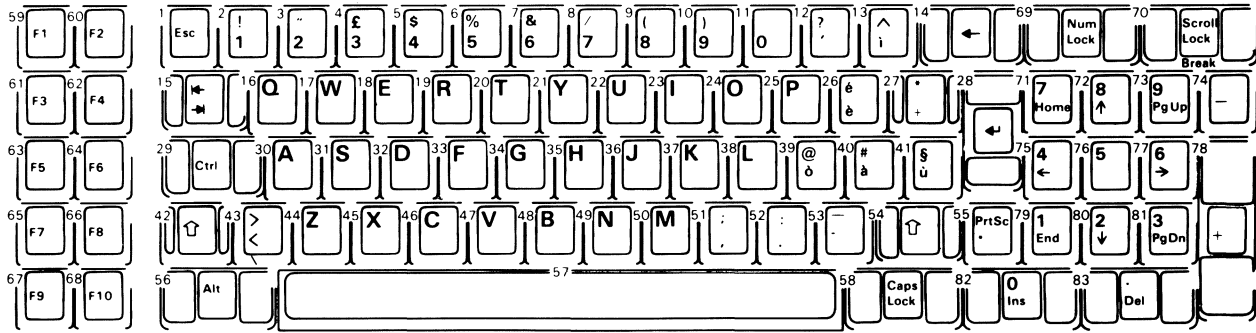
- French
- German
- Italian
- Spanish
- UK English
- US English



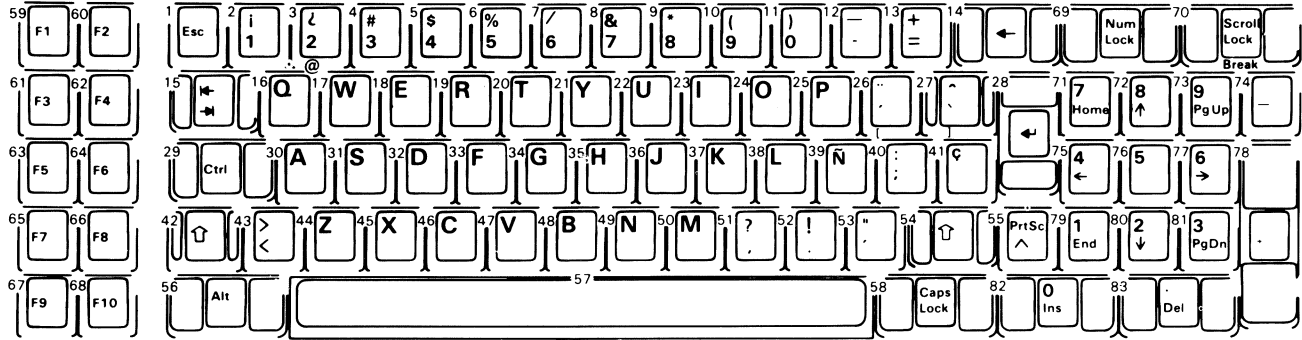
Note: Nomenclature is on both the top and front face of keybuttons as shown. The number to the upper left designates the button position.



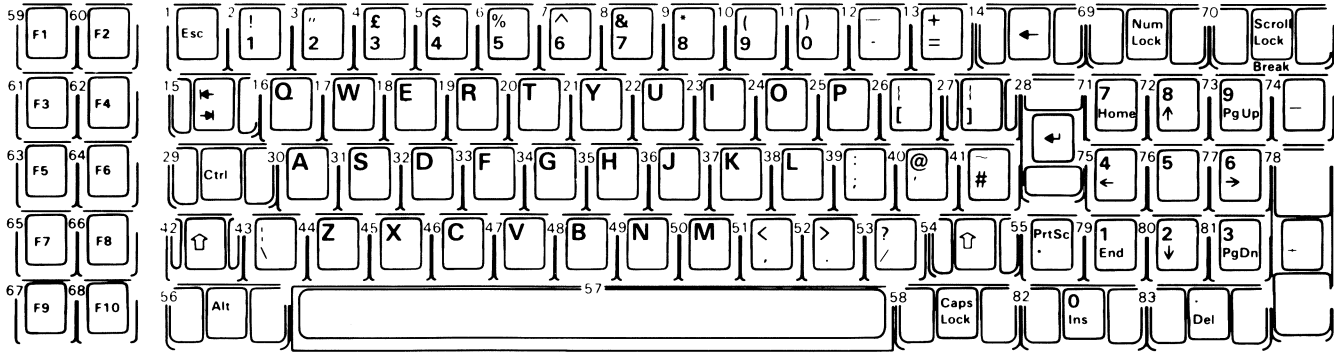
Note: Nomenclature is on both the top and front face of keybuttons as shown. The number to the upper left designates the button position.



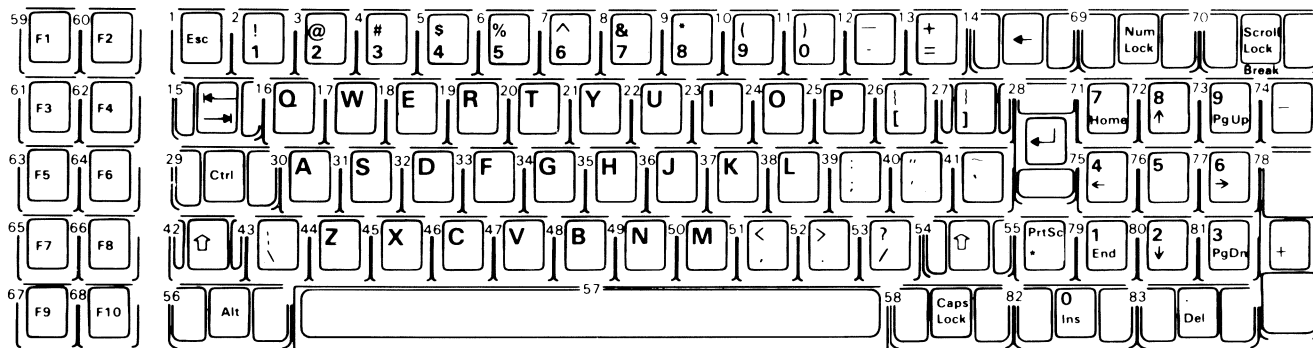
Note: Nomenclature is on both the top and front face of keybuttons as shown. The number to the upper left designates the button position.



Note: Nomenclature is on both the top and front face of keybuttons as shown. The number to the upper left designates the button position.

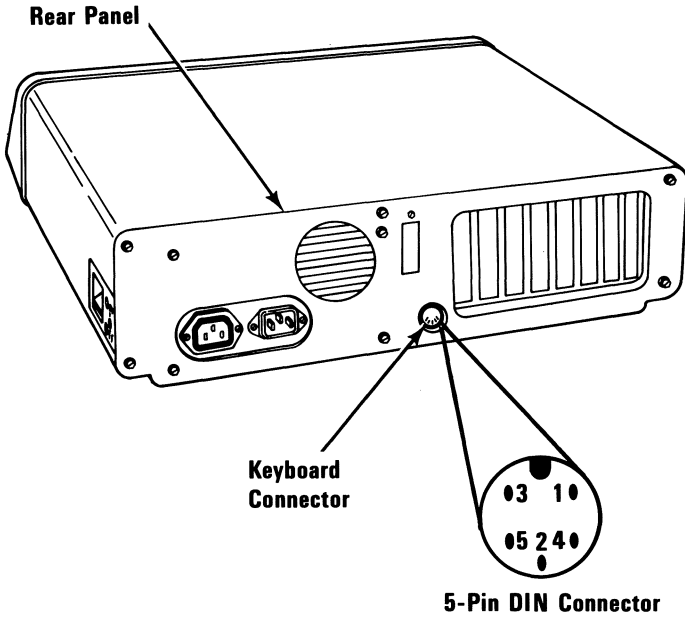


Note: Nomenclature is on both the top and front face of keybuttons as shown. The number to the upper left designates the button position.



Note: Nomenclature is on both the top and front face of keybutons as shown. The number to the upper left designates the button position.

Connector Specifications

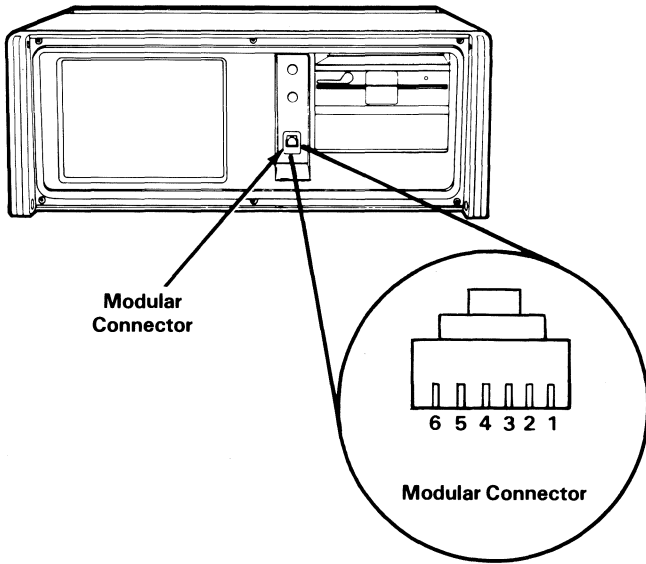


5-Pin DIN Connector

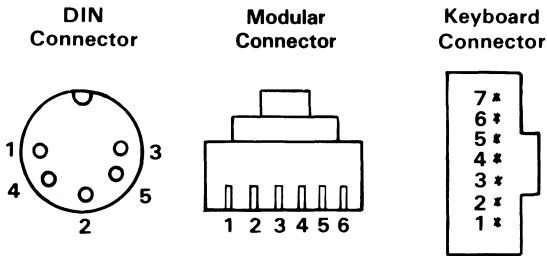
Pin	TTL Signal	Signal Level
1	+ Keyboard Clock	+ 5 Vdc
2	+ Keyboard Data	+ 5 Vdc
3	- Keyboard Reset (Not used by keyboard)	
Power Supply Voltages		Voltage
4	Ground	0
5	+ 5 Volts	+ 5 Vdc

Keyboard Interface Connector Specifications

SECTION 4



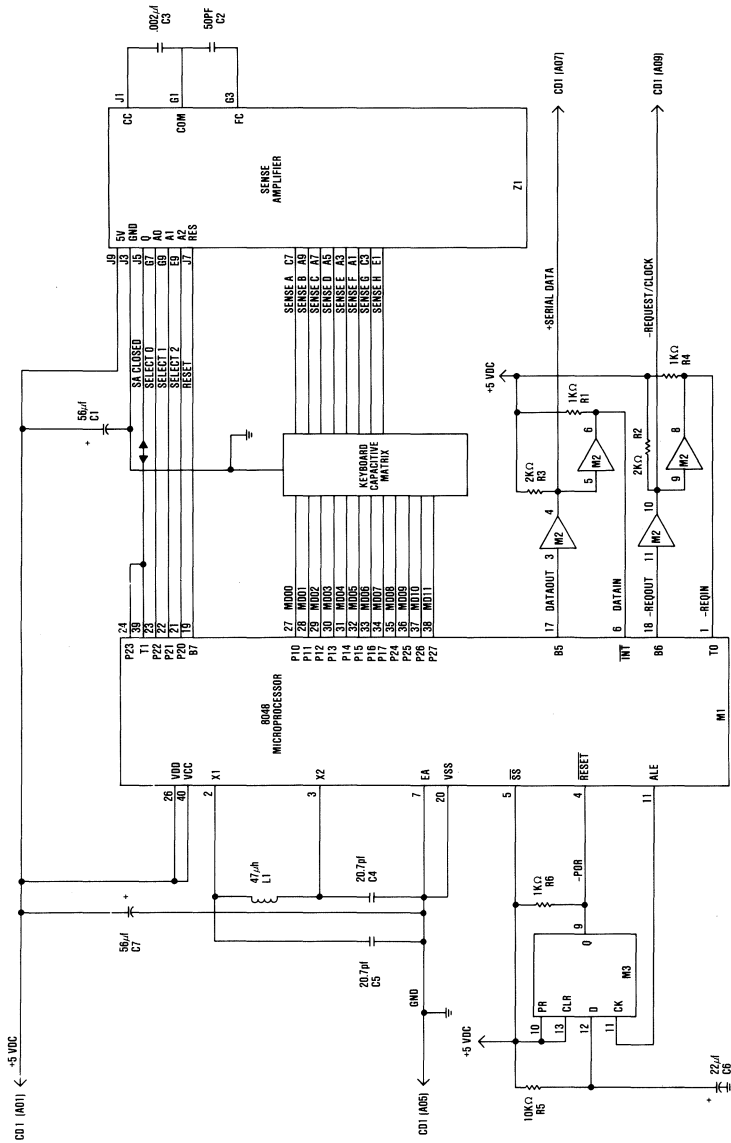
Keyboard Cable Connections



	Pin Side	Pin Side	Wire Side
Clock	1	4	6
Data	2	5	5
Ground	4	3	4
+5 Volts	5	2	2

Modular connector pin 1 is connected to the ground wire going to the chassis.
 The ground wire at the keyboard connector is attached to the ground screw on the keyboard logic board.

Keyboard Logic Diagram



83-Key Keyboard

SECTION 4

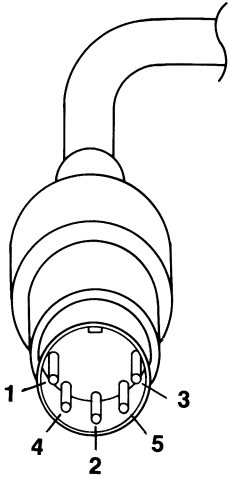
101/102-Key Keyboard

Description

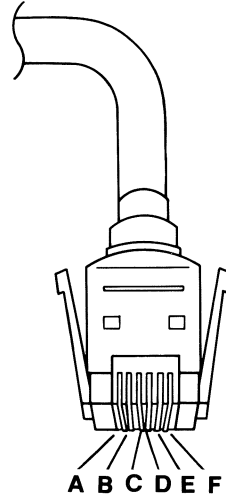
The keyboard has 101 keys (102 in countries outside the U. S.). At system power-on, the keyboard monitors the signals on the 'clock' and 'data' lines and establishes its line protocol.

Cables and Connectors

The keyboard cable connects to the system with a 5-pin DIN connector, and to the keyboard with a 6-position SDL connector. The following table shows the pin configuration and signal assignments.



DIN Connector



SDL Connector

DIN Connector Pins	SDL Connector Pins	Signal Name	Signal Type
1	C	+KBD CLK	Input/Output
2	E	+KBD DATA	Input/Output
3	A	Reserved	
4	D	Ground	Power
5	B	+5.0 Vdc	Power
	F	Not used	
Shield	Shield	Frame Ground	

Sequencing Key-Code Scanning

The keyboard detects all keys pressed, and sends each scan code in the correct sequence. When not serviced by the system, the keyboard stores the scan codes in its buffer.

Keyboard Buffer

A 16-byte first-in-first-out (FIFO) buffer in the keyboard stores the scan codes until the system is ready to receive them.

A buffer-overflow condition occurs when more than 16 bytes are placed in the keyboard buffer. An overflow code replaces the 17th byte. If more keys are pressed before the system allows keyboard output, the additional data is lost.

When the keyboard is allowed to send data, the bytes in the buffer will be sent as in normal operation, and new data entered is detected and sent. Response codes do not occupy a buffer position.

If keystrokes generate a multiple-byte sequence, the entire sequence must fit into the available buffer space or the keystroke is discarded and a buffer-overflow condition occurs.

Keys

With the exception of the Pause key, all keys are *make/break*. The make scan code of a key is sent to the keyboard controller when the key is pressed. When the key is released, its break scan code is sent.

Additionally, except for the Pause key, all keys are *typematic*. When a key is pressed and held down, the keyboard sends the make code for that key, delays 500 milliseconds \pm 20%, and begins sending a make code for that key at a rate of 10.9 characters per second \pm 20%.

If two or more keys are held down, only the last key pressed repeats at the typematic rate. Typematic operation stops when the last key pressed is released, even if other keys are still held down. If a key is pressed and held down while keyboard transmission is inhibited, only the first make code is stored in the buffer. This prevents buffer overflow as a result of typematic action.

Power-On Routine

The following activities take place when power is first applied to the keyboard.

Power-On Reset

The keyboard logic generates a 'power-on reset' signal (POR) when power is first applied to the keyboard. POR occurs during 150 milliseconds to 2.0 seconds from the time power is first applied to the keyboard.

Basic Assurance Test

The basic assurance test (BAT) consists of a keyboard processor test, a checksum of the read-only memory (ROM), and a random-access memory (RAM) test. During the BAT, activity on the 'clock' and 'data' lines is ignored. The BAT takes from 300 to 500 milliseconds. This is in addition to the time required by the POR.

Upon satisfactory completion of the BAT, a completion code (hex AA) is sent to the system, and keyboard scanning begins. If a BAT failure occurs, the keyboard sends an error code to the system. The keyboard is then disabled pending command input. Completion codes are sent 450 milliseconds to 2.5 seconds after POR, and between 300 and 500 milliseconds after a Reset command is acknowledged.

Immediately following POR, the keyboard monitors the signals on the keyboard 'clock' and 'data' lines and sets the line protocol.

Commands from the System

Reset (Hex FF)

The system lowers the 'clock' line for a minimum of 12.5 milliseconds. The keyboard then begins to clock bits on the 'data' line. The result is a Reset command causing the keyboard to reset itself, perform a BAT, and return the appropriate completion code.

Commands to the System

The following table shows the commands that the keyboard may send to the system, and their hexadecimal values.

Command	Hex Value
BAT Completion Code	AA
BAT Failure Code	FC
Key Detection Error/Overrun	FF

The commands the keyboard sends to the system are described below, in alphabetic order.

BAT Completion Code (Hex AA)

Following satisfactory completion of the BAT, the keyboard sends hex AA. Any other code indicates a failure of the keyboard.

BAT Failure Code (Hex FC)

If a BAT failure occurs, the keyboard sends this code, discontinues scanning, and waits for a system response or reset.

Key Detection Error (Hex FF)

The keyboard sends a key detection error character (hex FF) if conditions in the keyboard make it impossible to identify a switch closure.

Overflow (Hex FF)

An overflow character (hex FF) is placed in the keyboard buffer and replaces the last code when the buffer capacity has been exceeded. The code is sent to the system when it reaches the top of the buffer queue.

Keyboard Scan Codes

Each key is assigned a base scan code and, in some cases, extra codes to generate artificial shift states in the system. The typematic scan codes are identical to the base scan code for each key.

Scan Code Tables

The following keys send the codes as shown, regardless of any shift states in the keyboard or the system. Refer to "Keyboard Layouts" beginning on page 4-44 to determine the character associated with each key number.

Key Number	Make Code	Break Code
1	29	A9
2	02	82
3	03	83
4	04	84
5	05	85
6	06	86
7	07	87
8	08	88
9	09	89
10	0A	8A
11	0B	8B
12	0C	8C
13	0D	8D
15	0E	8E
16	0F	8F
17	10	90
18	11	91
19	12	92
20	13	93
21	14	94
22	15	95
23	16	96
24	17	97
25	18	98
26	19	99
27	1A	9A
28	1B	9B
29 *	2B	AB
30	3A	BA
31	1E	9E
32	1F	9F
33	20	A0

* 101-key keyboard only.

Key Number	Make Code	Break Code
34	21	A1
35	22	A2
36	23	A3
37	24	A4
38	25	A5
39	26	A6
40	27	A7
41	28	A8
42 **	2B	AB
43	1C	9C
44	2A	AA
45 **	56	D6
46	2C	AC
47	2D	AD
48	2E	AE
49	2F	AF
50	30	B0
51	31	B1
52	32	B2
53	33	B3
54	34	B4
55	35	B5
57	36	B6
58	1D	9D
60	38	B8
61	39	B9
62	EO 38	EO B8
64	EO 1D	EO 9D
90	45	C5
91	47	C7
92	4B	CB
93	4F	CF
96	48	C8
97	4C	CC
98	50	D0
99	52	D2
100	37	B7
101	49	C9
102	4D	CD
103	51	D1
104	53	D3
105	4A	CA
106	4E	CE
108	EO 1C	EO 9C
110	01	81
112	3B	BB
113	3C	BC
114	3D	BD
115	3E	BE
116	3F	BF
117	40	CO
118	41	C1
119	42	C2

** 102-key keyboard only.

SECTION 4

Key Number	Make Code	Break Code
120	43	C3
121	44	C4
122	57	D7
123	58	D8
125	46	C6

The remaining keys send a series of codes dependent on the state of the various shift keys (Ctrl, Alt, and Shift), and the state of Num Lock (On or Off). Because the base scan code is identical to that of another key, an extra code (hex E0) has been added to the base code to make it unique.

Key No.	Base Case, or Shift+Num Lock Make/Break	Shift Case Make/Break *	Num Lock on Make/Break
75	E0 52 /E0 D2	E0 AA E0 52 /E0 D2 E0 2A	E0 2A E0 52 /E0 D2 E0 AA
76	E0 53 /E0 D3	E0 AA E0 53 /E0 D3 E0 2A	E0 2A E0 53 /E0 D3 E0 AA
79	E0 4B /E0 CB	E0 AA E0 4B /E0 CB E0 2A	E0 2A E0 4B /E0 CB E0 AA
80	E0 47 /E0 C7	E0 AA E0 47 /E0 C7 E0 2A	E0 2A E0 47 /E0 C7 E0 AA
81	E0 4F /E0 CF	E0 AA E0 4F /E0 CF E0 2A	E0 2A E0 4F /E0 CF E0 AA
83	E0 48 /E0 C8	E0 AA E0 48 /E0 C8 E0 2A	E0 2A E0 48 /E0 C8 E0 AA
84	E0 50 /E0 D0	E0 AA E0 50 /E0 D0 E0 2A	E0 2A E0 50 /E0 D0 E0 AA
85	E0 49 /E0 C9	E0 AA E0 49 /E0 C9 E0 2A	E0 2A E0 49 /E0 C9 E0 AA
86	E0 51 /E0 D1	E0 AA E0 51 /E0 D1 E0 2A	E0 2A E0 51 /E0 D1 E0 AA
89	E0 4D /E0 CD	E0 AA E0 4D /E0 CD E0 2A	E0 2A E0 4D /E0 CD E0 AA

* If the left Shift key is held down, the AA/2A shift break and make is sent with the other scan codes. If the right Shift key is held down, B6/36 is sent. If both Shift keys are down, both sets of codes are sent with the other scan code.

Key No.	Scan Code Make/Break	Shift Case Make/Break *
95	E0 35/E0 B5	E0 AA E0 35/E0 B5 E0 2A
* If the left Shift key is held down, the AA/2A shift break and make is sent with the other scan codes. If the right Shift key is held down, B6/36 is sent. If both Shift keys are down, both sets of codes are sent with the other scan code.		

Key No.	Scan Code Make/Break	Ctrl Case, Shift Case Make/Break	Alt Case Make/Break
124	E0 2A E0 37 /E0 B7 E0 AA	E0 37/E0 B7	54/D4

Key No.	Make Code	Ctrl Key Pressed
126 *	E1 1D 45 E1 9D C5	E0 46 E0 C6
* This key is not typematic. All associated scan codes occur on the make of the key.		

SECTION 4

Clock and Data Signals

The keyboard and system communicate over the 'clock' and 'data' lines. The source of each of these lines is an open-collector device on the keyboard that allows either the keyboard or the system to force a line to an inactive (low) level. When no communication is occurring, the 'clock' line is at an active (high) level. The state of the 'data' line is held inactive (low) by the keyboard.

An inactive signal will have a value of at least 0, but not greater than +0.7 volts. A signal at the inactive level is a logical 0. An active signal will have a value of at least +2.4, but not greater than +5.5 volts. A signal at the active level is a logical 1. Voltages are measured between a signal source and the dc network ground.

The keyboard 'clock' line provides the clocking signals used to clock serial data from the keyboard. If the host system forces the 'clock' line to an inactive level, keyboard transmission is inhibited.

When the keyboard sends data to the system, it generates the 'clock' signal to time the data. The system can prevent the keyboard from sending data by forcing the 'clock' line to an inactive level, or by holding the 'data' line at an inactive level.

During the BAT, the keyboard allows the 'clock' and 'data' lines to go to an active level.

Data Stream

Data transmissions from the keyboard consist of a 9-bit data stream sent serially over the 'data' line. A logical 1 is sent at an active (high) level. The following table shows the functions of the bits.

Bit	Function
1	Start bit (always 1)
2	Data bit 0 (least-significant)
3	Data bit 1
4	Data bit 2
5	Data bit 3
6	Data bit 4
7	Data bit 5
8	Data bit 6
9	Data bit 7 (most-significant)

Keyboard Data Output

When the keyboard is ready to send data, it first checks the status of the keyboard 'clock' line. If the line is active (high), the keyboard issues a request-to-send (RTS) by making the 'clock' line inactive (low). The system must respond with a clear-to-send (CTS), generated by allowing the 'data' line to become active, within 250 microseconds after RTS, or data will be stored in the keyboard buffer. After receiving CTS, the keyboard begins sending the 9 serial bits. The leading edge of the first clock pulse will follow CTS by 60 to 120 microseconds. During each clock cycle, the keyboard clock is active for 25 to 50 microseconds. Each data bit is valid from 2.5 microseconds before the leading edge until 2.5 microseconds after the trailing edge of each keyboard clock cycle.

Keyboard Encoding and Usage

The keyboard routine, provided by IBM in the ROM BIOS, is responsible for converting the keyboard scan codes into what will be termed *Extended ASCII*. The extended ASCII codes returned by the ROM routine are mapped to the US English keyboard layout. Some operating systems may make provisions for alternate keyboard layouts by providing an interrupt replacer,

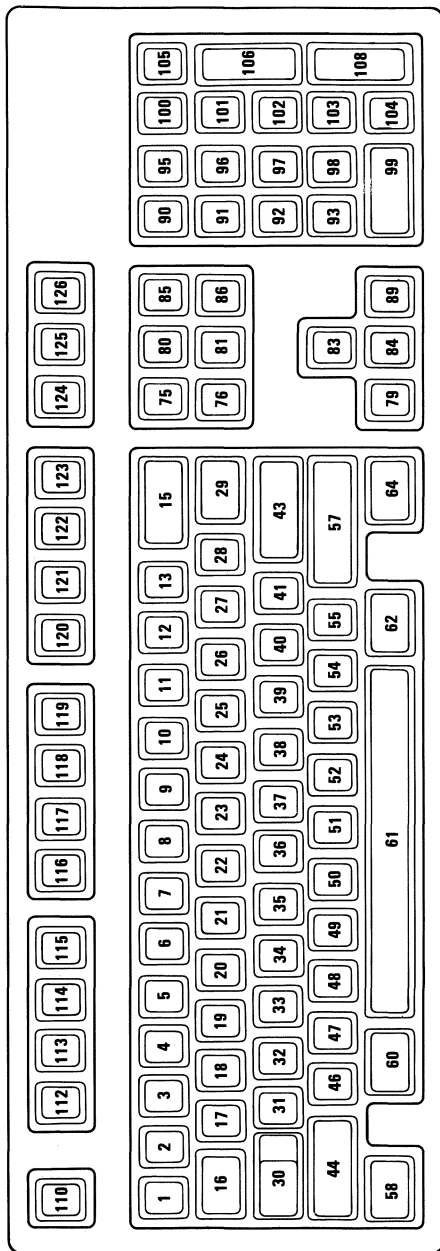
which resides in the read/write memory. This section discusses only the ROM routine.

Extended ASCII encompasses 1-byte character codes, with possible values of 0 to 255, an extended code for certain extended keyboard functions, and functions handled within the keyboard routine or through interrupts.

Character Codes

The character codes described later are passed through the BIOS keyboard routine to the system or application program. A "-1" means the combination is suppressed in the keyboard routine. The codes are returned in the AL register. See "Characters, Keystrokes, and Color" later in this manual for the exact codes.

The following figure shows the keyboard layout and key positions.



SECTION 4

Key	Base Case	Uppercase	Ctrl	Alt
1	'	~	-1	(*)
2	1	!	-1	(*)
3	2	@	Nul(000) (*)	(*)
4	3	#	-1	(*)
5	4	\$	-1	(*)
6	5	%	-1	(*)
7	6	^	RS(030)	(*)
8	7	&	-1	(*)
9	8	*	-1	(*)
10	9	(-1	(*)
11	0)	-1	(*)
12	-	_	US(031)	(*)
13	=	+	-1	(*)
15	Backspace (008)	Backspace (008)	Del(127)	(*)
16	→ (009)	← (*)	(*)	(*)
17	q	Q	DC1(017)	(*)
18	w	W	ETB(023)	(*)
19	e	E	ENQ(005)	(*)
20	r	R	DC2(018)	(*)
21	t	T	DC4(020)	(*)
22	y	Y	EM(025)	(*)
23	u	U	NAK(021)	(*)
24	i	I	HT(009)	(*)
25	o	O	SI(015)	(*)
26	p	P	DLE(016)	(*)
27	{	{	Esc(027)	(*)
28	}	}	GS(029)	(*)
29	\		FS(028)	(*)
30	Caps Lock	-1	-1	-1
31	a	A	SOH(001)	(*)
32	s	S	DC3(019)	(*)
33	d	D	EOT(004)	(*)
34	f	F	ACK(006)	(*)
35	g	G	BEL(007)	(*)
36	h	H	BS(008)	(*)
37	j	J	LF(010)	(*)
38	k	K	VT(011)	(*)
39	l	L	FF(012)	(*)
40	;	;	-1	(*)
41	'	'	-1	(*)
43	CR	CR	LF(010)	(*)
44	Shift (Left)	-1	-1	-1
46	z	Z	SUB(026)	(*)
47	x	X	CAN(024)	(*)
48	c	C	ETX(003)	(*)

Notes:
(*) Refer to "Extended Functions" in this section.

Character Codes (Part 1 of 2)

Key	Base Case	Uppercase	Ctrl	Alt
49	v	V	SYN(022)	(*)
50	b	B	STX(002)	(*)
51	n	N	SO(014)	(*)
52	m	M	CR(013)	(*)
53	,	<	-1	(*)
54	.	>	-1	(*)
55	/	?	-1	(*)
57 Shift (Right)	-1	-1	-1	-1
58 Ctrl (Left)	-1	-1	-1	-1
60 Alt (Left)	-1	-1	-1	-1
61	Space	Space	Space	Space
62 Alt (Right)	-1	-1	-1	-1
64 Ctrl (Right)	-1	-1	-1	-1
90 Num Lock	-1	-1	-1	-1
95	/	/	(*)	(*)
100	*	*	(*)	(*)
105	-	-	(*)	(*)
106	+	+	(*)	(*)
108	Enter	Enter	LF(010)	(*)
110	Esc	Esc	Esc	(*)
112	Null (*)	Null (*)	Null (*)	Null (*)
113	Null (*)	Null (*)	Null (*)	Null (*)
114	Null (*)	Null (*)	Null (*)	Null (*)
115	Null (*)	Null (*)	Null (*)	Null (*)
116	Null (*)	Null (*)	Null (*)	Null (*)
117	Null (*)	Null (*)	Null (*)	Null (*)
118	Null (*)	Null (*)	Null (*)	Null (*)
119	Null (*)	Null (*)	Null (*)	Null (*)
120	Null (*)	Null (*)	Null (*)	Null (*)
121	Null (*)	Null (*)	Null (*)	Null (*)
122	Null (*)	Null (*)	Null (*)	Null (*)
123	Null (*)	Null (*)	Null (*)	Null (*)
125 Scroll Lock	-1	-1	-1	-1
126	Pause(**)	Pause(**)	Break(**)	Pause(**)

Notes:
 (*) Refer to "Extended Functions" in this section.
 (**) Refer to "Special Handling" in this section.

SECTION 4

Character Codes (Part 2 of 2)

The following table lists keys that have meaning only in Num Lock, Shift, or Ctrl states. The Shift key temporarily reverses the current Num Lock state.

Key	Num Lock	Base Case	Alt	Ctrl
91	7	Home (*)	-1	Clear Screen
92	4	← (*)	-1	Reverse Word(*)
93	1	End (*)	-1	Erase to EOL(*)
96	8	↑ (*)	-1	(*)
97	5	(*)	-1	(*)
98	2	↓ (*)	-1	(*)
99	0	Ins	-1	(*)
101	9	Page Up (*)	-1	Top of Text and Home
102	6	→ (*)	-1	Advance Word (*)
103	3	Page Down (*)	-1	Erase to EOS (*)
104	.	Delete (*, **)	(**)	(**)

Notes:
 (*) Refer to "Extended Functions" in this section.
 (**) Refer to "Special Handling" in this section.

Special Character Codes

Extended Functions

For certain functions that cannot be represented by a standard ASCII code, an extended code is used. A character code of 000 (null) is returned in AL. This indicates that the system or application program should examine a second code, which will indicate the actual function. Usually, but not always, this second code is the scan code of the primary key that was pressed. This code is returned in AH.

The following table is a list of the extended codes and their functions.

Second Code	Function
1	Alt Esc
3	Nul Character
14	Alt Backspace
15	← (Back-tab)
16-25	Alt Q, W, E, R, T, Y, U, I, O, P
26-28	Alt [] ←
30-38	Alt A, S, D, F, G, H, J, K, L
39-41	Alt ;
43	Alt \
44-50	Alt Z, X, C, V, B, N, M
51-53	Alt , . /
55	Alt Keypad *
59-68	F1 to F10 Function Keys (Base Case)
71	Home
72	↑ (Cursor Up)
73	Page Up
74	Alt Keypad -
75	← (Cursor Left)
76	Center Cursor
77	→ (Cursor Right)
78	Alt Keypad +
79	End
80	↓ (Cursor Down)
81	Page Down
82	Ins (Insert)
83	Del (Delete)
84-93	Shift F1 to F10
94-103	Ctrl F1 to F10
104-113	Alt F1 to F10
114	Ctrl PrtSc (Start/Stop Echo to Printer)
115	Ctrl ← (Reverse Word)
116	Ctrl → (Advance Word)
117	Ctrl End (Erase to End of Line-EOL)
118	Ctrl PgDn (Erase to End of Screen-EOS)
119	Ctrl Home (Clear Screen and Home)
120-131	Alt 1, 2, 3, 4, 5, 6, 7, 8, 9, 0, -, = keys 2-13
132	Ctrl PgUp (Top 25 Lines of Text and Cursor Home)
133-134	F11, F12
135-136	Shift F11, F12
137-138	Ctrl F11, F12
139-140	Alt F11, F12
141	Ctrl Up/8
142	Ctrl Keypad -
143	Ctrl Keypad 5
144	Ctrl Keypad +
145	Ctrl Down/2
146	Ctrl Ins/0
147	Ctrl Del/.
148	Ctrl Tab
149	Ctrl Keypad /
150	Ctrl Keypad *

Keyboard Extended Functions (Part 1 of 2)

Second Code	Function	
151	Alt	Home
152	Alt	Up
153	Alt	Page Up
155	Alt	Left
157	Alt	Right
159	Alt	End
160	Alt	Down
161	Alt	Page Down
162	Alt	Insert
163	Alt	Delete
164	Alt	Keypad /
165	Alt	Tab
166	Alt	Enter

Keyboard Extended Functions (Part 2 of 2)

Shift States

Most shift states are handled within the keyboard routine, and are not apparent to the system or application program. In any case, the current status of active shift states is available by calling an entry point in the BIOS keyboard routine. The following keys result in altered shift states:

Shift: This key temporarily shifts keys 1 through 13, 16 through 29, 31 through 41, and 46 through 55, to uppercase (base case if in Caps Lock state). Also, the Shift temporarily reverses the Num Lock or non-Num Lock state of keys 91 through 93, 96, 98, 99, and 101 through 104.

Ctrl: This key temporarily shifts keys 3, 7, 12, 15 through 29, 31 through 39, 43, 46 through 52, 75 through 89, 91 through 93, 95 through 108, 112 through 124 and 126 to the Ctrl state. The Ctrl key is also used with the Alt and Del keys to cause the system-reset function; with the Scroll Lock key to cause the break function; and with the Num Lock key to cause the pause function. The system-reset, break, and pause functions are described under "Special Handling" later in this section.

Alt: This key temporarily shifts keys 1 through 29, 31 through 43, 46 through 55, 75 through 89, 95, 100, and 105 through 124 to the Alt state. The Alt key is also used with the Ctrl and Del keys to cause a system reset.

The Alt key also allows the user to enter any character code from 0 to 255. The user holds down the Alt key and types the decimal value of the characters desired on the numeric keypad (keys 91 through 93, 96 through 99, and 101 through 103). The Alt key is then released. If the number is greater than 255, a modulo-256 value is used. This value is interpreted as a character code and is sent through the keyboard routine to the system or application program. Alt is handled internal to the keyboard routine.

Caps Lock: This key shifts keys 17 through 26, 31 through 39, and 46 through 52 to uppercase. When Caps Lock is pressed again, it reverses the action. Caps Lock is handled internal to the keyboard routine.

Scroll Lock: When interpreted by appropriate application programs, this key indicates that the cursor-control keys will cause windowing over the text rather than moving the cursor. When the Scroll Lock key is pressed again, it reverses the action. The keyboard routine simply records the current shift state of the Scroll Lock key. It is the responsibility of the application program to perform the function.

Num Lock: This key shifts keys 91 through 93, 96 through 99, and 101 through 104 to uppercase. When Num Lock is pressed again, it reverses the action. Num Lock is handled internal to the keyboard routine.

Shift Key Priorities and Combinations: If combinations of the Alt, Ctrl, and Shift keys are pressed and only one is valid, the priority is as follows: the Alt key is first, the Ctrl key is second, and the Shift key is third. The only valid combination is Alt and Ctrl, which is used in the system-reset function.

Special Handling

System Reset

The combination of any Alt, Ctrl, and Del keys results in the keyboard routine that starts a system reset or restart. System reset is handled by BIOS.

Break

The combination of the Ctrl and Pause/Break keys results in the keyboard routine signaling interrupt hex 1B. The extended characters AL=hex 00, and AH=hex 00 are also returned.

Pause

The Pause key causes the keyboard interrupt routine to loop, waiting for any character or function key to be pressed. This provides a method of temporarily suspending an operation, such as listing or printing, and then resuming the operation. The method is not apparent to either the system or the application program. The key stroke used to resume operation is discarded. Pause is handled internal to the keyboard routine.

Print Screen

The Print Screen key results in an interrupt invoking the print-screen routine. This routine works in the alphameric or graphics mode, with unrecognizable characters printing as blanks.

System Request

When the System Request (Alt and Print Screen) key is pressed, a hex 8500 is placed in AX, and an interrupt hex 15 is executed. When the Sys Req key is released, a hex 8501 is placed in AX, and another interrupt hex 15 is executed. If an application is to use System Request, the following rules must be observed:

Save the previous address.

Overlay interrupt vector hex 15.

Check AH for a value of hex 85:

If yes, process may begin.

If no, go to previous address.

The application program must preserve the value in all registers, except AX, upon return. System Request is handled internal to the keyboard routine.

Other Characteristics

The keyboard routine does its own buffering, and the keyboard buffer is large enough to support entries by a fast typist. However, if a key is pressed when the buffer is full, the key will be ignored and the "alarm" will sound.

The keyboard routine also suppresses the typematic action of the following keys: Ctrl, Shift, Alt, Num Lock, Scroll Lock, Caps Lock, and Ins.

During each interrupt hex 09 from the keyboard, an interrupt hex 15, function (AH)=hex 4F is generated by the BIOS after the scan code is read from the keyboard adapter. The scan code is passed in the (AL) register with the carry flag set. This is to allow an operating system to intercept each scan code prior to its being handled by the interrupt hex 09 routine, and have a chance to change or act on the scan code. If the carry flag is changed to 0 on return from interrupt hex 15, the scan code will be ignored by the interrupt handler.

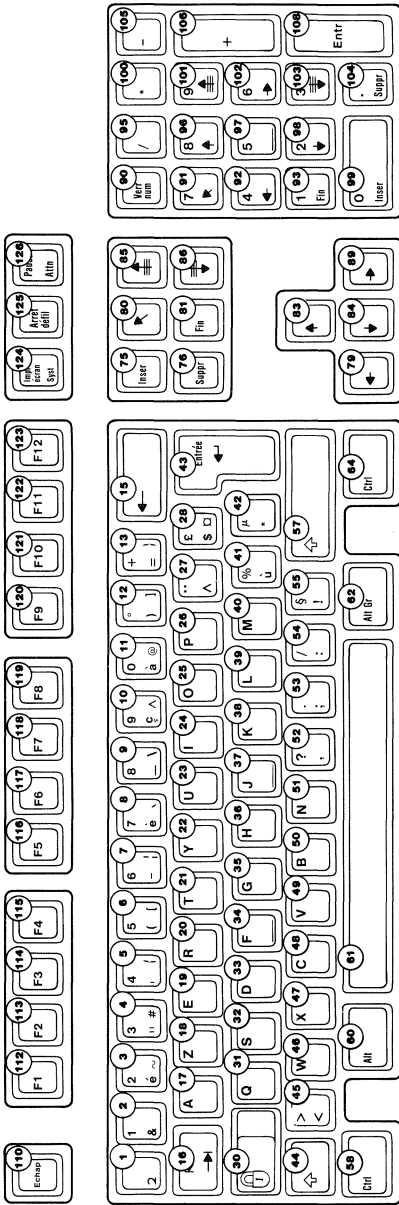
Keyboard Layouts

The keyboard is available in six layouts:

- French
- German
- Italian
- Spanish
- UK English
- US English

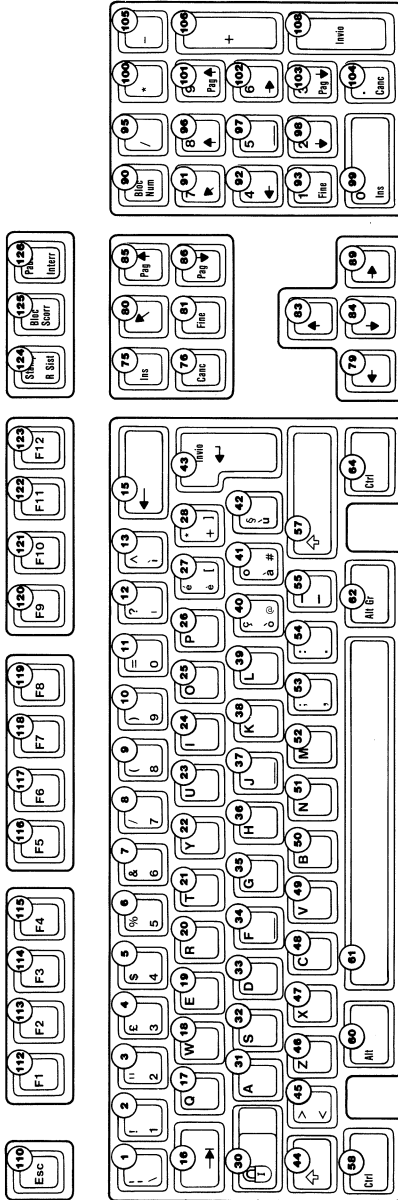
The various layouts are shown in alphabetic order on the following pages. Nomenclature is on both the top and front face of the keybuttons. The number to the upper right designates the keybutton position.

French Keyboard



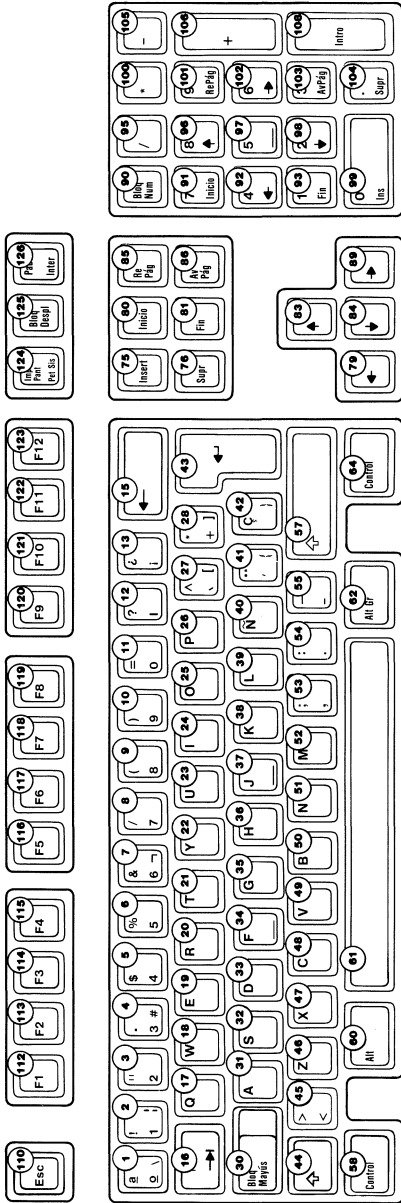
SECTION 4

Italian Keyboard

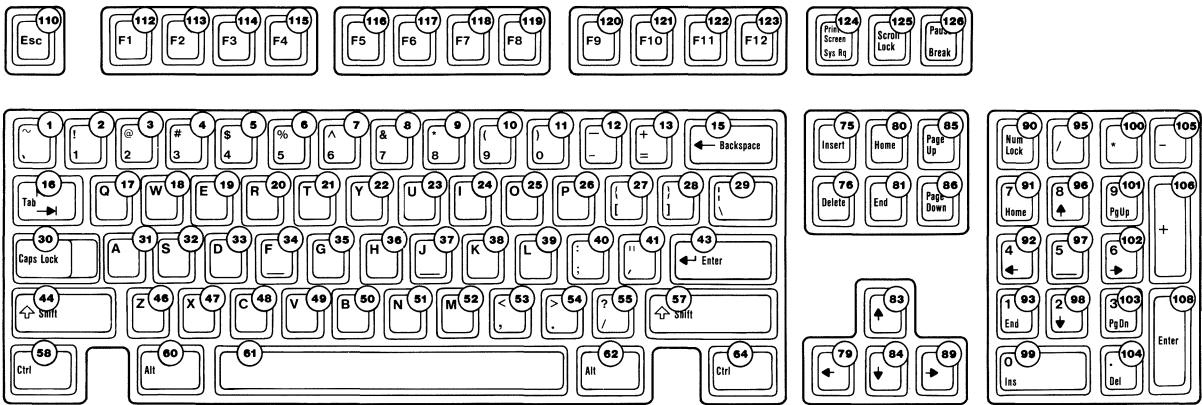


SECTION 4

Spanish Keyboard

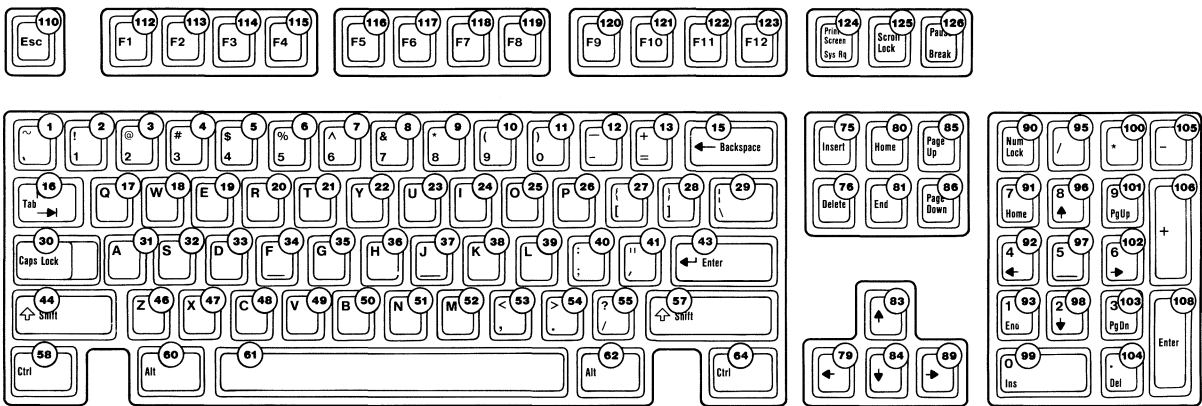


UK English Keyboard



SECTION 4

US English Keyboard



Specifications

The specifications for the keyboard follow.

Power Requirements

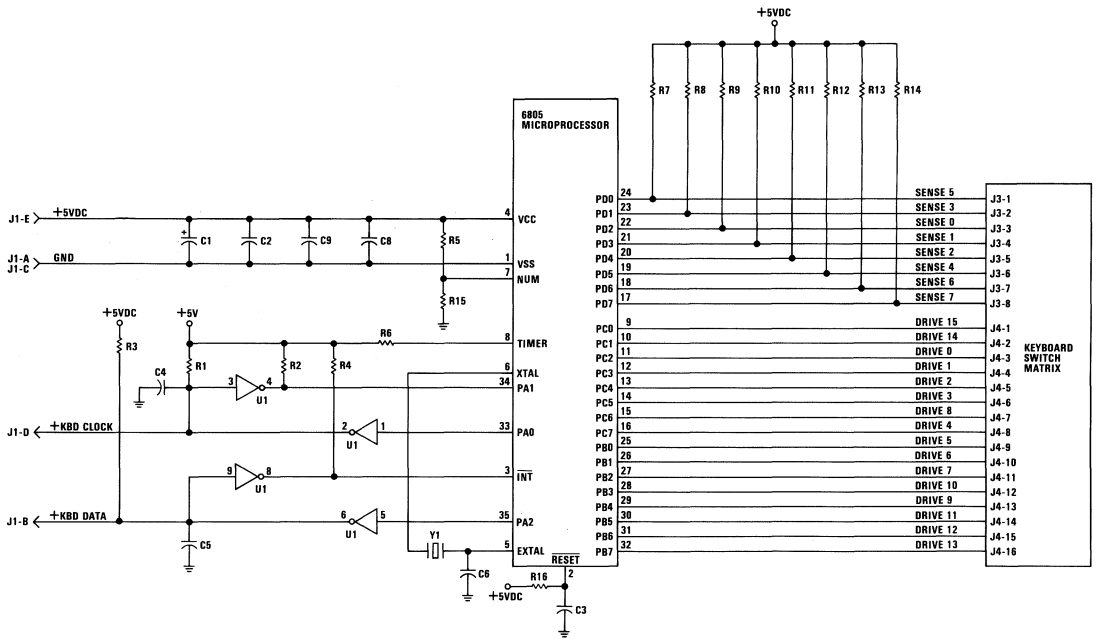
- +5 Vdc \pm 10%
- Current cannot exceed 275 mA.

Size

- Length: 492 millimeters (19.4 inches)
- Depth: 210 millimeters (8.3 inches)
- Height: 58 millimeters (2.3 inches), legs extended

Weight

2.25 kilograms (5.0 pounds)



101/102-KEY KEYBOARD

SECTION 5. SYSTEM BIOS

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Vectors with Special Meanings	5-5
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Quick Reference - 256/640K Board	5-11
System BIOS Listing - 11/8/82	5-111
Quick Reference - 64/256K Board	5-111

Notes:

System BIOS Usage

The basic input/output system (BIOS) resides in ROM on the system board and provides device level control for the major I/O devices in the system. Additional ROM modules may be located on option adapters to provide device level control for that option adapter. (BIOS listings for an option adapter are located in the *Technical Reference Options and Adapters* manual.) BIOS routines enable the assembler language programmer to perform block (disk and diskette) or character-level I/O operations without concern for device address and operating characteristics. System services, such as time-of-day and memory size determination, are provided by the BIOS.

Note: BIOS listings for both the 256/640 and 64/256 system boards are included in this manual.

The goal is to provide an operational interface to the system and relieve the programmer of the concern about the characteristics of hardware devices. The BIOS interface insulates the user from the hardware, thus allowing new devices to be added to the system, yet retaining the BIOS level interface to the device. In this manner, user programs become transparent to hardware modifications and enhancements.

The IBM Personal Computer *Macro Assembler* manual and the IBM Personal Computer *Disk Operating System (DOS)* manual provide useful programming information related to this section. A complete listing of the BIOS is given in this section.

Access to the BIOS is through the 8088 software interrupts. Each BIOS entry point is available through its own interrupt.

The software interrupts, hex 10 through hex 1A, each access a different BIOS routine. For example, to determine the amount of memory available in the system,

INT 12H

invokes the BIOS routine for determining memory size and returns the value to the caller.

Parameter Passing

All parameters passed to and from the BIOS routines go through the 8088 registers. The prologue of each BIOS function indicates the registers used on the call and the return. For the memory size example, no parameters are passed. The memory size, in 1K-byte increments, is returned in the AX register.

If a BIOS function has several possible operations, the AH register is used as input to indicate the desired operation. For example, to set the time of day, the following code is required:

```
MOV AH,1 ;function is to set time of day.
```

```
MOV CX,HIGH_COUNT ;establish the current time.
```

```
MOV DX,LOW_COUNT
```

```
INT 1AH ;set the time.
```

To read the time of day:

```
MOV AH,0 ;function is to read time of day.
```

```
INT 1AH ;read the timer.
```

Generally, the BIOS routines save all registers except for AX and the flags. Other registers are modified on return only if they are returning a value to the caller. The exact register usage is in the prologue of each BIOS function.

Int	Address	Name	BIOS Entry
0	0-3	Divide by Zero	D11
1	4-7	Single Step	D11
2	8-B	Nonmaskable	NMI_INT
3	C-F	Breakpoint	D11
4	10-13	Overflow	D11
5	14-17	Print Screen	PRINT_SCREEN
6	18-1B	Reserved	D11
7	1C-1F	Reserved	D11
8	20-23	Timer	TIMER_INT
9	24-27	Keyboard	KB_INT
A	28-2B	Reserved	D11
B	2C-2F	Communications	D11
C	30-33	Communications	D11

8088 Software Interrupt Listing (Part 1 of 2)

Int	Address	Name	BIOS Entry
D	34-37	Alternate Printer	D11
E	38-3B	Diskette	DISK_INT
F	3C-3F	Printer	D11
10	40-43	Video	VIDEO_IO
11	44-47	Equipment Check	EQUIPMENT
12	48-4B	Memory	MEMORY_SIZE DETERMINE
13	4C-4F	Diskette	DISKETTE_IO
14	50-53	Communications	RS232_IO
15	54-57	Cassette	CASSETTE_IO
16	58-5B	Keyboard	KEYBOARD_IO
17	5C-5F	Printer	PRINTER_TO
18	60-63	Resident BASIC	F600:0000
19	64-67	Bootstrap	BOOTSTRAP
1A	68-6B	Time of Day	TIME OF DAY
1B	6C-6F	Keyboard Break	DUMMY_RETURN
1C	70-73	Timer Tick	DUMMY_RETURN
1D	74-77	Video Initialization	VIDEO_PARS
1E	78-7B	Diskette Parameters	DISK_BASE
1F	7C-7F	Video Graphics Chars	0
40	100-103	Diskette pointer save area for Fixed Disk	
41	104-107	Fixed Disk Parameters	FD_TBL
5A	168-16B	Cluster	D000:XXXX
5B	16C-16F	Used by Cluster Program	
60-67	180-19F	Reserved for User Programs	

8088 Software Interrupt Listing (Part 2 of 2)

Note: For BIOS index, see the BIOS Quick Reference on page 5-11 or 5-111.

Vectors with Special Meanings

Interrupt Hex 1B - Keyboard Break Address

This vector points to the code to be used when the Ctrl and Break keys are pressed on the keyboard. The vector is invoked while responding to the keyboard interrupt, and control should be returned through an IRET instruction. The power-on routines initialize this vector to an IRET instruction, so that nothing will occur when the Ctrl and Break keys are pressed unless the application program sets a different value.

Control may be retained by this routine, with the following problems. The Break may have occurred during interrupt

processing, so that one or more End of Interrupt commands must be sent to the 8259 Controller. Also, all I/O devices should be reset in case an operation was underway at that time.

Interrupt Hex 1C - Timer Tick

This vector points to the code to be executed on every system-clock tick. This vector is invoked while responding to the timer interrupt, and control should be returned through an IRET instruction. The power-on routines initialize this vector to point to an IRET instruction, so that nothing will occur unless the application modifies the pointer. It is the responsibility of the application to save and restore all registers that will be modified.

Interrupt Hex 1D - Video Parameters

This vector points to a data region containing the parameters required for the initialization of the 6845 on the video card. Note that there are four separate tables, and all four must be reproduced if all modes of operation are to be supported. The power-on routines initialize this vector to point to the parameters contained in the ROM video routines.

Interrupt Hex 1E - Diskette Parameters

This vector points to a data region containing the parameters required for the diskette drive. The power-on routines initialize the vector to point to the parameters contained in the ROM diskette routine. These default parameters represent the specified values for any IBM drives attached to the system. Changing this parameter block may be necessary to reflect the specifications of the other drives attached.

Interrupt Hex 1F - Graphics Character Extensions

When operating in the graphics modes of the IBM Color/Graphics Monitor Adapter (320 by 200 or 640 by 200), the read/write character interface forms the character from the ASCII code point, using a set of dot patterns. The dot patterns for the first 128 code points are contained in ROM. To access the second 128 code points, this vector must be established to point at a table of up to 1K bytes, where each code point is represented by eight bytes of graphic information. At power-on, this vector is initialized to 000:0, and it is the responsibility of the user to change this vector if additional code points are required.

Interrupt Hex 40 - Reserved

When an IBM Fixed Disk Adapter is installed, the BIOS routines use interrupt hex 30 to revector the diskette pointer.

Interrupt Hex 41 - Fixed Disk Parameters

This vector points to a data region containing the parameters required for the fixed disk drive. The power-on routines initialize the vector to point to the parameters contained in the ROM disk routine. These default parameters represent the specified values for any IBM fixed disk drives attached to the system. Changing this parameter block may be necessary to reflect the specifications of the other fixed disk drives attached.

Other Read/Write Memory Usage

The IBM BIOS routines use 256 bytes of memory from absolute hex 400 to hex 4FF. Locations hex 400 to hex 407 contain the base addresses of any RS-232C cards attached to the system. Locations hex 408 to hex 40F contain the base addresses of the Printer Adapter.

Memory locations hex 300 to hex 3FF are used as a stack area during the power-on initialization, and bootstrap when control is passed to it from power-on. If the user desires the stack in a different area, the area must be set by the application.

Interrupt	Address	Function
20	80-83	DOS program terminate
21	84-87	DOS function call
22	88-8B	DOS terminate address
23	8C-8F	DOS Ctrl Break exit address
24	90-93	DOS fatal error vector
25	94-97	DOS absolute disk read
26	98-9B	DOS absolute disk write
27	9C-9F	DOS terminate, fix in storage
28-3F	A0-FF	Reserved for DOS
40-5F	100-17F	Reserved for BIOS
60-67	180-19F	Reserved for user program interrupts
68-6F	1A0-1BF	Not used
80-85	200-217	Reserved for BASIC
86-F0	218-3C3	Used by BASIC interpreter while BASIC is running
F1-FF	3C4-3FF	Not used

Hardware, Basic, and DOS Interrupts

Address	Mode	Function
400-4A1	ROM BIOS	See BIOS listing
4A2-4EF		Reserved
4F0-4FF		Reserved as intra-application communication area for any application
500-5FF		Reserved for DOS and BASIC
500	DOS	Print screen status flag store 0=Print screen not active or successful print screen operation 1=Print screen in progress 255=Error encountered during print screen operation
504	DOS	Single drive mode status byte
510-511	BASIC	BASIC's segment address store
512-515	BASIC	Clock interrupt vector segment:offset store
516-519	BASIC	Break key interrupt vector segment:offset store
51A-51D	BASIC	Disk error interrupt vector segment:offset store

Reserved Memory Locations

If you do DEF SEG (Default workspace segment):

Offset	Length	
2E	2	Line number of current line being executed
347	2	Line number of last error
30	2	Offset into segment of start of program text
358	2	Offset into segment of start of variables (end of program text 1-1)
6A	1	Keyboard buffer contents 0=No characters in buffer 1=Characters in buffer
4E	1	Character color in graphics mode*

* Set to 1, 2, or 3 to get text in colors 1-3.
Do not set to 0. The default is 3.

Basic Workspace Variables

Example

```
100 PRINT PEEK (&H2E) + 256 x PEEK (&H2F)
```

L	H
Hex 64	Hex 00

Starting Address	
00000	BIOS interrupt vectors
00080	Available interrupt vectors
00400	BIOS data area
00500	User read/write memory
C8000	Disk Adapter
F0000	Read only memory
FE000	BIOS program area

BIOS Memory Map

BIOS Programming Hints

The BIOS code is invoked through software interrupts. The programmer should not “hard code” BIOS addresses into application programs. The internal workings and absolute addresses within BIOS are subject to change without notice.

If an error is reported by the disk or diskette code, you should reset the drive adapter and retry the operation. A specified number of retries should be required on diskette reads to ensure the problem is not due to motor startup.

When altering I/O-port bit values, the programmer should change only those bits that are necessary to the current task. Upon completion, the programmer should restore the original environment. Failure to adhere to this practice may be incompatible with present and future applications.

Adapter Cards with System-Accessible ROM Modules

The ROM BIOS provides a facility to integrate adapter cards with on-board ROM code into the system. During the POST, interrupt vectors are established for the BIOS calls. After the default vectors are in place, a scan for additional ROM modules takes place. At this point, a ROM routine on the adapter card may gain control. The routine may establish or intercept interrupt vectors to hook themselves into the system.

The absolute addresses hex C8000 through hex F4000 are scanned in 2K blocks in search of a valid adapter card ROM. A valid ROM is defined as follows:

- Byte 0:** Hex 55
- Byte 1:** Hex AA
- Byte 2:** A length indicator representing the number of 512-byte blocks in the ROM (length/512). A checksum is also done to test the integrity of the ROM module. Each byte in the defined ROM is summed modulo hex 100. This sum must be 0 for the module to be deemed valid.

When the POST identifies a valid ROM, it does a far call to byte 3 of the ROM (which should be executable code). The adapter card may now perform its power-on initialization tasks. The feature ROM should return control to the BIOS routines by executing a far return.

System BIOS Listing - 01/10/86

Quick Reference - 256/640K Board

Map	5-13
Header	5-14
EQUATES	5-15
ABS0	5-19
DATA Segment	5-20
Diskette	5-23
INT 13H	5-23
Drive Type	5-25
Diskette__IO__1	5-25
DMA__Setup	5-36
Motor__On	5-40
Disk__Int	5-44
Diskette__Setup	5-45
Keyboard BIOS	5-46
Scan Tables	5-56
Printer BIOS	5-57
RS232 BIOS	5-59
Video BIOS	5-62
BIOS1	5-80
INT 15H	5-80
Joystick Support	5-82
POST	5-84
Determine Configuration	5-87
8259 Test	5-89
Keyboard Test	5-90
Expansion Test	5-91
Boot__Strap (INT 19H)	5-94
Time__of__Day (INT 1AH)	5-95
Beep	5-96

STGTST_CNT	5-97
Disk_Base	5-99
NMI	5-100
DDS	5-103
Timer_Int	5-103
Character Generator	5-104
D11	5-107
Print Screen	5-108

Address	Publics by Name
F000:E729	A1
F000:15CC	ACT_DISP_PAGE
F000:1600	BASIC
F000:EC5C	BEEP
F000:1C4F	CASSETTE_IO_1
F000:E73C	CONF_TBL
F000:FA6E	CRT_CHAR_GEN
F000:FA12	DDS
F000:0062	DISKETTE_IO_1
F000:EF71	DISK_BASE
F000:08C4	DISK_INT_1
F000:08DB	DSKETTE_SETUP
F000:1D37	FILL
F000:1000	HEADER
F000:0D78	KB_INT_1
F000:0C57	KEYBOARD_IO_1
F000:F0E4	M5
F000:F0EC	M6
F000:F0F4	M7
F000:EF79	MD_TBL1
F000:EF86	MD_TBL2
F000:EF93	MD_TBL3
F000:EFA0	MD_TBL4
F000:EFAD	MD_TBL5
F000:EFBA	MD_TBL6
F000:0A40	NEC_OUTPUT
F000:12BE	PRINTER_IO_1
F000:FFFF0	P_O_R
F000:1725	READ_AC_CURRENT
F000:15B5	READ_CURSOR
F000:1865	READ_DOT
F000:1BAB	READ_LPEN
F000:E05B	RESET
F000:0B32	RESULTS
F000:1344	RS232_IO_1
F000:16D3	SCROLL_DOWN
F000:1635	SCROLL_UP
F000:0A64	SEEK
F000:15EE	SET_COLOR
F000:158D	SET_CPOS
F000:156C	SET_CTYPE
F000:1485	SET_MODE
F000:144E	VIDEO_IO_1
F000:FOA4	VIDEO_PARMS
F000:1563	VIDEO_RETURN
F000:1614	VIDEO_STATE
F000:ECA0	WAITF
F000:1782	WRITE_AC_CURRENT
F000:17B4	WRITE_C_CURRENT
F000:1876	WRITE_DOT
F000:17E1	WRITE_STRING
F000:1B24	WRITE_TTY

Address	Publics by Value
F000:0000	HEADER
F000:0062	DISKETTE_IO_1
F000:0A40	NEC_OUTPUT
F000:0A64	SEEK
F000:0B32	RESULTS
F000:08C4	DISK_INT_1
F000:08DB	DSKETTE_SETUP
F000:0C57	KEYBOARD_IO_1
F000:0D78	KB_INT_1
F000:129E	PRINTER_IO_1
F000:1344	RS232_IO_1
F000:144E	VIDEO_IO_1
F000:1485	SET_MODE
F000:1563	VIDEO_RETURN
F000:156C	SET_CTYPE
F000:158D	SET_CPOS
F000:15B5	READ_CURSOR
F000:15CC	ACT_DISP_PAGE
F000:15EE	SET_COLOR
F000:1614	VIDEO_STATE
F000:1635	SCROLL_UP
F000:16D3	SCROLL_DOWN
F000:1725	READ_AC_CURRENT
F000:1782	WRITE_AC_CURRENT
F000:17B4	WRITE_C_CURRENT
F000:17E1	WRITE_STRING
F000:1865	READ_DOT
F000:1876	WRITE_DOT
F000:1B24	WRITE_TTY
F000:1BAB	READ_LPEN
F000:1C4F	CASSETTE_IO_1
F000:1D37	FILL
F000:1600	BASIC
F000:E05B	RESET
F000:E729	A1
F000:E73C	CONF_TBL
F000:E05C	BEEP
F000:ECA0	WAITF
F000:EF79	MD_TBL1
F000:EF86	MD_TBL2
F000:EF93	MD_TBL3
F000:EFA0	MD_TBL4
F000:EFAD	MD_TBL5
F000:EFBA	MD_TBL6
F000:EF71	DISK_BASE
F000:FOA4	VIDEO_PARMS
F000:F0E4	M5
F000:F0EC	M6
F000:F0F4	M7
F000:FA12	DDS
F000:FA6E	CRT_CHAR_GEN
F000:FFFF0	P_O_R

SECTION 5

PAGE 118,121
TITLE HEADER --- 01/08/86 POWER ON SELF TEST (POST)

BIOS I/O INTERFACE

THESE LISTINGS PROVIDE INTERFACE INFORMATION FOR ACCESSING
THE BIOS ROUTINES. THE POWER ON SELF TEST IS INCLUDED.

THE BIOS ROUTINES ARE MEANT TO BE ACCESSED THROUGH
SOFTWARE INTERRUPTS ONLY. ANY ADDRESSES PRESENT IN
THESE LISTINGS ARE INCLUDED ONLY FOR COMPLETENESS,
NOT FOR REFERENCE. APPLICATIONS WHICH REFERENCE ANY
ABSOLUTE ADDRESSES WITHIN THE CODE SEGMENTS OF BIOS
VIOLATE THE STRUCTURE AND DESIGN OF BIOS.

MODULE REFERENCE

HEADER.ASM --> DEFINITIONS
DSEG.INC --> DATA SEGMENT LOCATIONS
POSTEQU.INC --> COMMON EQUATES FOR POST AND BIOS
DSKETTE.ASM --> DISKETTE BIOS
 DISKETTE_IO_I - INT 13H BIOS ENTRY (40H) -INT 13H
 DISK_INT_I - HARDWARE INTERRUPT HANDLER -INT 0EH
 DSKETTE_SETUP - POST SETUP DRIVE TYPES
KEYBRD.ASM --> KEYBOARD BIOS
 KEYBOARD_IO_I - INT 16H BIOS ENTRY -INT 16H
 KB_INT_I - HARDWARE INTERRUPT -INT 09H
 SND_DATA - KEYBOARD TRANSMISSION
PRT.ASM --> PRINTER ADAPTER BIOS -INT 17H
RS232.ASM --> COMMUNICATIONS BIOS FOR RS232 -INT 14H
VIDEO.ASM --> VIDEO BIOS -INT 10H
BIOS1.ASM --> INTERRUPT 15H BIOS ROUTINES -INT 15H
 DEV_OPEN - NULL DEVICE OPEN HANDLER
 DEV_CLOSE - NULL DEVICE CLOSE HANDLER
 PRG_TERM - NULL PROGRAM TERMINATION
 JOY_STICK - JOYSTICK PORT HANDLER
 SYS_REQ - NULL SYSTEM REQUEST KEY
 EXT_MEMORY - EXTENDED MEMORY SIZE DETERMINE
 DEVCE_BUSY - NULL DEVICE BUSY HANDLER
 INT_COMPLETE - NULL INTERRUPT COMPLETE HANDLER
POST.ASM --> BIOS INTERRUPT ROUTINES
 POST - POWER ON SELF TEST & INITIALIZATION
 TIME_OF_DAY_I - TIME OF DAY ROUTINES -INT 1AH
 PRINT_SCREEN_I - PRINT SCREEN ROUTINE -INT 05H
 TIMER_INT_I - TIMER1 INTERRUPT HANDLER ->INT 1CH
 DDS - LOAD (DS:) WITH DATA SEGMENT
 BEEP - SPEAKER BEEP CONTROL ROUTINE
 WAITF - FIXED TIME WAIT ROUTINE

.LIST

```

66                                     PAGE
67 C INCLUDE POSTEQU.INC
68 C ;-----
69 C ; EQUATES USED BY POST AND BIOS ;
70 C ;-----
71 C
72 = 0000 C SYSTEM EQU 0 ; 0 PC-XT, 1 PC-AT
73 = 00F8 C MODEL_BYTE EQU 0FBH ; SYSTEM MODEL BYTE
74 = 0000 C SUB_MODEL_BYTE EQU 000H ; SYSTEM SUB-MODEL TYPE
75 = 0001 C BIOS_LEVEL EQU 001H ; BIOS REVISION LEVEL
76 C ENDIF
77 C
78 C ;----- KEYBOARD INTERFACE AND DIAGNOSTIC CONTROL REGISTERS -----
79 C PORT_A EQU 060H ; KEYBOARD SCAN CODE/CONTROL PORT
80 = 0061 C PORT_B EQU 061H ; PORT B READ/WRITE DIAGNOSTIC REGISTER
81 = 0062 C PORT_C EQU 062H ; 8255 PORT C ADDR
82 = 0063 C CMD_PORT EQU 063H
83 C
84 C
85 = 0060 C KB_DATA EQU 60H ; KEYBOARD SCAN CODE PORT
86 = 0061 C KB_CTL EQU 61H ; CONTROL BITS FOR KEYBOARD SENSE DATA
87 = 0054 C ID_2A EQU 054H ; ALTERNATE 2ND ID CHAR FOR KBX
88 = 00E0 C MC_E0 EQU 224 ; GENERAL MARKER CODE
89 = 00E1 C MC_E1 EQU 225 ; PAUSE KEY MARKER CODE
90 C
91 C
92 = 00F3 C RAM_PAR_ON EQU 11110011B ; AND MASK FOR PARITY CHECKING ENABLE ON
93 = 000C C RAM_PAR_OFF EQU 00001100B ; OR MASK FOR PARITY CHECKING ENABLE OFF
94 = 00C0 C PARTY_ERR EQU 11000000B ; R/W MEMORY - I/O CHANNEL PARITY ERROR
95 = 0001 C GATE2 EQU 00000001B ; TIMER 2 INPUT GATE CLOCK BIT
96 = 0002 C SPK2 EQU 00000010B ; SPEAKER OUTPUT DATA ENABLE BIT
97 = 0010 C REFRESH_BIT EQU 00010000B ; REFRESH TEST BIT
98 = 0020 C OUT2 EQU 00100000B ; SPEAKER TIMER OUT2 INPUT BIT
99 = 0040 C IO_CHECK EQU 01000000B ; I/O (MEMORY) CHECK OCCURRED BIT MASK
100 = 0080 C PARITY_CHECK EQU 10000000B ; MEMORY PARITY CHECK OCCURRED BIT MASK
101 C ENDIF

```

```

102 C PAGE
103 C |----- KEYBOARD RESPONSE -----|
104 = 00AA EQU 0AAH ; RESPONSE FROM SELF DIAGNOSTIC
105 = 00FA EQU 0FAH ; ACKNOWLEDGE FROM TRANSMISSION
106 = 00FE EQU 0FEH ; RESEND REQUEST
107 = 00FF EQU 0FFH ; OVER RUN SCAN CODE
108 C
109 C |----- FLAG EQUATES WITHIN *KB_FLAG -----|
110 = 0001 EQU 0000001B ; RIGHT SHIFT KEY DEPRESSED
111 = 0002 EQU 0000010B ; LEFT SHIFT KEY DEPRESSED
112 = 0004 EQU 0000100B ; CONTROL SHIFT KEY DEPRESSED
113 = 0008 EQU 0001000B ; ALTERNATE SHIFT KEY DEPRESSED
114 = 0010 EQU 00010000B ; SCROLL LOCK STATE HAS BEEN TOGGLED
115 = 0020 EQU 00100000B ; NUM LOCK STATE HAS BEEN TOGGLED
116 = 0040 EQU 01000000B ; CAPS LOCK STATE HAS BEEN TOGGLED
117 = 0080 EQU 10000000B ; INSERT STATE IS ACTIVE
118 C
119 C |----- FLAG EQUATES WITHIN *KB_FLAG_1 -----|
120 = 0004 EQU 0000100B ; SYSTEM KEY DEPRESSED AND HELD
121 = 0008 EQU 0001000B ; SUSPEND KEY HAS BEEN TOGGLED
122 = 0010 EQU 00010000B ; SCROLL LOCK KEY IS DEPRESSED
123 = 0020 EQU 00100000B ; NUM LOCK KEY IS DEPRESSED
124 = 0040 EQU 01000000B ; CAPS LOCK KEY IS DEPRESSED
125 = 0080 EQU 10000000B ; INSERT KEY IS DEPRESSED
126 C
127 C |----- FLAGS EQUATES WITHIN *KB_FLAG_2 -----|
128 = 0007 EQU 0000111B ; KEYBOARD LED STATE BITS
129 C ; RESERVED (MUST BE ZERO)
130 = 0010 EQU 00010000B ; ACKNOWLEDGMENT RECEIVED
131 = 0020 EQU 00100000B ; RESEND RECEIVED FLAG
132 = 0040 EQU 01000000B ; MODE INDICATOR UPDATE
133 = 0080 EQU 10000000B ; KEYBOARD TRANSMIT ERROR FLAG
134 C
135 C |----- FLAGS EQUATES WITHIN *KB_FLAG_3 -----|
136 = 0001 EQU 0000001B ; LAST CODE WAS THE E1 HIDDEN CODE
137 = 0002 EQU 0000010B ; LAST CODE WAS THE E0 HIDDEN CODE
138 = 0004 EQU 0000100B ; RIGHT CTL KEY DOWN
139 = 0008 EQU 0001000B ; ALL GRAPHICS KEY DOWN (W.T. ONLY)
140 C ; RESERVED (MUST BE ZERO)
141 = 0010 EQU 00010000B ; KBX INSTALLED
142 = 0020 EQU 00100000B ; FORCE NUM LOCK IF READ ID AND KBX
143 = 0040 EQU 01000000B ; LAST CHARACTER WAS FIRST ID CHARACTER
144 = 0080 EQU 10000000B ; DOING A READ ID (MUST BE BIT0)
145 C
146 C |----- KEYBOARD SCAN CODES -----|
147 = 00AB EQU 0ABH ; 1ST ID CHARACTER FOR KBX
148 = 0041 EQU 041H ; 2ND ID CHARACTER FOR KBX
149 = 003B EQU 56 ; SCAN CODE FOR ALTERNATE SHIFT KEY
150 = 001D EQU 29 ; SCAN CODE FOR CONTROL KEY
151 = 003A EQU 58 ; SCAN CODE FOR SHIFT LOCK KEY
152 = 0053 EQU 83 ; SCAN CODE FOR DELETE KEY
153 = 0052 EQU 82 ; SCAN CODE FOR INSERT KEY
154 = 002A EQU 42 ; SCAN CODE FOR LEFT SHIFT
155 = 0045 EQU 69 ; SCAN CODE FOR NUMBER LOCK KEY
156 = 0036 EQU 54 ; SCAN CODE FOR RIGHT SHIFT
157 = 0046 EQU 70 ; SCAN CODE FOR SCROLL LOCK KEY
158 = 0054 EQU 84 ; SCAN CODE FOR SYSTEM KEY
159 = 0057 EQU 87 ; F11 KEY MAKE
160 = 0058 EQU 88 ; F12 KEY MAKE

```

```

161 C PAGE
162 C ENDIF
163 C
164 C ;----- DISKETTE EQUATES -----
165 = 0050 C CARD_ID EQU 01010000B ; CONTROLLER CARD I.D. BIT
166 = 0001 C DUAL EQU 00000001B ; MASK FOR FDC ADAPTER I.D.
167 = 0080 C INT_FLAG EQU 10000000B ; INTERRUPT OCCURRENCE FLAG
168 = 0080 C DSK_CHG EQU 10000000B ; DISKETTE CHANGE FLAG MASK BIT
169 = 0010 C DETERMINED EQU 00010000B ; SET STATE DETERMINED IN STATE BITS
170 = 0010 C HOME EQU 00010000B ; TRACK 0 MASK
171 = 0004 C SENSE_DRV_ST EQU 00000100B ; SENSE DRIVE STATUS COMMAND
172 = 0030 C TRK_SLAP EQU 030H ; CRASH_STOP (48 TPI DRIVES)
173 = 000A C QUIET_SEEK EQU 00AH ; SEEK TO TRACK 10
174 = 0002 C MAX_DRV EQU 2 ; MAX NUMBER OF DRIVES
175 = 000F C HD12_SETTLE EQU 15 ; 1.2 M HEAD SETTLE TIME
176 = 0014 C HD320_SETTLE EQU 20 ; 320 K HEAD SETTLE TIME
177 = 0025 C MOTOR_WAIT EQU 37 ; 2 SECONDS OF COUNTS FOR MOTOR TURN OFF
178 C
179 C ;----- DISKETTE ERRORS -----
180 = 0080 C TIME_OUT EQU 080H ; ATTACHMENT FAILED TO RESPOND
181 = 0040 C BAD_SEEK EQU 040H ; SEEK OPERATION FAILED
182 = 0020 C BAD_NEC EQU 020H ; DISKETTE CONTROLLER HAS FAILED
183 = 0010 C BAD_CRC EQU 010H ; BAD CRC ON DISKETTE READ
184 = 000C C MED_NOT_FND EQU 00CH ; MEDIA TYPE FOUND
185 = 0009 C DMA_BOUNDARY EQU 009H ; ATTEMPT TO DMA ACROSS 64K BOUNDARY
186 = 0008 C BAD_DMA EQU 008H ; DMA OVERRUN ON OPERATION
187 = 0006 C MEDIA_CHANGE EQU 006H ; MEDIA REMOVED ON DUAL ATTACH CARD
188 = 0004 C RECORD_NOT_FND EQU 004H ; REQUESTED SECTOR NOT FOUND
189 = 0003 C WRITE_PROTECT EQU 003H ; WRITE ATTEMPTED ON WRITE PROTECT DISK
190 = 0002 C BAD_ADDR_MARK EQU 002H ; ADDRESS MARK NOT FOUND
191 = 0001 C BAD_CMD EQU 001H ; BAD COMMAND PASSED TO DISKETTE 1/0
192 C
193 C ;----- DISK CHANGE LINE EQUATES -----
194 = 0001 C NOCHGLN EQU 001H ; NO DISK CHANGE LINE AVAILABLE
195 = 0002 C CHGLN EQU 002H ; DISK CHANGE LINE AVAILABLE
196 C
197 C ;----- MEDIA/DRIVE STATE INDICATORS -----
198 = 0001 C TRK_CAPA EQU 00000001B ; 80 TRACK CAPABILITY
199 = 0002 C FMT_CAPA EQU 00000010B ; MULTIPLE FORMAT CAPABILITY (1.2M)
200 = 0004 C DRV_DET EQU 00000100B ; DRIVE DETERMINED
201 = 0010 C MED_DET EQU 00010000B ; MEDIA DETERMINED BIT
202 = 0020 C DBL_STEP EQU 00100000B ; DOUBLE STEP BIT
203 = 00C0 C RATE_MSK EQU 11000000B ; MASK FOR CLEARING ALL BUT RATE
204 = 0000 C RATE_500 EQU 00000000B ; 500 KBS DATA RATE
205 = 0040 C RATE_300 EQU 01000000B ; 300 KBS DATA RATE
206 = 0080 C RATE_250 EQU 10000000B ; 250 KBS DATA RATE
207 = 000C C STRT_MSK EQU 00001100B ; OPERATION START RATE MASK
208 = 00C0 C SEND_MSK EQU 11000000B ; MASK FOR SEND RATE BITS
209 C
210 C ;----- MEDIA/DRIVE STATE INDICATORS COMPATIBILITY -----
211 = 0000 C M3D3U EQU 00000000B ; 360 MEDIA/DRIVE NOT ESTABLISHED
212 = 0001 C M3D1U EQU 00000001B ; 360 MEDIA,1.2DRIVE NOT ESTABLISHED
213 = 0002 C MID1U EQU 00000010B ; 1.2 MEDIA/DRIVE NOT ESTABLISHED
214 = 0007 C MED_UNK EQU 00000111B ; NONE OF THE ABOVE

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SECTION 5

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215 C PAGE
216 C ----- INTERRUPT EQUATES -----
217 = 0020 C EQU 020H ; END OF INTERRUPT COMMAND TO 8259
218 = 0020 C INTA00 EQU 020H ; 8259 PORT
219 = 0021 C INTA01 EQU 021H ; 8259 PORT
220 = 00A0 C INTB00 EQU 0A0H ; 2ND 8259
221 = 00A1 C INTB01 EQU 0A1H ;
222 = 0070 C INT_TYPE EQU 070H ; START OF 8259 INTERRUPT TABLE LOCATION
223 = 0010 C INT_VIDEO EQU 010H ; VIDEO VECTOR
224 C -----
225 = 0008 C DMA08 EQU 008H ; DMA STATUS REGISTER PORT ADDRESS
226 = 0000 C DMA EQU 000H ; DMA CH.0 ADDRESS REGISTER PORT ADDRESS
227 = 0000 C DMA18 EQU 000H ; 2ND DMA STATUS PORT ADDRESS
228 = 00C0 C DMA1 EQU 0C0H ; 2ND DMA CH.0 ADDRESS REGISTER ADDRESS
229 C -----
230 = 0040 C TIMER EQU 040H ; 8253 TIMER - BASE ADDRESS
231 = 0043 C TIM_CTL EQU 043H ; 8253 TIMER CONTROL PORT ADDR
232 = 0040 C TIMER0 EQU 040H ; 8253 TIMER/CNTR 0 PORT ADDR
233 C -----
234 C MANUFACTURING PORT -----
235 = 0080 C MFG_PORT EQU 80H ; MANUFACTURING AND POST CHECKPOINT PORT
236 C ; DMA CHANNEL 0 PAGE REGISTER ADDRESS
237 C -----
238 C ;----- MANUFACTURING BIT DEFINITION FOR MFG_ERR_FLAG+1 -----
239 = 0001 C MEM_FAIL EQU 00000010B ; STORAGE TEST FAILED (ERROR 20X)
240 = 0002 C PRO_FAIL EQU 00000010B ; VIRTUAL MODE TEST FAILED (ERROR 104)
241 = 0004 C LMCS_FAIL EQU 00000100B ; LOW MEG CHIP SELECT FAILED (ERROR 109)
242 = 0008 C KYCLK_FAIL EQU 00001000B ; KEYBOARD CLOCK TEST FAILED (ERROR 304)
243 = 0010 C KY_SYS_FAIL EQU 00010000B ; KEYBOARD OR SYSTEM TEST FAILED (ERROR 303)
244 = 0020 C KYBD_FAIL EQU 00100000B ; KEYBOARD FAILED (ERROR 301)
245 = 0040 C DSK_FAIL EQU 01000000B ; DISKETTE TEST FAILED (ERROR 601)
246 = 0080 C KEY_FAIL EQU 10000000B ; KEYBOARD LOCKED (ERROR 302)
247 C -----
248 C ;-----
249 = 0081 C DMA_PAGE EQU 081H ; START OF DMA PAGE REGISTERS
250 = 008F C LAST_DMA_PAGE EQU 08FH ; LAST DMA PAGE REGISTER
251 C -----
252 C ;-----
253 C ;X287 EQU 0F0H ; MATH COPROCESSOR CONTROL PORT
254 C -----
255 C ;-----
256 = 0000 C POST_S5 EQU 00000H ; POST STACK SEGMENT
257 = 8000 C POST_SP EQU 80000H ; POST STACK POINTER
258 = 0030 C STACK_S5 EQU 30H ; STACK SEGMENT USED DURING POST
259 = 0100 C TOS EQU 100H ; STACK -- USED DURING POST ONLY
260 C ; USE WILL OVERLAY INTERRUPTS VECTORS
261 C -----
262 C ;-----
263 C ;-----
264 = 000D C CR EQU 000DH ; CARRIAGE RETURN CHARACTER
265 = 000A C LF EQU 000AH ; LINE FEED CHARACTER
266 = 0008 C RVRT EQU 00001000B ; VIDEO VERTICAL RETRACE BIT
267 = 0001 C RHRZ EQU 00000001B ; VIDEO HORIZONTAL RETRACE BIT
268 = 0100 C H EQU 256 ; HIGH BYTE FACTOR (X 100H)
269 = 0101 C X EQU H+1 ; HIGH AND LOW BYTE FACTOR (X 101H)
270 C -----
271 .LIST

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272 PAGE
273 INCLUDE DSEG.INC
274 C ELSE
275 C-----
276 C      8088 INTERRUPT LOCATIONS
277 C      REFERENCED BY POST & BIOS
278 C-----
279 C ENDIF
280
281 0000 ABS0 SEGMENT AT 0 ; ADDRESS= 0000:0000
282
283 0000 ?? *STG_LOC0 DB ? ; START OF INTERRUPT VECTOR TABLE
284
285 0008 ORG 4*002H
286 0008 ???????? *NMI_PTR DD ? ; NON-MASKABLE INTERRUPT VECTOR
287
288 0014 ORG DD 4*005H
289 0014 ???????? *INT5_PTR DD ? ; PRINT SCREEN INTERRUPT VECTOR
290
291 0020 ORG DD 4*008H
292 0020 ???????? *INT_PTR DD ? ; HARDWARE INTERRUPT POINTER (8-F)
293
294 0040 ORG DD 4*010H
295 0040 ???????? *VIDEO_INT DD ? ; VIDEO I/O INTERRUPT VECTOR
296
297 004C ORG DD 4*013H
298 004C ???????? *ORG_VECTOR DD ? ; DISKETTE/DISK INTERRUPT VECTOR
299
300 0060 ORG DD 4*018H
301 0060 ???????? *BASIC_PTR DD ? ; POINTER TO CASSETTE BASIC
302
303 0074 ORG DD 4*01DH
304 0074 ???????? *PARM_PTR DD ? ; POINTER TO VIDED PARAMETERS
305
306 0078 ORG DD 4*01EH
307 0078 ???????? *DISK_POINTER DD ? ; POINTER TO DISKETTE PARAMETER TABLE
308
309 007C ORG DD 4*01FH
310 007C ???????? *EXT_PTR DD ? ; POINTER TO GRAPHIC CHARACTERS 128-265
311
312 0100 ORG DD 4*040H
313 0100 ???????? *DISK_VECTOR DD ? ; POINTER TO DISKETTE INTERRUPT CODE
314
315 0104 ORG DD 4*041H
316 0104 ???????? *HF_TBL_VEC DD ? ; POINTER TO FIRST DISK PARAMETER TABLE
317
318 0118 ORG DD 4*046H
319 0118 ???????? *HF2_TBL_VEC DD ? ; POINTER TO SECOND DISK PARAMETER TABLE
320
321 01C0 ORG DD 4*070H
322 01C0 ???????? *SLAVE_INT_PTR DD ? ; POINTER TO SLAVE INTERRUPT HANDLER
323
324 01D8 ORG DD 4*076H
325 01D8 ???????? *HDISK_INT DD ? ; POINTER TO FIXED DISK INTERRUPT CODE
326
327 ORG 400H
328 DATA_AREA LABEL BYTE ; ABSOLUTE LOCATION OF DATA SEGMENT
329 DATA_WORD LABEL WORD
330
331 0500 ORG 0500H
332 0500 *MFG_TEST_RTN LABEL FAR ; LOAD LOCATION FOR MANUFACTURING TESTS
333
334 7C00 ORG 7C00H
335 7C00 *BOOT_LOCN LABEL FAR ; BOOT STRAP CODE LOAD LOCATION
336
337 7C00 ABS0 ENDS

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338 C PAGE
339 C ;
340 C ;-----
341 C ; ROM BIOS DATA AREAS
342 C ;-----
343 C DATA SEGMENT AT 40H ; ADDRESS= 0040:0000
344 C DATA LABEL BYTE
345 C @RS232_BASE DW ? ; BASE ADDRESSES OF RS232 ADAPTERS
346 C @RS232_BASE DW ? ; SECOND LOGICAL RS232 ADAPTER
347 C @RS232_BASE DW ? ; RESERVED
348 C @RS232_BASE DW ? ; RESERVED
349 C @PRINTER_BASE DW ? ; BASE ADDRESSES OF PRINTER ADAPTERS
350 C @PRINTER_BASE DW ? ; SECOND LOGICAL PRINTER ADAPTER
351 C @PRINTER_BASE DW ? ; THIRD LOGICAL PRINTER ADAPTER
352 C @PRINTER_BASE DW ? ; RESERVED
353 C @EQUIP_FLAG DW ? ; INSTALLED HARDWARE FLAGS
354 C @MFG_TEST DB ? ; INITIALIZATION FLAGS
355 C @MEMORY_SIZE DW ? ; BASE MEMORY SIZE IN K BYTES ( X 1024)
356 C @MFG_ERR_FLAG DB ? ; SCRATCHPAD FOR MANUFACTURING
357 C @MFG_ERR_FLAG DB ? ; ERROR CODES
358 C ;
359 C ;-----
360 C ; KEYBOARD DATA AREAS
361 C ;-----
362 C ;
363 C @KB_FLAG DB ? ; KEYBOARD SHIFT STATE AND STATUS FLAGS
364 C @KB_FLAG_1 DB ? ; SECOND BYTE OF KEYBOARD STATUS
365 C @ALT_INDIT DB ? ; STORAGE FOR ALTERNATE KEY PAD ENTRY
366 C @BUFFER_HEAD DW ? ; POINTER TO HEAD OF KEYBOARD BUFFER
367 C @BUFFER_TAIL DW ? ; POINTER TO TAIL OF KEYBOARD BUFFER
368 C ;
369 C ;----- HEAD = TAIL INDICATES THAT THE BUFFER IS EMPTY
370 C ;-----
371 C @KB_BUFFER DW 16 DUP(?) ; ROOM FOR 15 SCAN CODE ENTRIES
372 C ;
373 C ;-----
374 C ; DISKETTE DATA AREAS
375 C ;-----
376 C ;
377 C @SEEK_STATUS DB ? ; DRIVE RECALIBRATION STATUS
378 C @SEEK_STATUS DB ? ; BIT 3-0 = DRIVE 3-0 RECALIBRATION
379 C @SEEK_STATUS DB ? ; BEFORE NEXT SEEK IF BIT 15 = 0
380 C ;
381 C @MOTOR_STATUS DB ? ; MOTOR STATUS
382 C @MOTOR_STATUS DB ? ; BIT 3-0 = DRIVE 3-0 CURRENTLY RUNNING
383 C @MOTOR_STATUS DB ? ; BIT 7 = CURRENT OPERATION IS A WRITE
384 C @MOTOR_STATUS DB ? ; TIME OUT COUNTER FOR MOTOR(S) TURN OFF
385 C @MOTOR_COUNT DB ? ; RETURN CODE STATUS BYTE
386 C @DSKETTE_STATUS DB ? ; CMD_BLOCK IN STACK FOR DISK OPERATION
387 C @DSKETTE_STATUS DB ? ; STATUS BYTES FROM DISKETTE OPERATION
388 C @NEC_STATUS DB 7 DUP(?)
389 C ;
390 C ;-----
391 C ; VIDEO DISPLAY DATA AREA
392 C ;-----
393 C ;
394 C @CRT_MODE DB ? ; CURRENT DISPLAY MODE (TYPE)
395 C @CRT_COLS DW ? ; NUMBER OF COLUMNS ON SCREEN
396 C @CRT_LEN DW ? ; LENGTH OF REGEN BUFFER IN BYTES
397 C @CRT_START DW ? ; STARTING ADDRESS IN REGEN BUFFER
398 C @CURSOR_POSN DW 8 DUP(?) ; CURSOR FOR EACH OF UP TO 8 PAGES
399 C ;
400 C ;-----
401 C @CURSOR_MODE DW ? ; CURRENT CURSOR MODE SETTING
402 C @CURSOR_MODE DW ? ; CURRENT PAGE BEING DISPLAYED
403 C @ACTIVE_PAGE DB ? ; BASE ADDRESS FOR ACTIVE DISPLAY CARD
404 C @ACTIVE_PAGE DB ? ; CURRENT SETTING OF THE 3x8 REGISTER
405 C @CURSOR_MODE_SET DB ? ; CURRENT PALETTE SETTING - COLOR CARD
406 C @CRT_PALETTE DB ?
407 C ;
408 C ;-----
409 C ; POST AND BIOS WORK DATA AREA
410 C ;-----
411 C ;
412 C @I/O_ROM_INIT DW ? ; STACK SAVE, ETC.
413 C @I/O_ROM_INIT DW ? ; POINTER TO ROM INITIALIZATION ROUTINE
414 C @I/O_ROM_SEG DW ? ; POINTER TO I/O ROM SEGMENT
415 C @INTR_FLAG DB ? ; FLAG INDICATING AN INTERRUPT HAPPENED
416 C ;
417 C ;-----
418 C ; TIMER DATA AREA
419 C ;-----
420 C ;
421 C @TIMER_LOW DW ? ; LOW WORD OF TIMER COUNT
422 C @TIMER_HIGH DW ? ; HIGH WORD OF TIMER COUNT
423 C @TIMER_OFL DB ? ; TIMER HAS ROLLED OVER SINCE LAST READ
424 C ;
425 C ;-----
426 C ; SYSTEM DATA AREA
427 C ;-----
428 C ;
429 C @BIOS_BREAK DB ? ; BIT 7=1 IF BREAK KEY HAS BEEN PRESSED
430 C @BIOS_BREAK DB ? ; WORD=1234H IF KEYBOARD RESET UNDERWAY
431 C @RESET_FLAG DW ?
432 C ;
433 C ;-----
434 C ; FIXED DISK DATA AREAS
435 C ;-----
436 C ;
437 C @DISK_STATUS1 DB ? ; FIXED DISK STATUS
438 C @HF_NUM DB ? ; COUNT OF FIXED DISK DRIVES
439 C @CONTROL_BYTE DB ? ; HEAD CONTROL BYTE
440 C @CONTROL_BYTE DB ? ; RESERVED (PORT OFFSET)
441 C @PORT_OFF DB ?
442 C ;

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443 C PAGE
444 C |-----|
445 C | TIME-OUT VARIABLES |
446 C |-----|
447 C
448 0078 ?? C *PRINT_TIM_OUT DB ? ; TIME OUT COUNTERS FOR PRINTER RESPONSE
449 0079 ?? C DB ? ; SECOND LOGICAL PRINTER ADAPTER
450 007A ?? C DB ? ; THIRD LOGICAL PRINTER ADAPTER
451 007B ?? C DB ? ; RESERVED
452 007C ?? C *RS232_TIM_OUT DB ? ; TIME OUT COUNTERS FOR RS232 RESPONSE
453 007D ?? C DB ? ; SECOND LOGICAL RS232 ADAPTER
454 007E ?? C DB ? ; RESERVED
455 007F ?? C DB ? ; RESERVED
456 C
457 C |-----|
458 C | ADDITIONAL KEYBOARD DATA AREA |
459 C |-----|
460 C
461 C
462 0080 ????? C *BUFFER_START DW ? ; BUFFER LOCATION WITHIN SEGMENT 40H
463 0082 ????? C *BUFFER_END DW ? ; OFFSET OF KEYBOARD BUFFER START
464 C
465 C |-----|
466 C | EGA/PGA DISPLAY WORK AREA |
467 C |-----|
468 C
469 0084 ?? C *ROWS DB ? ; ROWS ON THE ACTIVE SCREEN (LESS 1)
470 0085 ??? C *POINTS DW ? ; BYTES PER CHARACTER
471 0087 ?? C *INFO DB ? ; MODE OPTIONS
472 0088 ?? C *INFO_3 DB ? ; FEATURE BIT SWITCHES
473 0089 ?? C DB ? ; RESERVED FOR DISPLAY ADAPTERS
474 008A ?? C DB ? ; RESERVED FOR DISPLAY ADAPTERS
475 C
476 C |-----|
477 C | ADDITIONAL MEDIA DATA |
478 C |-----|
479 C
480 008B ?? C *LAstrate DB ? ; LAST DISKETTE DATA RATE SELECTED
481 008C ?? C *HF_STATUS DB ? ; STATUS REGISTER
482 008D ?? C *HF_ERROR DB ? ; ERROR REGISTER
483 008E ?? C *HF_INT_FLAG DB ? ; FIXED DISK INTERRUPT FLAG
484 008F ?? C *HF_CNTRL DB ? ; BIT 0-> PC-1/DUAL FDC ADAPTER CARD
485 0090 ?? C *DSK_STATE DB ? ; DRIVE 0 MEDIA STATE
486 0091 ?? C DB ? ; DRIVE 1 MEDIA STATE
487 0092 ?? C DB ? ; DRIVE 0 OPERATION START STATE
488 0093 ?? C DB ? ; DRIVE 1 OPERATION START STATE
489 0094 ?? C *DSK_TRK DB ? ; DRIVE 0 PRESENT CYLINDER
490 0095 ?? C DB ? ; DRIVE 1 PRESENT CYLINDER
491 C
492 C |-----|
493 C | ADDITIONAL KEYBOARD FLAGS |
494 C |-----|
495 C
496 0096 ?? C *KB_FLAG_3 DB ? ; KEYBOARD MODE STATE AND TYPE FLAGS
497 0097 ?? C *KB_FLAG_2 DB ? ; KEYBOARD LED FLAGS
498 C
499 C I/O SYSTEM
500 C |-----|
501 C | REAL TIME CLOCK DATA AREA |
502 C |-----|
503 C
504 0098 ????? C *USER_FLAG DW ? ; OFFSET ADDRESS OF USERS WAIT FLAG
505 009A ????? C *USER_FLAG_SEG DW ? ; SEGMENT ADDRESS OF USER WAIT FLAG
506 009C ????? C *RTC_LOW DW ? ; LOW WORD OF USER WAIT FLAG
507 009E ????? C *RTC_HIGH DW ? ; HIGH WORD OF USER WAIT FLAG
508 00A0 ?? C *RTC_WAIT_FLAG DB ? ; WAIT ACTIVE FLAG (01=BUSY, 80=POSTED)
509 C
510 C ENDIF ; (00=POST ACKNOWLEDGED)
511 C
512 C |-----|
513 C | AREA FOR NETWORK ADAPTER |
514 C |-----|
515 C
516 00A1 07 [ ?? ] C *NET DB 7 DUP(?) ; RESERVED FOR NETWORK ADAPTERS
517 C
518 C
519 C |-----|
520 C | EGA/PGA PALETTE POINTER |
521 C |-----|
522 C
523 00A8 ???????? C *SAVE_PTR DD ? ; POINTER TO EGA PARAMETER CONTROL BLOCK
524 C
525 C |-----|
526 C | TIMER DATA |
527 C |-----|
528 C
529 00CE C *DAY_COUNT ORG 0CEH ; COUNT OF DAYS FROM 1-1-80
530 00CE ???? C DW ? ;
531 C
532 C |-----|
533 C | DATA AREA - PRINT SCREEN |
534 C |-----|
535 C
536 C
537 0100 C ORG 100H ; ADDRESS= 0040:0100 (REF 0050:0000)
538 C
539 0100 ?? C *STATUS_BYTE DB ? ; PRINT SCREEN STATUS BYTE
540 C ; 00=READY/OK, 01=BUSY, FF=ERROR
541 C
542 0101 C DATA ENDS ; END OF BIOS DATA SEGMENT
543 C
544 C .LIST

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SECTION 5

545		PAGE							
546	0000	CODE	SEGMENT	WORD	PUBLIC				
547			PUBLIC	HEADER					
548									
549									
550			ASSUME	CS:CODE,DS:NOTHING,ES:NOTHING,SS:NOTHING					
551									
552	0000	HEADER	PROC	NEAR					
553									
554	= 0000		BEGIN	EQU	\$				
555	0000 36 32 58 30 38 35		DB	'62X0854	COPR. IBM CORP. 1981,1986				!COPYRIGHT NOTICE
556	34 20 43 4F 50 52								
557	2E 20 49 42 4D 20								
558	43 4F 52 50 2E 20								
559	31 39 38 31 2C 31								
560	39 38 36 20								
561				EVEN					!EVEN BOUNDARY
562	0022 20 20 20 20 20 20		DB						!PAD
563	20 20 20 20 20 20								
564	20 20 20 20 20 20								
565	20 20 20 20 20 20								
566	0039 20 20 20 20 20 20		DB						!PAD
567	20 20 20 20 20 20								
568	20 20 20 20 20 20								
569	20 20 20 20 20 20								
570									
571	0050	HEADER	ENDP						
572	0050	CODE	ENDS						
573			END						

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1      PAGE 118,121
2      TITLE DISKETTE -- 01/10/86 DISKETTE ADAPTER BIOS
3      .LIST
4      INT 13
5      DISKETTE I/O
6      THIS INTERFACE PROVIDES DISK ACCESS TO THE 5.25 INCH 360 KB,
7      1.2 MB, AND 720 KB 80 TRACK DISKETTE DRIVES.
8      INPUT
9      (AH)=0  RESET DISKETTE SYSTEM
10             HARD RESET TO NEC, PREPARE COMMAND, RECALIBRATE REQUIRED
11             ON ALL DRIVES
12
13             (AH)=1  READ THE STATUS OF THE SYSTEM INTO (AH)
14             #DISKETTE_STATUS FROM LAST OPERATION IS USED
15
16     REGISTERS FOR READ/WRITE/VERIFY/FORMAT
17     (DL) - DRIVE NUMBER (0-1 ALLOWED, VALUE CHECKED)
18     (DH) - HEAD NUMBER (0-1 ALLOWED, NOT VALUE CHECKED)
19     (CH) - TRACK NUMBER (NOT VALUE CHECKED)
20
21     MEDIA      DRIVE      TRACK NUMBER
22     320/360    320/360    0-39
23     320/360    1.2M      0-39
24     1.2M      1.2M      0-79
25     720K      720K      0-79
26
27     (CL) - SECTOR NUMBER (NOT VALUE CHECKED, NOT USED FOR FORMAT)
28     MEDIA      DRIVE      SECTOR NUMBER
29     320/360    320/360    1-8/9
30     320/360    1.2M      1-8/9
31     1.2M      1.2M      1-15
32     720K      720K      1-9
33
34     (AL) - NUMBER OF SECTORS (NOT VALUE CHECKED)
35     MEDIA      DRIVE      MAX NUMBER OF SECTORS
36     320/360    320/360    8/9
37     320/360    1.2M      8/9
38     1.2M      1.2M      15
39     720K      720K      9
40
41     (ES:BX) - ADDRESS OF BUFFER (NOT REQUIRED FOR VERIFY)
42
43             (AH)=2  READ THE DESIRED SECTORS INTO MEMORY
44             (AH)=3  WRITE THE DESIRED SECTORS FROM MEMORY
45             (AH)=4  VERIFY THE DESIRED SECTORS
46             (AH)=5  FORMAT THE DESIRED TRACK
47
48     (ES:BX) MUST POINT TO THE COLLECTION OF DESIRED ADDRESS FIELDS
49     FOR THE TRACK. EACH FIELD IS COMPOSED OF 4 BYTES, (C,H,R,N),
50     WHERE C = TRACK NUMBER, H=HEAD NUMBER, R = SECTOR NUMBER,
51     N= NUMBER OF BYTES PER SECTOR (00=128, 01=256, 02=512, 03=1024).
52     THERE MUST BE ONE ENTRY FOR EVERY SECTOR ON THE TRACK.
53     THIS INFORMATION IS USED TO FIND THE REQUESTED SECTOR DURING
54     READ/WRITE ACCESS.
55
56     PRIOR TO FORMATTING A DISKETTE, IF THERE EXISTS MORE THAN
57     ONE SUPPORTED MEDIA FORMAT TYPE WITHIN THE DRIVE IN QUESTION,
58     THEN "SET DASD TYPE" (INT 13H, AH = 17H) OR "SET MEDIA TYPE"
59     (INT 13H, AH = 18H) MUST BE CALLED TO SET THE DISKETTE TYPE
60     THAT IS TO BE FORMATTED. IF "SET DASD TYPE" OR "SET MEDIA TYPE"
61     IS NOT CALLED, THE FORMAT ROUTINE WILL ASSUME THE MEDIA FORMAT
62     TO BE THE MAXIMUM CAPACITY OF THE DRIVE.
63
64     THESE PARAMETERS OF DISK_BASE MUST BE CHANGED IN ORDER TO
65     FORMAT THE FOLLOWING MEDIAS:
66
67     : MEDIA : DRIVE : PARM 1 : PARM 2 :
68     : 320K : 320K/360K/1.2M : 50H : 8 :
69     : 360K : 320K/360K/1.2M : 50H : 9 :
70     : 1.2M : 1.2M : 54H : 15 :
71     : 720K : 720K : 50H : 9 :
72
73     NOTES: - PARM 1 = GAP LENGTH FOR FORMAT
74            - PARM 2 = EOT (LAST SECTOR ON TRACK)
75            - DISK BASE IS POINTED TO BY DISK POINTER LOCATED
76              AT ABSOLUTE ADDRESS 017BH.
77            - WHEN FORMAT OPERATIONS ARE COMPLETE, THE PARAMETERS
78              SHOULD BE RESTORED TO THEIR RESPECTIVE INITIAL VALUES.
79
80
81
82     (AH)=8  READ DRIVE PARAMETERS
83     REGISTERS
84     INPUT
85     (DL) - DRIVE NUMBER (0-1 ALLOWED, VALUE CHECKED)
86     OUTPUT
87     (ES:DI) POINTS TO DRIVE PARAMETERS TABLE
88     (CH) - LOW ORDER 8 OF 10 BITS MAXIMUM NUMBER OF TRACKS
89     (CL) - BITS 7 & 6 - HIGH ORDER TWO BITS OF MAXIMUM TRACKS
90     - BITS 5 THRU 0 - MAXIMUM SECTORS PER TRACK
91     - MAXIMUM HEAD NUMBER
92     (DL) - NUMBER OF DISKETTE DRIVES INSTALLED
93     (BH) - 0
94     (BL) - BITS 7 THRU 4 - 0
95     - BITS 3 THRU 0 - VALID DRIVE TYPE VALUE IN CMOS
96     (AX) - 0
97     UNDER THE FOLLOWING CIRCUMSTANCES:
98     (1) THE DRIVE NUMBER IS INVALID,
99     (2) THE DRIVE TYPE IS UNKNOWN AND CMOS IS NOT PRESENT,
100    (3) THE DRIVE TYPE IS UNKNOWN AND CMOS IS BAD,
101    (4) OR THE DRIVE TYPE IS UNKNOWN AND THE CMOS DRIVE TYPE IS INVALID
102    THEN ES,AX,BX,CX,DH,DI=0 ; DL=NUMBER OF DRIVES;
103    IF NO DRIVES ARE PRESENT THEN: ES,AX,BX,CX,DX,DI=0.
104    #DISKETTE_STATUS = 0 AND CY IS RESET.
105
106             (AH)=15  READ DASD TYPE
107             OUTPUT REGISTERS
108             (AH) - ON RETURN IF CARRY FLAG NOT SET, OTHERWISE ERROR
109                   00 - DRIVE NOT PRESENT
110                   01 - DISKETTE, NO CHANGE LINE AVAILABLE
111                   02 - DISKETTE, CHANGE LINE AVAILABLE
112                   03 - RESERVED
113             (DL) - DRIVE NUMBER (0-1 ALLOWED, VALUE CHECKED)
114
    
```

SECTION 5

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115 -----
116 (AH)=16 DISK CHANGE LINE STATUS
117 OUTPUT REGISTERS
118 (AH) - 00 - DISK CHANGE LINE NOT ACTIVE
119       06 - DISK CHANGE LINE ACTIVE & CARRY BIT ON
120       (DL) - DRIVE NUMBER (0-1 ALLOWED, VALUE CHECKED)
121 -----
122 (AH)=17 SET DASD TYPE FOR FORMAT
123 INPUT REGISTERS
124 (AL) - 00 - NOT USED
125       01 - DISKETTE 320/360K IN 360K DRIVE
126       02 - DISKETTE 360K IN 1.2M DRIVE
127       03 - DISKETTE 1.2M IN 1.2M DRIVE
128       04 - DISKETTE 720K IN 720K DRIVE
129 (DL) - DRIVE NUMBER (0-1 ALLOWED, VALUE CHECKED)
130       DO NOT USE WHEN DISKETTE ATTACH CARD USED)
131 -----
132 (AH)=18 SET MEDIA TYPE FOR FORMAT
133 INPUT REGISTERS
134 (CH) - LOW ORDER 8 OF 10 BITS MAXIMUM NUMBER OF TRACKS
135 (CL) - BITS 7 & 6 - HIGH ORDER TWO BITS OF MAXIMUM TRACKS
136       - BITS 5 THRU 0 - MAXIMUM SECTORS PER TRACK
137 (DL) - DRIVE NUMBER (0-1 ALLOWED, VALUE CHECKED)
138       TYPE AH(151)
139 OUTPUT REGISTERS
140 (ES:DI) - POINTER TO DRIVE PARAMETERS TABLE FOR THIS MEDIA TYPE,
141          UNCHANGED IF (AH) IS NON-ZERO
142 (AH) - 00H, CY = 0, TRACK AND SECTORS/TRACK COMBINATION IS SUPPORTED
143       - 01H, CY = 1, FUNCTION IS NOT AVAILABLE
144       - 0CH, CY = 1, TRACK AND SECTORS/TRACK COMBINATION IS NOT SUPPORTED
145 -----
146 DISK CHANGE STATUS IS ONLY CHECKED WHEN A MEDIA SPECIFIED IS OTHER
147 THAN 360 KB DRIVE. IF THE DISK CHANGE LINE IS FOUND TO BE
148 ACTIVE THE FOLLOWING ACTIONS TAKE PLACE:
149     ATTEMPT TO RESET DISK CHANGE LINE TO INACTIVE STATE.
150     IF ATTEMPT SUCCEEDS SET DASD TYPE FOR FORMAT AND RETURN DISK
151     CHANGE ERROR CODE
152     IF ATTEMPT FAILS RETURN TIMEOUT ERROR CODE AND SET DASD TYPE
153     TO A PREDETERMINED STATE INDICATING MEDIA TYPE UNKNOWN.
154     IF THE DISK CHANGE LINE IN INACTIVE PERFORM SET DASD TYPE FOR FORMAT.
155 -----
156 DATA VARIABLE -- #DISK POINTER
157 DOUBLE WORD POINTER TO THE CURRENT SET OF DISKETTE PARAMETERS
158 -----
159 OUTPUT FOR ALL FUNCTIONS
160 AH = STATUS OF OPERATION
161     STATUS BITS ARE DEFINED IN THE EQUATES FOR #DISKETTE_STATUS
162     VARIABLE IN THE DATA SEGMENT OF THIS MODULE
163 CY = 0 SUCCESSFUL OPERATION (AH=0 ON RETURN, EXCEPT FOR READ DASD
164     TYPE AH(151)).
165     CY = 1 FAILED OPERATION (AH HAS ERROR REASON)
166     FOR READ/WRITE/VERIFY
167     DS,BX,DX,CX PRESERVED
168     NOTE: IF AN ERROR IS REPORTED BY THE DISKETTE CODE, THE APPROPRIATE
169     ACTION IS TO RESET THE DISKETTE, THEN RETRY THE OPERATION.
170     ON READ ACCESSES, NO MOTOR START DELAY IS TAKEN, SO THAT
171     THREE RETRIES ARE REQUIRED ON READS TO ENSURE THAT THE
172     PROBLEM IS NOT DUE TO MOTOR START-UP.
173 -----
174 .LIST
175 DISKETTE STATE MACHINE - ABSOLUTE ADDRESS 40:90 (DRIVE A) & 91 (DRIVE B)
176 .LIST
177
178 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
179 |---|---|---|---|---|---|---|---|
180 |   |   |   |   |   |   |   |   |
181 |   |   |   |   |   |   |   |   |
182 |   |   |   |   |   |   |   |   |
183 |   |   |   |   |   |   |   |   |
184 |   |   |   |   |   |   |   |   |
185 |   |   |   |   |   |   |   |   |
186 |   |   |   |   |   |   |   |   |
187 |   |   |   |   |   |   |   |   |
188 |   |   |   |   |   |   |   |   |
189 |   |   |   |   |   |   |   |   |
190 |   |   |   |   |   |   |   |   |
191 |   |   |   |   |   |   |   |   |
192 |   |   |   |   |   |   |   |   |
193 |   |   |   |   |   |   |   |   |
194 |   |   |   |   |   |   |   |   |
195 |   |   |   |   |   |   |   |   |
196 |   |   |   |   |   |   |   |   |
197 |   |   |   |   |   |   |   |   |
198 |   |   |   |   |   |   |   |   |
199 |   |   |   |   |   |   |   |   |
200 |   |   |   |   |   |   |   |   |
201 |   |   |   |   |   |   |   |   |
202 |   |   |   |   |   |   |   |   |
203 |   |   |   |   |   |   |   |   |
204 |   |   |   |   |   |   |   |   |
205 |   |   |   |   |   |   |   |   |
206 |   |   |   |   |   |   |   |   |
207 |   |   |   |   |   |   |   |   |
208 |   |   |   |   |   |   |   |   |
209 |   |   |   |   |   |   |   |   |
210 |   |   |   |   |   |   |   |   |
211 |---|---|---|---|---|---|---|---|
212 STATE OPERATION STARTED - ABSOLUTE ADDRESS 40:92 (DRIVE A) & 93 (DRIVE B)
213 .LIST
214 PRESENT CYLINDER NUMBER - ABSOLUTE ADDRESS 40:94 (DRIVE A) & 95 (DRIVE B)

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216 PAGE
217
218 MD_STRUC STRUC
219 MD_SPEC1 DB ? ; SRT=D, HD UNLOAD=0F - 1ST SPECIFY BYTE
220 MD_SPEC2 DB ? ; HD LOAD=1, MODE=DMA - 2ND SPECIFY BYTE
221 MD_OFF_TIM DB ? ; WAIT TIME AFTER OPERATION TILL MOTOR OFF
222 MD_BVT_SEC DB ? ; 512 BYTES/SECTOR
223 MD_SEC_TRK DB ? ; EOT ( LAST SECTOR ON TRACK)
224 MD_GAP DB ? ; GAP LENGTH
225 MD_DTL DB ? ; DTL
226 MD_GAP3 DB ? ; GAP LENGTH FOR FORMAT
227 MD_FIL_BYT DB ? ; FILL BYTE FOR FORMAT
228 MD_HD_TIM DB ? ; HEAD SETTLE TIME (MILLISECONDS)
229 MD_STR_TIM DB ? ; MOTOR START TIME (1/8 SECONDS)
230 MD_MAX_TRK DB ? ; MAX. TRACK NUMBER
231 MD_RATE DB ? ; DATA TRANSFER RATE
232 MD_STRUC ENDS
233 = 007F BIT7OFF EQU 7FH
234 = 0080 BITTON EQU 80H
235
236 PUBLIC DISK_INT_1
237 PUBLIC DSKETTE_SETUP
238 PUBLIC DISKETTE_IO_1
239 PUBLIC NEC_OUTPDT
240 PUBLIC RESULTS
241 PUBLIC SEEK
242
243 EXTRN DDS:NEAR
244 EXTRN DISK_BASE:NEAR
245 EXTRN WAITF:NEAR
246 EXTRN MD_TBL1:NEAR
247 EXTRN MD_TBL2:NEAR
248 EXTRN MD_TBL3:NEAR
249 EXTRN MD_TBL4:NEAR
250 EXTRN MD_TBL5:NEAR
251 EXTRN MD_TBL6:NEAR
252
253 0000 CODE SEGMENT BYTE PUBLIC
254
255 ASSUME CS:CODE,DS:DATA,ES:DATA
256
257 ;-----
258 ; DRIVE TYPE TABLE
259 ;-----
260 DR_TYPE LABEL BYTE
261 0000 01 DB 01 ; DRIVE TYPE, MEDIA TABLE
262 0001 0000 E DW OFFSET MD_TBL1
263 0003 82 DB 02+BITTON
264 0004 0000 E DW OFFSET MD_TBL2
265 0006 02 DB 02
266 0007 0000 E DW OFFSET MD_TBL3
267 0009 03 DB 03
268 000A 0000 E DW OFFSET MD_TBL4
269 000C 84 DB 04+BITTON
270 000D 0000 E DW OFFSET MD_TBL5
271 000F 04 DB 04
272 0010 0000 E DW OFFSET MD_TBL6
273 = 0012 DR_TYPE_E EQU ; END OF TABLE
274 = 0006 DR_CNT DB 6 (DR_TYPE_E-DR_TYPE)/3 ; NUMBER OF DRIVE TYPES
275
276 DISKETTE_IO_1 PROC FAR ;>>> ENTRY POINT FOR ORG 0EC59H
277 STI ; INTERRUPTS BACK ON
278 PUSH BP ; USER REGISTER
279 DI ; USER REGISTER
280 PUSH DX ; HEAD #, DRIVE # OR USER REGISTER
281 PUSH CX ; BUFFER OFFSET PARAMETER OR REGISTER
282 PUSH CX ; TRACK #-SECTOR # OR USER REGISTER
283 MOV BP,SP ; BP == PARAMETER LIST DEP. ON AH
284 ; [BP] = SECTOR #
285 ; [BP+1] = TRACK #
286 ; [BP+2] = BUFFER OFFSET
287 ; FOR RETURN OF DRIVE PARAMETERS:
288 ; CL/[BP] = BITS 7-6 HI BITS OF MAX CYL
289 ; ; BITS 0-5 MAX SECTORS/TRACK
290 ; CH/[BP+1] = LOW 8 BITS OF MAX CYL.
291 ; BL/[BP+2] = BITS 7-4 = 0
292 ; ; BITS 3-0 = VALID CMOS TYPE
293 ; BH/[BP+3] = 0
294 ; DL/[BP+4] = # DRIVES INSTALLED
295 ; DH/[BP+5] = MAX HEAD #
296 ; DI/[BP+6] = OFFSET TO DISK BASE
297 ; BUFFER SEGMENT PARM OR USER REGISTER
298 ; USER REGISTERS
299 ; SEGMENT OF BIOS DATA AREA TO DS
300 001F 80 FC 19 CMP AH,(FNC_TAE-FNC_TABI)/2 ; CHECK FOR > LARGEST FUNCTION
301 0022 72 02 JB OK_FUNC ; FUNCTION OK
302 0024 B4 14 MOV AH,14H ; REPLACE WITH KNOWN INVALID FUNCTION
303
304 OK_FUNC: AH,1 ; RESET OR STATUS ?
305 0029 76 0C CBE OK_DRV ; IF RESET OR STATUS DRIVE ALWAYS OK
306 002B 80 FC 08 JMP AH,8 ; READ DRIVE PARAMS ?
307 002E 74 07 JZ OK_DRV ; IF 50 DRIVE CHECKED LATER
308 0030 80 FA 03 CBE DL,3 ; DRIVES 0,1,2 AND 3 OK
309 0033 76 02 CBE OK_DRV ; IF 0 OR 1 THEN JUMP
310 0035 B4 14 MOV AH,14H ; REPLACE WITH KNOWN INVALID FUNCTION
311
312 OK_DRV: CL,AH ; CL = FUNCTION
313 XOR CH,CH ; CX = FUNCTION
314 003B DD E1 SHL CL,1 ; FUNCTION TIMES 2
315 003D BB 0060 R MOV BX,OFFSET FNC_TAB ; LOAD START OF FUNCTION TABLE
316 0040 03 D9 ADD BX,CX ; ADD OFFSET INTO TABLE => ROUTINE
317 0042 8A E6 MOV AH,DH ; AX = HEAD #, # OF SECTORS OR DASH TYPE
318 0044 32 F6 XOR DH,DH ; DX = DRIVE #
319 0046 8B F0 MOV SI,AX ; SI = HEAD #, # OF SECTORS OR DASH TYPE
320 0048 8B FA MOV DI,DX ; DI = DRIVE #
321 004A 8A 26 0041 R MOV AH,#DSKETTE_STATUS ; LOAD STATUS TO AH FOR STATUS FUNCTION
322 004E C6 06 0041 R 00 MOV #DSKETTE_STATUS,0 ; INITIALIZE FOR ALL OTHERS
323
324 ; THROUGHOUT THE DISKETTE BIOS, THE FOLLOWING INFORMATION IS CONTAINED IN
325 ; THE FOLLOWING MEMORY LOCATIONS AND REGISTERS. NOT ALL DISKETTE BIOS
326 ; FUNCTIONS REQUIRE ALL OF THESE PARAMETERS.
327 ;
328 ;
329 ;
330 ; DI ; DRIVE #
    
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SECTION 5

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329          ; SI-HI : HEAD #
330          ; SI-LOW : # OF SECTORS OR DASD TYPE FOR FORMAT
331          ; ES : BUFFER SEGMENT
332          ; [BP] : SECTOR #
333          ; [BP+1] : TRACK #
334          ; [BP+2] : BUFFER OFFSET
335          ;
336          ; ACROSS CALLS TO SUBROUTINES THE CARRY FLAG (CY=1), WHERE INDICATED IN
337          ; SUBROUTINE PROLOGUES, REPRESENTS AN EXCEPTION RETURN (NORMALLY AN ERROR
338          ; CONDITION). IN MOST CASES, WHEN CY = 1, #DISKETTE_STATUS CONTAINS THE
339          ; SPECIFIC ERROR CODE.
340          ; (AH) = #DISKETTE STATUS
341          ; CALL WORD PTR CS:[BX] : CALL THE REQUESTED FUNCTION
342
343          POP SI : RESTORE ALL REGISTERS
344          POP DS
345          POP CX
346          POP BX
347          POP DX
348          POP DI
349          POP BP
350          RET 2 : THROW AWAY SAVED FLAGS
351
352          ;-----
353          FNC_TAB DW DISK RESET : AH = 00; RESET
354                  DW DISK_STATUS : AH = 01; STATUS
355                  DW DISK_READ : AH = 02; READ
356                  DW DISK_WRITE : AH = 03; WRITE
357                  DW DISK_VERIFY : AH = 04; VERIFY
358                  DW DISK_FORMAT : AH = 05; FORMAT
359                  DW FNC_ERR : AH = 06; INVALID
360                  DW FNC_ERR : AH = 07; INVALID
361                  DW DISK_PARAMS : AH = 08; READ DRIVE PARAMETERS
362                  DW FNC_ERR : AH = 09; INVALID
363                  DW FNC_ERR : AH = 0A; INVALID
364                  DW FNC_ERR : AH = 0B; INVALID
365                  DW FNC_ERR : AH = 0C; INVALID
366                  DW FNC_ERR : AH = 0D; INVALID
367                  DW FNC_ERR : AH = 0E; INVALID
368                  DW FNC_ERR : AH = 0F; INVALID
369                  DW FNC_ERR : AH = 10; INVALID
370                  DW FNC_ERR : AH = 11; INVALID
371                  DW FNC_ERR : AH = 12; INVALID
372                  DW FNC_ERR : AH = 13; INVALID
373                  DW FNC_ERR : AH = 14; INVALID
374                  DW DISK_TYPE : AH = 15; READ DASD TYPE
375                  DW DISK_CHANGE : AH = 16; CHANGE STATUS
376                  DW FORMAT_SET : AH = 17; SET DASD TYPE
377                  DW SET_MEDIA : AH = 18; SET MEDIA TYPE
378          = 0092 : FNC_TAE EQU $ : END
379          DISKETTE_10 : END
380          ;-----
381          ; DISK RESET
382          ; RESET THE DISKETTE SYSTEM.
383          ;
384          ; ON EXIT: #DISKETTE_STATUS, CY REFLECT STATUS OF OPERATION
385          ;-----
386          DISK_RESET PROC NEAR
387                  MOV DX,03F2H : ADAPTER CONTROL PORT
388                  CLI : NO INTERRUPTS
389                  MOV AL,#MOTOR_STATUS : GET DIGITAL OUTPUT REGISTER REFLECTION
390                  AND AL,00111111B : KEEP SELECTED AND MOTOR ON BITS
391                  ROL AL,1 : MOTOR VALUE TO HIGH NIBBLE
392                  ROL AL,1 : DRIVE SELECT TO LOW NIBBLE
393                  ROL AL,1
394                  ROL AL,1
395                  OR AL,00001000B : TURN ON INTERRUPT ENABLE
396                  OUT DX,AL : RESET THE ADAPTER
397                  MOV #SEEK_STATUS,0 : SET RECALIBRATE REQUIRED ON ALL DRIVES
398                  JMP $+2 : WAIT FOR I/O
399                  OR AL,00000100B : TURN OFF RESET BIT
400                  OUT DX,AL : RESET THE ADAPTER
401                  STI : ENABLE THE INTERRUPTS
402                  CALL WAIT_INT : WAIT FOR THE INTERRUPT
403                  JC DR_ERR : IF ERROR, RETURN IT
404                  MOV CX,11000000B : CL = EXPECTED #NEC_STATUS
405
406          NXT_DRV:
407                  PUSH CX : SAVE FOR CALL
408                  MOV AX,OFFSET DR_POP_ERR : LOAD NEC_OUTPUT ERROR ADDRESS
409                  PUSH AX :
410                  MOV AH,08H : SENSE INTERRUPT STATUS COMMAND
411                  CALL NEC_OUTPUT :
412                  POP AX : THROW AWAY ERROR RETURN
413                  CALL RESULTS : READ IN THE RESULTS
414                  POP CX : RESTORE AFTER CALL
415                  JC DR_ERR : ERROR RETURN
416                  CMP CL,#NEC_STATUS : TEST FOR DRIVE READY TRANSITION
417                  JNZ DR_ERR : EVERYTHING OK
418                  OR DR_ERR FE,C1 : NEXT EXPECTED #NEC_STATUS
419                  CMP CL,11000011B : ALL POSSIBLE DRIVES CLEARED
420                  JBE NXT_DRV : FALL THRU IF 11000100B OR >
421
422          ;---- SEND SPECIFY COMMAND TO NEC
423
424          J7:
425          ODD7 E8 03D1 R : CALL SEND_SPEC
426          ODDA :
427          ODDA E8 0832 R : CALL SETUP_END : VARIOUS CLEANUPS
428          ODDD 8D E0 : MOV BX,S1 : GET SAVED AL TO BL
429          ODDF 8A C3 : MOV AL,BL : PUT BACK FOR RETURN
430          ODE1 C3 : RET
431
432          DR_POP_ERR:
433          ODE2 59 : CX : CLEAR STACK
434          ODE3 :
435          ODE3 80 E0 0041 R 20 : OR #DISKETTE_STATUS,BAD_NEC : SET ERROR CODE
436          ODE4 EB F0 : JMP RESBAC : RETURN FROM RESET
437          ODEA :
438          ;-----
439          ; DISK_STATUS
440          ; DISKETTE STATUS.
441          ; ON ENTRY: AH = STATUS OF PREVIOUS OPERATION
442
    
```

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443 ;
444 ; ON EXIT:  @DSKETTE_STATUS, CY REFLECT STATUS OF OPERATION ;
445 -----
446 00EA PROC NEAR
447 00EA 88 26 0041 R @DSKETTE_STATUS, AH ; PUT BACK FOR SETUP_END
448 00EE E8 0832 R CALL SETUP_END ; VARIOUS CLEANUPS
449 00F1 8B DE MOV BX, SI ; GET SAVED AL TO BL
450 00F3 8A C4 MOV AL, AH ; STORE STATUS IN AL
451 00F5 C3 RET
452 00F6 ENDP
453
454 ; DISK_READ
455 ; DISKETTE READ. ;
456 ; ON ENTRY:  D1 = DRIVE # ;
457 ; S1-HI = HEAD # ;
458 ; S1-LOW = # OF SECTORS ;
459 ; ES = BUFFER SEGMENT ;
460 ; [BP] = SECTOR # ;
461 ; [BP+1] = TRACK # ;
462 ; [BP+2] = BUFFER OFFSET ;
463 ;
464 ; ON EXIT:  @DSKETTE_STATUS, CY REFLECT STATUS OF OPERATION ;
465 -----
466 00F6 PROC NEAR
467 00F6 80 26 003F R 7F AND @MOTOR_STATUS, 01111111B ; INDICATE A READ OPERATION
468 00FB 8B E646 MOV AX, 0E646H ; AX = NEC COMMAND, DMA COMMAND
469 00FE E8 04B3 R CALL RD_WR_VF ; COMMON READ/WRITE/VERIFY
470 0101 C3 RET
471 0102 ENDP
472
473 ; DISK_WRITE
474 ; DISKETTE WRITE. ;
475 ; ON ENTRY:  D1 = DRIVE # ;
476 ; S1-HI = HEAD # ;
477 ; S1-LOW = # OF SECTORS ;
478 ; ES = BUFFER SEGMENT ;
479 ; [BP] = SECTOR # ;
480 ; [BP+1] = TRACK # ;
481 ; [BP+2] = BUFFER OFFSET ;
482 ;
483 ; ON EXIT:  @DSKETTE_STATUS, CY REFLECT STATUS OF OPERATION ;
484 -----
485 0102 PROC NEAR
486 0102 8B C54A AX, 0C54AH ; AX = NEC COMMAND, DMA COMMAND
487 0105 80 0E 003F R 80 OR @MOTOR_STATUS, 10000000B ; INDICATE WRITE OPERATION
488 010A E8 04B3 R CALL RD_WR_VF ; COMMON READ/WRITE/VERIFY
489 010D C3 RET
490 010E ENDP
491
492 ; DISK_VERIFY
493 ; DISKETTE VERIFY. ;
494 ; ON ENTRY:  D1 = DRIVE # ;
495 ; S1-HI = HEAD # ;
496 ; S1-LOW = # OF SECTORS ;
497 ; ES = BUFFER SEGMENT ;
498 ; [BP] = SECTOR # ;
499 ; [BP+1] = TRACK # ;
500 ; [BP+2] = BUFFER OFFSET ;
501 ;
502 ; ON EXIT:  @DSKETTE_STATUS, CY REFLECT STATUS OF OPERATION ;
503 -----
504 010E PROC NEAR
505 010E 80 26 003F R 7F AND @MOTOR_STATUS, 01111111B ; INDICATE A READ OPERATION
506 0113 8B E642 MOV AX, 0E642H ; AX = NEC COMMAND, DMA COMMAND
507 0116 E8 04B3 R CALL RD_WR_VF ; COMMON READ/WRITE/VERIFY
508 0119 C3 RET
509 011A ENDP
510
511 ; DISK_FORMAT
512 ; DISKETTE FORMAT. ;
513 ; ON ENTRY:  D1 = DRIVE # ;
514 ; S1-HI = HEAD # ;
515 ; S1-LOW = # OF SECTORS ;
516 ; ES = BUFFER SEGMENT ;
517 ; [BP] = SECTOR # ;
518 ; [BP+1] = TRACK # ;
519 ; [BP+2] = BUFFER OFFSET ;
520 ; @DISK_POINTER POINTS TO THE PARAMETER TABLE OF ;
521 ; THIS DRIVE ;
522 ;
523 ; ON EXIT:  @DSKETTE_STATUS, CY REFLECT STATUS OF OPERATION ;
524 -----
525 011A PROC NEAR
526 011A E8 0404 R CALL XLAT_NEW ; TRANSLATE STATE TO PRESENT ARCH.
527 011D E8 05A0 R CALL FMT_TRNIT ; ESTABLISH STATE IF UNESTABLISHED
528 0120 80 0E 003F R 80 OR @MOTOR_STATUS, 10000000B ; INDICATE WRITE OPERATION
529 0125 F6 06 008F R 01 TEST @HF_CNTRL_DUAL ; TEST CONTROLLER I.D.
530 012A T4 05 JZ NO_CHG_CHECK ; NO CHG_CHECK
531 012C E8 05F5 R CALL MED_CHANGE ; MED_CHANGE
532 012F 72 41 JC FM_DON ; CHECK MEDIA CHANGE AND RESET IF SO
533 0131 ; MEDIA CHANGED, SKIP
534 0131 E8 0658 R NO_CHG_CHECK: CHK_LASTRATE ; 2F=1 ATTEMPT RATE IS SAME AS LAST RATE
535 0134 74 06 JZ FR ; YES, SKIP SPECIFY COMMAND
536 0136 E8 03D1 R CALL SEND_SPEC ; SEND SPECIFY COMMAND TO NEC
537 0139 E8 0637 R CALL SEND_RATE ; SEND DATA RATE TO CONTROLLER
538 013C ;
539 013C 80 4A AND @DMA_SETUP, 0 ; WILL WRITE TO THE DISKETTE
540 013E E8 0668 R CALL DMA_SETUP ; SET UP THE DMA
541 0141 72 2F JC FM_DON ; RETURN WITH ERROR
542 0143 B4 4D MOV AH, 040H ; ESTABLISH THE FORMAT COMMAND
543 0145 E8 06CB R CALL NEC_INIT ; INITIALIZE THE NEC
544 0148 72 28 JC FM_DON ;
545 014A E8 0172 R MOV AX, OFFSET FM_DON ; LOAD ERROR ADDRESS
546 014D 50 PUSH AX ; PUSH NEC OUT ERROR RETURN
547 014E B2 03 MOV DL, 3 ; BYTES/SECTOR VALUE TO NEC
548 0150 E8 08FE R CALL GET_PARM ;
549 0153 E8 09FD R CALL NEC_OUTPUT ;
550 0156 B2 04 MOV DL, 4 ; SECTORS/TRACK VALUE TO NEC
551 0158 E8 08FE R CALL GET_PARM ;
552 015B E8 09FD R CALL NEC_OUTPUT ;
553 015E B2 01 MOV DL, 1 ; GAP LENGTH VALUE TO NEC
554 0160 E8 08FE R CALL GET_PARM ;
555 0163 E8 09FD R CALL NEC_OUTPUT ;
556 0166 B2 08 MOV DL, 8 ; FILLER BYTE TO NEC

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SECTION 5

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557 0168 EB 08FE R      CALL    GET_PARM
558 016B EB 09F0 R      CALL    NEC_OUTPUT
559 016E 58             POP     AX
560 016F EB 0727 R      CALL    NEC_TERM          ; THROW AWAY ERROR
561 0172             FM_DON:
                    ; TERMINATE, RECEIVE STATUS, ETC.
562 0172 EB 0432 R      CALL    XLAT_OLD          ; TRANSLATE STATE TO COMPATIBLE MODE
563 0175 EB 0832 R      CALL    SETUP_END        ; VARIOUS CLEANUPS
564 0178 8B DE         MOV     BX,S1             ; GET SAVED AL TO BL
565 017A 8A C3         MOV     AL,BL           ; PUT BACK FOR RETURN
566 017C C3             RET
567 017D             DISK_FORMAT  ENDP
568
569 -----
570 ; FNC_ERR
571 ; INVALID FUNCTION REQUESTED OR INVALID DRIVE;
572 ; SET BAD COMMAND IN STATUS.
573 ;
574 ; ON EXIT:  #DISKETTE_STATUS, CY REFLECT STATUS OF OPERATION ;
575 -----
576 FNC_ERR PROC  NEAR          ; INVALID FUNCTION REQUEST
577             MOV     AX,S1          ; RESTORE AL
578             MOV     AH,BAD_CMD     ; SET BAD COMMAND ERROR
579             MOV     #DISKETTE_STATUS,AH ; STORE IN DATA AREA
580             STC                    ; SET CARRY INDICATING ERROR
581             RET
582 FNC_ERR ENDP
583 -----
584 ; DISK_PARMS
585 ; READ DRIVE PARAMETERS.
586 ; ON ENTRY:  DI = DRIVE #
587 ; ON EXIT:
588 ; CL/[BP] = BITS 7 & 6 HIGH 2 BITS OF MAX CYLINDER
589 ; BIT 0-5 MAX SECTORS/TRACK
590 ; CH/[BP+1] = LOW 8 BITS OF MAX CYLINDER
591 ; BL/[BP+2] = BITS 7-4 = 0
592 ; BITS 3-0 = VALID CMOS DRIVE TYPE
593 ;
594 ; BH/[BP+3] = 0
595 ; DL/[BP+4] = # DRIVES INSTALLED
596 ; DH/[BP+5] = MAX HEAD #
597 ; DI/[BP+6] = OFFSET OF MEDIA/DRIVE PARAMETER TABLE
598 ; ES = SEGMENT OF MEDIA/DRIVE PARAMETER TABLE
599 ; AX = 0
600 ; NOTE : THE ABOVE INFORMATION IS STORED IN THE USERS STACK AT
601 ; THE LOCATIONS WHERE THE MAIN ROUTINE WILL POP THEM
602 ; INTO THE APPROPRIATE REGISTERS BEFORE RETURNING TO THE
603 ; CALLER.
604 -----
605 DISK_PARMS PROC  NEAR
606             CMP     DI,80H          ; CHECK FOR FIXED MEDIA TYPE REQUEST
607             JB     DISK_P2         ; CONTINUE IF NOT REQUEST FALL THROUGH
608
609 ;---- FIXED DISK REQUEST FALL THROUGH ERROR
610
611             MOV     AX,S1          ; RESTORE AL WITH CALLERS VALUE
612             MOV     AH,BAD_CMD     ; SET BAD COMMAND ERROR IN (AH)
613             STC                    ; SET ERROR RETURN CODE
614             RET
615
616             DISK_P2:
617             CALL    XLAT_NEW        ; TRANSLATE STATE TO PRESENT ARCH.
618             MOV     WORD PTR [BP+2],0 ; DRIVE TYPE = 0
619             MOV     AX,#REQI_FLAG   ; LOAD EQUIPMENT FLAG FOR # DISKETTES
620             AND     AL,10000001B    ; KEEP DISKETTE DRIVE BITS
621             SHR     AL,1            ; ARE THERE ANY DRIVES INSTALLED?
622             JNC     NON_DRV        ; NO-->NO DRIVES, ZERO PARAMETERS
623             ROL     AL,1            ; ROTATE TO ORIGINAL POSITION
624             ROL     AL,1            ; ROTATE BITS 6 AND 7 TO 0 AND 1
625             ROL     AL,1
626             INC     AL              ; CONVERT TO RELATIVE I
627             MOV     [BP+4],AL       ; STORE NUMBER OF DRIVES
628             MOV     #PHF_CNTRL,DUAL ; CHECK CONTROLLER I.D.
629             JNZ     DPI_CONT       ; CONTINUE WITH USUAL PARMS CHECK
630             JMP     DET_PARMS      ; RETURN THIS CONTROLLERS PARMS
631
632             DPI_CONT:
633             CMP     DI,1            ; CHECK FOR VALID DRIVE
634             JA     NON_DRV1        ; DRIVE INVALID
635             MOV     BYTE PTR [BP+5],1 ; DRIVE NUMBER = 1
636             CALL    CMOS_TYPE      ; RETURN DRIVE TYPE IN AL
637             JC     CHK_EST         ; ON CMOS BAD CHECK ESTABLISHED
638             OR     AL,AL           ; TEST FOR NO DRIVE TYPE
639             JZ     CHK_EST         ; JUMP IF SO
640             CALL    DR_TYPE_CHECK  ; RTN CS:BX = MEDIA/DRIVE PARAM TBL
641             MOV     [BP+2],AL       ; TYPE NOT IN TABLE (POSSIBLE BAD CMOS)
642             MOV     CL,CS:[BX].MD_SEC_TRK ; STORE VALID CMOS DRIVE TYPE
643             MOV     CH,CS:[BX].MD_MAX_TRK ; GET MAX. TRACK NUMBER
644             MOV     CL,CS:[BX].MD_MAX_TRK ; GET MAX. TRACK NUMBER
645             JMP     SHORT STO_CX    ; CMOS GOOD, USE CMOS
646
647             CHK_EST:
648             MOV     AH,#DISK_STATE[DI] ; LOAD STATE FOR THIS DRIVE
649             TEST    AH,MED_DET      ; CHECK FOR ESTABLISHED STATE
650             JZ     NON_DRV1        ; CMOS BAD/INVALID AND UNESTABLISHED
651
652             USE_EST:
653             AND     AH,RATE_MSK     ; ISOLATE STATE
654             CMP     AH,250          ; RATE 250 ?
655             JNE     USE_EST2       ; NO, GO CHECK OTHER RATE
656
657 ;--- DATA RATE IS 250 KBS, TRY 360 KB TABLE FIRST
658
659             MOV     AL,01          ; DRIVE TYPE 1 (360KB)
660             CALL    DR_TYPE_CHECK  ; RTN CS:BX = MEDIA/DRIVE PARAM TBL
661             MOV     CL,CS:[BX].MD_SEC_TRK ; GET SECTOR/TRACK
662             MOV     CH,CS:[BX].MD_MAX_TRK ; GET MAX. TRACK NUMBER
663             TEST    #DISK_STATE[DI],TRK_CAPA ; 80 TRACK ?
664             JZ     STO_CX         ; MUST BE 360KB DRIVE
665
666 ;--- IT IS HIGH DATA RATE/80 TRACK DRIVE
667
668             PARM_HDR_BOT:
669             MOV     AL,04          ; DRIVE TYPE 4
670             CALL    DR_TYPE_CHECK  ; RTN CS:BX = MEDIA/DRIVE PARAM TBL
671             MOV     CL,CS:[BX].MD_SEC_TRK ; GET SECTOR/TRACK

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671 020B 2E: 8A 6F 0B          MOV     CH,CS:[BX].MD_MAX_TRK    ; GET MAX. TRACK NUMBER
672
673 020F
674 020F 89 4E 00          STO_CX: MOV     [BP],CX           ; SAVE IN STACK FOR RETURN
675 0212          ES_DI:
676 0212 89 5E 06          MOV     [BP+6],BX               ; ADDRESS OF MEDIA/DRIVE PARAM TABLE
677 0215 8C 58          MOV     AX,CS                  ; SEGMENT MEDIA/DRIVE PARAMETER TABLE
678 0217 8E C0          MOV     ES,AX                  ; ES IS SEGMENT OF TABLE
679
680 0219          DP_OUT:
681 0219 EB 0432 R        CALL   XLAT_OLD                ; TRANSLATE STATE TO COMPATIBLE MODE
682 021C F8 C0          XOR     AX,AX                  ; CLEAR
683 021E F8          CLC
684 021F C3          RET
685
686 ;---- NO DRIVE PRESENT HANDLER
687
688 0220          NON_DRV:
689 0220 C6 46 04 00      MOV     BYTE PTR [BP+4],0       ; CLEAR NUMBER OF DRIVES
690
691 0224          NON_DRV1:
692 0224 81 FF 0080        CMP     DI,80H                 ; CHECK FOR FIXED MEDIA TYPE REQUEST
693 0228 72 09          JNB    NON_DRV2               ; CONTINUE IF NOT REQUEST FALL THROUGH
694
695 ;---- FIXED DISK REQUEST FALL THROUGH ERROR
696
697 022A          FD_REQ_ERR:
698 022A EB 0432 R        CALL   XLAT_OLD                ; ELSE TRANSLATE TO COMPATIBLE MODE
699 022D 8B C6          MOV     MOV     AX,ST           ; RESTORE AL
700 022F 84 01          MOV     MOV     AH,BAD_CMD       ; SET BAD COMMAND ERROR
701 0231 F9          STC
702 0232 C3          RET
703
704 0233          NON_DRV2:
705 0233 33 C0          XOR     AX,AX                  ; CLEAR PARMS IF NO DRIVES OR CMOS BAD
706 0235 89 46 00      MOV     [BP],AX               ; TRACKS, SECTORS/TRACK = 0
707 0238 88 66 05      MOV     [BP+5],AH             ; HEAD = 0
708 023B 89 46 06      MOV     [BP+6],AX             ; OFFSET TO DISK BASE = 0
709 023E 8E C0          MOV     ES,AX                  ; ES IS SEGMENT OF TABLE
710 0240 EB D7          JMP     SHORT DP_OUT
711
712 ;--- DATA RATE IS EITHER 300 KBS OR 500 KBS, TRY 1.2 MB TABLE FIRST
713
714 0242          USE_EST2:
715 0242 80 02          MOV     AL,02                 ; DRIVE TYPE 2 (1.2MB)
716 0244 EB 03B1 R      CALL   DR_TYPE_CHECK           ; RTN CS:BX = MEDIA/DRIVE PARAM TBL
717 0247 2E: 8A 4F 04      MOV     CL,CS:[BX].MD_SEC_TRK ; GET SECTOR/TRACK
718 024B 2E: 8A 6F 0B      MOV     CH,CS:[BX].MD_MAX_TRK ; GET MAX. TRACK NUMBER
719 024F 80 FC 40      CMP     AH,RATE_300           ; RATE 300 ?
720 0252 74 BB          JE     STO_CX                  ; MUST BE 1.2MB DRIVE
721 0254 EB AC          JMP     SHORT PARM_HOR_80T     ; ELSE, HIGH DATA RATE/80 TRACK DRIVE
722 0256
723 0256 83 FF 03          CMP     JA
724 0259 77 D8          JAE    NON_DRV2               ; YES-->DRIVE NUMBER INVALID
725 025B 81 09          TEST   CL,9                   ;
726 025D F6 85 0090 R 01 ; TEST   #DSK_STATE[DI],TRK_CAPA ; IS DRIVE 80 TRACKS?(RELATIVE ZERO)
727 0262 80 01          MOV     AL,1                   ; SET CMOS TYPE 1
728 0264 85 27          MOV     CH,39                  ; NUMBER OF TRACKS (RELATIVE ZERO)
729 0266 74 04          JZ     SET_TYP1               ; IF ZERO TYPE = 1
730 0268 80 03          MOV     AL,3                   ; SET CMOS TYPE 3
731 026A 85 4F          MOV     CH,79                  ; NUMBER OF TRACKS (RELATIVE ZERO)
732 026C          SET_TYP1:
733 026C 88 46 02      MOV     [BP+2],AL             ; STORE TYPE
734 026F C6 46 03 00      MOV     MOV     BYTE PTR [BP+3],0 ; BYTE PTR [BP+3],0
735 0273 C6 46 05 01      MOV     MOV     BYTE PTR [BP+5],1 ; BYTE PTR [BP+5],1
736 0277 EB 03B1 R      CALL   DR_TYPE_CHECK           ; ADDRESS OF DISK BASE
737 027A EB 93          JMP     STO_CX                 ; GO SET TRKS/SEC,CYL,ES:BX AND EXIT
738
739 027C          DISK_PARMS          ENDP
740
741 ;-----
742 ; DISK_TYPE
743 ; THIS ROUTINE RETURNS THE TYPE OF MEDIA INSTALLED.
744 ; ON ENTRY: DI = DRIVE #
745 ; ON EXIT: AH = DRIVE TYPE, C6=0
746 ;-----
747
748 027C          DISK_TYPE          PROC NEAR
749 027C F6 06 008F R 01 ; TEST   #HF_CNTRL,DUAL          ; CHECK CONTROLLER I.D.
750 0281 74 22          JZ     NO_CHNG                ;
751 0283 EB 0404 R      CALL   XLAT_NEW                ; TRANSLATE STATE TO PRESENT ARCH.
752 0286 8A 85 0090 R ; MOV     AL,#DSK_STATE[DI]      ; GET PRESENT STATE INFORMATION
753 028A 0A C0          OR     AL,AL                   ; CHECK FOR NO DRIVE
754 028C 74 13          JZ     NO_DRV                  ;
755 028E 84 01          MOV     AH,NOCHGLN            ; NO CHANGE LINE FOR 40 TRACK DRIVE
756 0290 A8 01          TEST   AL,TRK_CAPA            ; IS THIS DRIVE AN 80 TRACK DRIVE?
757 0292 74 02          JZ     DT_BACK                 ; IF NO JUMP
758 0294 84 02          MOV     AH,CHGLN              ; CHANGE LINE FOR 80 TRACK DRIVE
759
760 0296          DT_BACK:
761 0296 50          PUSH   AX                      ; SAVE RETURN VALUE
762 0297 EB 0432 R      CALL   XLAT_OLD                ; TRANSLATE STATE TO COMPATIBLE MODE
763 029A 58          POP     AX                      ; RESTORE RETURN VALUE
764 029B F8          DISK_TYPE_EX: CLC
765 029B F8          MOV     MOV     BX,S1           ; GET SAVED AL TO BL
766 029C 8B DE          MOV     MOV     AL,BL           ; PUT BACK FOR RETURN
767 029E 8A C3          RET
768 02A0 C3
769 02A1          NO_DRV:
770 02A1 32 E4          XOR     AH,AH                  ; NO DRIVE PRESENT OR UNKNOWN
771 02A3 EB F1          JMP
772 02A5          NO_CHNG:
773 02A5 A1 0010 R      MOV     AX,#EQUIP_FLAG         ; LOAD EQUIPMENT FLAG FOR # DISKETTES
774 02A8 D0 E8          SHR     AL,1                   ; SHIFT DRIVES PRESENT BIT INTO CARRY
775 02AA 73 F5          JNC    NO_DRV                 ; NO DRIVE IN SYSTEM
776 02AC 84 11          MOV     MOV     AH,0            ; DISKETTE NO CHANGE LINE AVAILABLE
777 02AE EB EB          JMP     DISK_TYPE_EX
778 02B0
779
780 ;-----
781 ; DISK_CHANGE
782 ; THIS ROUTINE RETURNS THE STATE OF THE DISK CHANGE LINE.
783 ; ON ENTRY: DI = DRIVE #
784 ;-----

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785 ; ON EXIT: AH = #DSKETTE_STATUS ;
786 ; 00 - DISK CHANGE LINE INACTIVE, CY = 0 ;
787 ; 06 - DISK CHANGE LINE ACTIVE, CY = 1 ;
788 -----
789 02B0 ; DISK_CHANGE PROC NEAR ; TEST CONTROLLER I.D.
790 02B0 F6 06 008F R 01 ; TEST #HF_CNTRL,DUAL ;
791 02B5 75 03 ; JNZ DC1 ;
792 02B7 E9 017D R ; JMP FNC_ERR ; ERROR FOR THIS KIND OF CONTROLLER
793 02BA ; DC1: CALL XLAT_NEW ; TRANSLATE STATE TO PRESENT ARCH.
794 02BA E8 0404 R ; MOV AL,#DSK_STATE[D1] ; GET MEDIA STATE INFORMATION
795 02BC 8A 85 0090 R ; OR AL,AL ; DRIVE PRESENT ?
796 02C1 0A 0C ; JZ DC_NON ; JUMP IF NO DRIVE
797 02C3 74 19 ; TEST AL,TRK_CAPA ; 80 TRACK DRIVE ?
798 02C5 A8 01 ; JZ SETIT ; IF SO , CHECK CHANGE LINE
799 02C7 74 05 ;
800 ;
801 02C9 E8 0821 R ; DC0: CALL READ_DSKCHNG ; GO CHECK STATE OF DISK CHANGE LINE
802 02CC 74 05 ; JZ FINIS ; CHANGE LINE NOT ACTIVE
803 ;
804 02CE C6 06 0041 R 06 ; SETIT: MOV #DSKETTE_STATUS,MEDIA_CHANGE ; INDICATE MEDIA REMOVED
805 ;
806 02D3 E8 0432 R ; FINIS: CALL XLAT_OLD ; TRANSLATE STATE TO COMPATIBLE MODE
807 02D6 E8 0832 R ; CALL SETUP_END ; VARIOUS CLEANUPS
808 02D9 8B DE ; MOV BX,S1 ; GET SAVED AL TO BL
809 02DB 8A C3 ; MOV AL,BL ; PUT BACK FOR RETURN
810 02DD C3 ; RET ;
811 ;
812 02DE ; DC_NON: OR #DSKETTE_STATUS,TIME_OUT ; SET TIMEOUT, NO DRIVE
813 02DE 80 0E 0041 R 80 ; JMP SHORT FINIS ;
814 02E3 EB EE ; DISK_CHANGE ENDP
815 02E5 ;
816 ;
817 ;
818 ;
819 ;
820 ;
821 ;
822 ; ON ENTRY: S1 LOW = DASD TYPE FOR FORMAT ;
823 ; D1 = DRIVE # ;
824 ;
825 ; ON EXIT: #DSKETTE_STATUS REFLECTS STATUS ;
826 ; AH = #DSKETTE_STATUS ;
827 ; CY = 1 IF ERROR ;
828 -----
829 02E5 ; FORMAT_SET PROC NEAR ;
830 02E5 E8 0404 R ; CALL XLAT_NEW ; TRANSLATE STATE TO PRESENT ARCH.
831 02E8 56 ; PUSH S1 ; SAVE DASD TYPE
832 02E9 8B C6 ; MOV AX,S1 ; AH = ? , AL = DASD TYPE
833 02EB 32 E4 ; XOR AH,AH ; AH = 0 , AL = DASD TYPE
834 02ED BB F0 ; MOV S1,AX ; S1 = DASD TYPE
835 02EF 80 A5 0090 R 0F ; AND #DSK_STATE[D1],NOT MED_DET+DBL_STEP+RATE_MSK ; CLEAR STATE
836 02F4 4E ; DEC S1 ; CHECK FOR 320/360K MEDIA & DRIVE
837 02F5 75 07 ; JNZ NOT_320 ; BYPASS IF NOT
838 02F7 80 8D 0090 R 90 ; OR #DSK_STATE[D1],MED_DET+RATE_250 ; SET TO 320/360
839 02FC EB 3E ; JMP SHORT 50 ;
840 ;
841 02FE ; NOT_320: ;
842 02FE F6 06 008F R 01 ; TEST #HF_CNTRL,DUAL ; TEST CONTROLLER I.D.
843 0303 74 0A ; JZ CALL MED_CHANGE ; CHECK FOR TIME_OUT
844 0305 E8 05F5 R ; CALL #DSKETTE_STATUS,TIME_OUT ;
845 0308 80 3E 0041 R 80 ; CMP #DSKETTE_STATUS,TIME_OUT ;
846 030D 74 2D ; JZ 50 ; IF TIME OUT TELL CALLER
847 ;
848 030F 4E ; S3: DEC S1 ; CHECK FOR 320/360K IN 1.2M DRIVE
849 0310 75 07 ; JNZ NOT_320_12 ; BYPASS IF NOT
850 0312 80 8D 0090 R 70 ; OR #DSK_STATE[D1],MED_DET+DBL_STEP+RATE_300 ; SET STATE
851 0317 EB 23 ; JMP SHORT 50 ;
852 ;
853 0319 ; NOT_320_12: ;
854 0319 4E ; DEC S1 ; CHECK FOR 1.2M MEDIA IN 1.2M DRIVE
855 031A 75 07 ; JNZ NOT_12 ; BYPASS IF NOT
856 031C 80 8D 0090 R 10 ; OR #DSK_STATE[D1],MED_DET+RATE_500 ; SET STATE VARIABLE
857 0321 EB 19 ; JMP RETURN TO CALLER
858 ;
859 0323 ; NOT_12: ;
860 0323 4E ; DEC S1 ; CHECK FOR SET DASD TYPE 04
861 0324 75 20 ; JNZ FS_ERR ; BAD COMMAND EXIT IF NOT VALID TYPE
862 ;
863 0326 F6 85 0090 R 04 ; TEST #DSK_STATE[D1],DRY_DET ; DRIVE DETERMINED ?
864 0328 74 09 ; JZ ASSUME ; IF STILL NOT DETERMINED ASSUME
865 032D 80 50 ; MOV AL,MED_DET+RATE_300 ;
866 032F F6 85 0090 R 02 ; TEST #DSK_STATE[D1],FMT_CAPA ; MULTIPLE FORMAT CAPABILITY ?
867 0334 75 02 ; JNZ OR_IT_IN ; IF 1.2 M THEN DATA RATE 300
868 ;
869 0336 ; ASSUME: MOV AL,MED_DET+RATE_250 ; SET UP
870 0336 80 90 ;
871 ;
872 0338 ; OR_IT_IN: OR #DSK_STATE[D1],AL ; OR IN THE CORRECT STATE
873 0338 08 85 0090 R ;
874 ;
875 033C ; S0: ;
876 033C E8 0432 R ; CALL XLAT_OLD ; TRANSLATE STATE TO COMPATIBLE MODE
877 033F E8 0832 R ; CALL SETUP_END ; VARIOUS CLEANUPS
878 0342 5B ; POP BX ; GET SAVED AL TO BL
879 0343 8A C3 ; MOV AL,BL ; PUT BACK FOR RETURN
880 0345 C3 ; RET ;
881 ;
882 0346 ; FS_ERR: MOV #DSKETTE_STATUS,BAD_CMD ; UNKNOWN STATE,BAD COMMAND
883 0346 C6 06 0041 R 01 ; JMP SHORT 50 ;
884 0348 EB EF ;
885 ;
886 034D ; FORMAT_SET ENDP
887 ;
888 ;
889 ;
890 ;
891 ;
892 ;
893 ; ON ENTRY: [BP] = SECTOR PER TRACK ;
894 ; [BP+1] = TRACK # ;
895 ; D1 = DRIVE # ;
896 ;
897 ; ON EXIT: #DSKETTE_STATUS REFLECTS STATUS ;
898 ; IF NO ERROR: ;

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999 ; AH = 0 ;
900 ; CY = 0 ;
901 ; ES = SEGMENT OF MEDIA/DRIVE PARAMETER TABLE ;
902 ; DI [(BP+6) = OFFSET OF MEDIA/DRIVE PARAMETER TABLE ;
903 ; IF ERROR: ;
904 ; AH = 0DSKETTE_STATUS ;
905 ; CY = 1 ;
906 ;
-----
907 0340 SET_MEDIA PROC NEAR
908 0340 EB 0404 R CALL XLAT_NEW ; TRANSLATE STATE TO PRESENT ARCH.
909 0350 33 DB XOR BX,BX ; ZERO INDEX POINTER
910 0352 80 7E 01 27 CMP BYTE PTR [BP+1],39 ; MAX. TRACK = 40 ?
911 0356 75 0B JNE TBL_CHK1
912 0358 F6 85 0090 R 01 TEST 0DSK_STATE[DI],TRK_CAPA ; 80 TRACK DRIVE ?
913 035D 74 16 JZ MD_FND ; POINT TO TABLE ENTRY 1
914 035F B3 03 MOV MOV BL,3 ; POINT TO TABLE ENTRY 2
915 0361 EB 12 JMP SHORT MD_FND
916 0363
917 0363 B3 06 MOV BL,6 ; POINT TO TABLE ENTRY 3
918 0365 80 7E 00 0F CMP BYTE PTR [BP],15 ; SECTORS/TRACK = 15 ?
919 0369 74 0A JE MD_FND
920 036B B3 12 MOV BL,18 ; POINT TO TABLE ENTRY 6
921 036D 80 7E 00 12 CMP BYTE PTR [BP],18 ; SECTORS/TRACK = 18 ?
922 0371 74 02 JE MD_FND
923 0373 B3 0C MOV BL,12 ; POINT TO TABLE ENTRY 4
924 0375
925 0375 2E: 8B 9F 0001 R MD_FND: MOV BX,CS:WORD PTR DR_TYPE[BX+1] ; DI = MEDIA/DRIVE PARAMETER TAB
LE
926 037A 2E: 8A 47 04 MOV AL,CS:[BX].MD_SEC_TRK ; GET SECTOR/TRACK
927 037E 2E: 8A 67 0B MOV AH,CS:[BX].MD_MAX_TRK ; GET MAX. TRACK #
928 0382 39 46 00 CMP [BP],AX ; MATCH ?
929 0385 75 23 JNE ER_RTN ; NOT SUPPORTED
930 0387 2E: 8A 47 0C MOV AL,CS:[BX].MD_RATE ; GET RATE
931 038B 3C 40 CMP AL,RATE_300 ; DOUBLE STEP REQUIRED FOR RATE 300
932 038D 75 02 JNE MD_SET
933 038F 0C 20 OR AL,DBL_STEP
934 0391
935 0391 89 5E 06 MD_SET: MOV [BP+6],BX ; SAVE TABLE POINTER IN STACK
936 0394 0C 10 OR AL,MED_DET ; SET MEDIA ESTABLISHED
937 0396 80 A5 0090 R 0F AND 0DSK_STATE[DI],NOT MED_DET+DBL_STEP+RATE_MSK ; CLEAR STATE
938 039B 0F 85 0090 R 0F CHM 0DSK_STATE[DI],AL ; SET STATE
939 039F 8C C8 MOV AX,CS ; SEGMENT MEDIA/DRIVE PARAMETER TABLE
940 03A1 8E C0 MOV ES,AX ; ES IS SEGMENT OF TABLE
941 03A3
942 03A3 E8 0432 R SM_RTN: CALL XLAT_OLD ; TRANSLATE STATE TO COMPATIBLE MODE
943 03A6 E8 0832 R CALL SETUP_END ; VARIOUS CLEANUPS
944 03A9 C3 RET
945 03AA
946 03AA C6 06 0041 R 0C ER_RTN: MOV 0DSKETTE_STATUS,MED_NOT_FND ; ERROR, MEDIA TYPE NOT FOUND
947 03AF EB F2 JMP SM_RTN
948 03B1 SET_MEDIA ENDP
-----
949
950 ;
951 ; DR_TYPE_CHECK ;
952 ; CHECK IF THE GIVEN DRIVE TYPE IN REGISTER (AL) ;
953 ; IS SUPPORTED IN BIOS DRIVE TYPE TABLE ;
954 ; ON ENTRY: ;
955 ; AL = DRIVE TYPE ;
956 ; ON EXIT: ;
957 ; CS = SEGMENT OF MEDIA/DRIVE PARAMETER TABLE (CODE) ;
958 ; CY = 0 DRIVE TYPE SUPPORTED ;
959 ; BX = OFFSET TO MEDIA/DRIVE PARAMETER TABLE ;
960 ; CY = 1 DRIVE TYPE NOT SUPPORTED ;
961 ; REGISTERS ALTERED: BX ;
-----
962
963 03B1 DR_TYPE_CHECK PROC NEAR
964 03B1 50 PUSH CX
965 03B2 51 PUSH CX
966 03B3 33 DB XOR BX,BX ; BX = INDEX TO DR_TYPE TABLE
967 03B5 B9 0006 MOV CX,DR_CNT ; CX = LOOP COUNT
968 03B8
969 03B8 2E: 8A 7A 0000 R TYPE_CHK: MOV AH,CS:DR_TYPE[BX] ; GET DRIVE TYPE
970 03BD 3A C4 CMP AL,AH ; DRIVE TYPE MATCH ?
971 03BF 74 08 JE DR_TYPE_VALID ; YES, RETURN WITH CARRY RESET
972 03C1 83 C3 03 ADD BX,3 ; CHECK NEXT DRIVE TYPE
973 03C4 E2 F2 LOOP TYPE_CHK ; CHECK RETURN DRIVE TYPE
974 03C6 F9 STC ; DRIVE TYPE NOT FOUND IN TABLE
975 03C7 EB 05 JMP SHORT TYPE_RTN
976 03C9
977 03C9 2E: 8B 9F 0001 R DR_TYPE_VALID: MOV BX,CS:WORD PTR DR_TYPE[BX+1] ; BX = MEDIA TABLE
978 03CE
979 03CE 59 POP CX
980 03CF 58 POP AX
981 03D0 C3 RET
982 03D1 DR_TYPE_CHECK ENDP
-----
983
984 ;
985 ; SEND_SPEC ;
986 ; SEND THE SPECIFY COMMAND TO CONTROLLER USING DATA FROM ;
987 ; THE DRIVE PARAMETER TABLE POINTED BY 0DSK_POINTER ;
988 ; ON ENTRY: ;
989 ; 0DISK_POINTER = DRIVE PARAMETER TABLE ;
990 ; ON EXIT: ;
991 ; NONE ;
992 ; REGISTERS ALTERED: AX ;
-----
992 03D1 SEND_SPEC PROC NEAR
993 03D1 B8 03EB R MOV AX,OFFSET SPECBAC ; LOAD ERROR ADDRESS
994 03D4 50 PUSH AX ; PUSH NEC_OUT ERROR RETURN
995 03D5 B4 03 MOV AH,03H ; SPECIFY COMMAND
996 03D7 E8 09F0 R CALL NEC_OUTPUT ; OUTPUT THE COMMAND
997 03DA 2A D2 SUB DL,DL ; FIRST SPECIFY BYTE
998 03DC E8 08FE R CALL GET_PARM ; GET PARAMETER TO AH
999 03DF E8 09F0 R CALL NEC_OUTPUT ; OUTPUT THE COMMAND
1000 03E2 B2 01 MOV DL,T ; SECOND SPECIFY BYTE
1001 03E4 E8 08FE R CALL GET_PARM ; GET PARAMETER TO AH
1002 03E7 E8 09F0 R CALL NEC_OUTPUT ; OUTPUT THE COMMAND
1003 03EA 58 POP AX ; POP ERROR RETURN
1004 03EB
1005 03EB C3 SPECBAC: RET
1006 03EC SEND_SPEC ENDP
-----
1007
1008 ;
1009 ; SEND_SPEC MD ;
1010 ; SEND THE SPECIFY COMMAND TO CONTROLLER USING DATA FROM ;
1011 ; THE MEDIA/DRIVE PARAMETER TABLE POINTED BY (CS;BX) ;

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1012 ; ON ENTRY: CS:BX = MEDIA/DRIVE PARAMETER TABLE ;
1013 ; ON EXIT : NONE ;
1014 ; REGISTERS ALTERED: AX,CX,DX ;
1015 ----- ;
1016 03EC SEND_SPEC_MD PROC NEAR
1017 03EC B8 0403 R MOV AX,OFFSET SPEC_ESBAC ; LOAD ERROR ADDRESS
1018 03EF 50 PUSH AX ; PUSH NEC_OUT ERROR RETURN
1019 03F0 B4 03 MOV AH,03H ; SPECIFY COMMAND
1020 03F2 EB 09F0 R CALL NEC_OUTPUT ; OUTPUT THE COMMAND
1021 03F5 2E: 8A 27 MOV AH,CS:[BX].MD_SPEC1 ; GET 1ST SPECIFY BYTE
1022 03FB EB 09F0 R CALL NEC_OUTPUT ; OUTPUT THE COMMAND
1023 03FB 2E: 8A 67 01 MOV AH,CS:[BX].MD_SPEC2 ; GET SECOND SPECIFY BYTE
1024 03FF EB 09F0 R CALL NEC_OUTPUT ; OUTPUT THE COMMAND
1025 0402 58 POP AX ; POP ERROR RETURN
1026 0403 SPEC_ESBAC:
1027 0403 C3 RET
1028 0404 SEND_SPEC_MD ENDP
1029 ----- ;
1030 ; XLAT_NEW ;
1031 ; TRANSLATES DISKETTE STATE LOCATIONS FROM COMPATIBLE ;
1032 ; MODE TO NEW ARCHITECTURE. ;
1033 ;
1034 ; ON ENTRY: DI : DRIVE ;
1035 ----- ;
1036 0404 XLAT_NEW PROC NEAR
1037 0404 F6 06 00BF R 01 TEST #HF_CNTRL_DUAL ; TEST CONTROLLER I.D.
1038 0409 74 22 JZ XN_OUT
1039 040B 83 FF 01 CMP DI,1 ; VALID DRIVE ?
1040 040E 77 1D JA XN_OUT ; IF INVALID BACK
1041 0410 80 B8 0090 R 00 CMP #DSK_STATE[DI],0 ; NO DRIVE ?
1042 0415 74 17 JZ DO_DET ; IF NO DRIVE ATTEMPT DETERMINE
1043 0417 8B CF MOV CX,DI ; CX = DRIVE NUMBER
1044 0419 D0 E1 SHL CL,1 ; CL = SHIFT COUNT, A=0, B=4
1045 041B D0 E1 SHL CL,1
1046 041D A0 00BF R MOV AL,#HF_CNTRL ; DRIVE INFORMATION
1047 0420 D2 C8 ROR AL,CL ; TO LOW NIBBLE
1048 0422 24 07 AND AL,DRV_DET+FMT_CAPA+TRK_CAPA ; KEEP DRIVE BITS
1049 0424 80 B8 0090 R FB AND #DSK_STATE[DI],NOT DRV_DET+FMT_CAPA+TRK_CAPA ; KEEP DRIVE BITS
1050 0429 08 B8 0090 R OR #DSK_STATE[DI],AL ; UPDATE DRIVE STATE
1051 042D XN_OUT:
1052 042D C3 RET
1053 ;
1054 042E DO_DET:
1055 042E EB 0B2B R CALL DRIVE_DET ; TRY TO DETERMINE
1056 0431 C3 RET
1057 ;
1058 0432 XLAT_NEW ENDP
1059 ----- ;
1060 ; XLAT_OLD ;
1061 ; TRANSLATES DISKETTE STATE LOCATIONS FROM NEW ;
1062 ; ARCHITECTURE TO COMPATIBLE MODE. ;
1063 ;
1064 ; ON ENTRY: DI : DRIVE ;
1065 ----- ;
1066 0432 XLAT_OLD PROC NEAR
1067 0432 F6 06 00BF R 01 TEST #HF_CNTRL_DUAL ; TEST CONTROLLER I.D.
1068 0437 74 79 JZ XO_OUT
1069 0439 83 FF 01 CMP DI,1 ; VALID DRIVE ?
1070 043C 77 74 JA XO_OUT ; IF INVALID BACK
1071 043E 80 BD 0090 R 00 CMP #DSK_STATE[DI],0 ; NO DRIVE ?
1072 0443 74 6D JZ XO_OUT ; IF NO DRIVE TRANSLATE DONE
1073 ;
1074 ;----- TEST FOR SAVED DRIVE INFORMATION ALREADY SET
1075 ;
1076 0445 8B CF MOV CX,DI ; CX = DRIVE NUMBER
1077 0447 D0 E1 SHL CL,1 ; CL = SHIFT COUNT, A=0, B=4
1078 0449 D0 E1 SHL CL,1
1079 044B B4 02 MOV AH,FMT_CAPA ; LOAD MULTI DATA RATE BIT MASK
1080 044D D2 CC ROR AH,CL ; ROTATE BY MASK
1081 044F B4 26 00BF R TEST #HF_CNTRL,AH ; MULTI-DATA RATE DETERMINED ?
1082 0453 75 16 JNZ SAVE_SET ; IF SO, NO NEED TO RE-SAVE
1083 ;
1084 ;----- ERASE DRIVE BITS IN #HF_CNTRL FOR THIS DRIVE
1085 ;
1086 0455 B4 07 MOV AH,DRV_DET+FMT_CAPA+TRK_CAPA ; MASK TO KEEP
1087 0457 D2 CC ROR AH,CL ; FIX MASK TO KEEP
1088 0459 F6 D4 NOT AH ; TRANSLATE MASK
1089 045B 20 26 00BF R AND #HF_CNTRL,AH ; KEEP BITS FROM OTHER DRIVE INTACT
1090 ;
1091 ;----- ACCESS CURRENT DRIVE BITS AND STORE IN #HF_CNTRL
1092 ;
1093 045F 8A 05 0090 R MOV AL,#DSK_STATE[DI] ; ACCESS STATE
1094 0463 24 07 AND AL,DRV_DET+FMT_CAPA+TRK_CAPA ; KEEP DRIVE BITS
1095 0465 D2 C8 ROR AL,CL ; FIX FOR THIS DRIVE
1096 0467 08 B8 00BF R OR #HF_CNTRL,AL ; UPDATE SAVED DRIVE STATE
1097 ;
1098 ;----- TRANSLATE TO COMPATIBILITY MODE
1099 ;
1100 046B SAVE_SET:
1101 046B 8A 05 0090 R MOV AH,#DSK_STATE[DI] ; ACCESS STATE
1102 046F 8A FC MOV BH,AH ; TO BH FOR LATER
1103 0471 80 E4 C0 AND AH,RATE_MSK ; KEEP ONLY RATE
1104 0474 80 FC C0 CMP AH,RATE_500 ; RATE 500 ?
1105 0477 74 10 JZ CHK_HDR_80T ; YES 1.2/1.2 OR HIGH DATA RATE 80 TRK
1106 0479 B0 01 MOV AL,#3DIU ; AL = 360 IN 1.2 UNESTABLISHED
1107 047B 80 FC 40 AND AH,RATE_300 ; RATE 300 ?
1108 047E 75 16 JNZ CHK_360 ; NO, 360/360 / 720/720
1109 0480 F6 C7 20 TEST BH,DBL_STEP ; YES, DOUBLE STEP ?
1110 0483 75 1D JNZ TST_DET ; YES, MUST BE 360 IN 1.2
1111 ;
1112 0485 UNKN0:
1113 0485 B0 07 MOV AL,MED_UNK ; 'NONE OF THE ABOVE'
1114 0487 EB 20 JMP SHORT AL_SET ; PROCESS COMPLETE
1115 ;
1116 0489 CHK_HDR_80T:
1117 0489 EB 08CF R CALL CMOS_TYPE ; RETURN DRIVE TYPE IN (AL)
1118 048C 72 F7 JC UNKN0 ; ERROR, SET 'NONE OF THE ABOVE'
1119 048E 3C 02 CMP AL,2 ; 2MB DRIVE ?
1120 0490 75 F3 JNE UNKN0 ; NO, GO SET 'NONE OF THE ABOVE'
1121 0492 B0 02 MOV AL,M1DIU ; AL = 1.2 IN 1.2 UNESTABLISHED
1122 0494 EB 0C JMP SHORT TST_DET
1123 ;
1124 0496 CHK_250:
1125 0496 B0 00 MOV AL,#3D3U ; AL = 360 IN 360 UNESTABLISHED

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1126 0498 80 FC 80          CMP     AH,RATE_250          ; RATE 250 ?
1127 049B 75 E8            JNZ     UNKNO                ; IF 50 FALL THRU
1128 049D F6 C7 01        TEST    BH,TRK_CAPA         ; 80 TRACK CAPABILITY ?
1129 04A0 75 E3            JNZ     UNKNO                ; IF 50 JUMP, FALL THRU TEST DET
1130
1131 04A2                    TST_DET:
1132 04A2 F6 C7 10        TEST    BH,MED_DET          ; DETERMINED ?
1133 04A5 74 02            JZ      AL_SET              ; IF NOT THEN SET
1134 04A7 04 03            ADD     AL,3                 ; MAKE DETERMINED/ESTABLISHED
1135
1136 04A9                    AL_SET:
1137 04A9 80 A5 0090 R F8  AND     0DSK_STATE[D1],NOT DRV_DET+FWT_CAPA+TRK_CAPA ; CLEAR DRIVE
1138 04AE 08 85 0090 R     OR      0DSK_STATE[D1],AL   ; REPLACE WITH COMPATIBLE MODE
1139 04B2                    XO_OUT:
1140 04B2 C3              RET
1141 04B3                    XLAT_OLD      ENDP
1142
1143
1144 -----
1145 ; RD_WR_VF
1146 ; COMMON READ, WRITE AND VERIFY;
1147 ; MAIN LOOP FOR STATE RETRIES.
1148 ;
1149 ; ON ENTRY:  AH : READ/WRITE/VERIFY DEC PARAMETER
1150 ;           AL : READ/WRITE/VERIFY DMA PARAMETER
1151 ;
1152 ; ON EXIT:   0DSKETTE_STATUS, CY REFLECT STATUS OF OPERATION
1153 -----
1153 04B3                    RD_WR_VF      PROC    NEAR
1154 04B3 50                PUSH    AX                   ; SAVE DMA, NEC PARAMETERS
1155 04B4 EB 0404 R        CALL    XLAT_NEW            ; TRANSLATE STATE TO PRESENT ARCH.
1156 04B7 EB 0561 R        CALL    SETUP_STATE        ; INITIALIZE START AND END RATE
1157 04BA 58                POP     AX                   ; RESTORE READ/WRITE/VERIFY
1158
1159 04BB                    DO_AGAIN:
1160 04BB F6 06 00B8 R 01  TEST    0HF_CNTRL,DUAL      ; TEST CONTROLLER I.D.
1161 04C0 74 0A            JZ      RWV                 ;
1162 04C2 50                PUSH    AX                   ; SAVE READ/WRITE/VERIFY PARAMETER
1163 04C3 EB 05F5 R        CALL    MED_CHANGE         ; MEDIA CHANGE AND RESET IF CHANGED
1164 04C6 58                POP     AX                   ; RESTORE READ/WRITE/VERIFY
1165 04C7 73 03            JNC     RWV                 ;
1166 04C9 E9 0552 R        JMP     RWV_END            ;
1167 04CC
1168 04CC 50                RWV:      PUSH    AX                   ; SAVE READ/WRITE/VERIFY PARAMETER
1169
1170 04CD 8A B5 0090 R     MOV     DH,0DSK_STATE[D1]   ; GET RATE STATE OF THIS DRIVE
1171 04D1 80 E6 C0        AND     DH,RATE_MSK         ; KEEP ONLY RATE
1172
1173 04D4 EB 08CF R        CALL    CMOS_TYPE          ; RETURN DRIVE TYPE IN (AL)
1174 04D7 0A C0            OR      AL,AL               ; TEST FOR NO DRIVE
1175 04D9 74 2F            JZ      RWV_ASSUME         ; ASSUME TYPE, USE MAX TRACK
1176 04DB EB 03B1 R     CALL    DR_TYPE_CHECK      ; RTN CS:BX = MEDIA/DRIVE PARAM TBL
1177 04DE 72 2A            JC      RWV_ASSUME         ; TYPE NOT IN TABLE ASSUME DEFAULT
1178
1179 ;--- SEARCH FOR MEDIA/DRIVE PARAMETER TABLE
1180
1181 04E0 57                PUSH    DI                   ; SAVE DRIVE #
1182 04E1 33 DB            XOR     BX,BX               ; BX = INDEX TO DR_TYPE TABLE
1183 04E3 B9 0006        MOV     CX,DR_CNT          ; CX = LOOP COUNT
1184 04E6
1185 04E6 2E: 8A AT 0000 R  RWV_DR_SEARCH:  MOV     AH,CS:DR_TYPE[BX]   ; GET DRIVE TYPE
1186 04EB 80 E4 7F        AND     AH,BIT7OFF         ; MASK OUT MSB
1187 04EE 3A C4            CMP     AL,AH               ; DRIVE TYPE MATCH ?
1188 04F0 75 0B            JNE     RWV_NXT_MD         ; NO, CHECK NEXT DRIVE TYPE
1189 04F2
1190 04F2 2E: 8B BF 0001 R  RWV_DR_FND:  MOV     DI,WORD PTR CS:DR_TYPE[BX+1] ; DI = MEDIA/DRIVE PARAMETER TABLE
1191 04F7
1192 04F7 2E: 3A 75 0C     RWV_MD_SEARCH:  CMP     DH,CS:[D1].MD_RATE ; MATCH ?
1193 04FB 74 1D            JE      RWV_MD_FND         ; YES, GO GET 1ST SPECIFY BYTE
1194 04FD
1195 04FD B3 C3 03        RWV_NXT_MD:  ADD     BX,3                 ; CHECK NEXT DRIVE TYPE
1196 0500 E2 E4            LOOP   RWV_DR_SEARCH       ;
1197 0502 C6 06 0041 R FF  MOV     0DSKETTE_STATUS,OFFH ; FORCE IT TO RETRY
1198 0507 5F              POP     DI                   ; RESTORE DRIVE #
1199 0508 EB 3F              JMP     SHORT CHK_RET      ; GO RETRY
1200
1201 ;--- ASSUME PRIMARY DRIVE IS INSTALLED AS SHIPPED
1202
1203 050A                    RWV_ASSUME:
1204 050A BB 0000 E        MOV     BX,OFFSET MD_TBL1   ; POINT TO 40 TK 250 KBS
1205 050D F6 85 0090 R 01  TEST    0DSK_STATE[D1],TRK_CAPA ; TEST FOR 80 TRACK
1206 0512 74 09            JZ      RWV_MD_FND1        ; MUST BE 40 TRACK
1207 0514 BB 0000 E        MOV     BX,OFFSET MD_TBL3   ; POINT TO 80 TK 500 KBS
1208 0517 EB 04 90        JMP     RWV_MD_FND1        ; GO SET SPECIFY PARAMETERS
1209
1210 ;--- CS:BX POINTS TO MEDIA/DRIVE PARAMETER TABLE
1211
1212 051A                    RWV_MD_FND:
1213 051A 8B DF            MOV     BX,DI                ; BX = MEDIA/DRIVE PARAMETER TABLE
1214 051C 5F              POP     DI                   ; RESTORE DRIVE #
1215 051D
1216
1217 ;--- SEND THE SPECIFY COMMAND TO THE CONTROLLER
1218
1219 051D EB 03EC R        CALL    SEND_SPEC_MD       ;
1220 0520 EB 0658 R        CALL    CHK_LA_STRATE      ; IF !=1 ATTEMPT RATE IS SAME AS LAST RATE
1221 0523 74 03            JZ      RWV_DBL            ; YES, SKIP SEND RATE COMMAND
1222 0525 EB 0637 R        CALL    SEND_RATE         ; SEND DATA RATE TO NEC
1223 0528
1224 0528 53                PUSH    BX                   ; SAVE MEDIA/DRIVE PARAM ADDRESS
1225 0529 EB 084C R     CALL    SETUP_DBL         ; CHECK FOR DOUBLE STEP
1226 052C 5B              POP     BX                   ; RESTORE ADDRESS
1227 052D 72 1A            JC      CHK_RET            ; ERROR FROM READ ID, POSSIBLE RETRY
1228 052F 58                POP     AX                   ; RESTORE NEC, DMA COMMAND
1229 0530 50                PUSH    AX                   ; SAVE NEC COMMAND
1230 0531 53                PUSH    BX                   ; SAVE MEDIA/DRIVE PARAM ADDRESS
1231 0532 EB 0668 R     CALL    DMA_SETUP         ; SET UP THE DMA
1232 0535 5B              POP     BX                   ; RESTORE ADDRESS
1233 0536 58                POP     AX                   ; RESTORE NEC COMMAND
1234 0537 72 1F            JC      RWV_BAC            ; CHECK FOR DMA BOUNDARY ERROR
1235 0539 50                PUSH    AX                   ; SAVE NEC COMMAND
1236 053A 53                PUSH    BX                   ; SAVE MEDIA/DRIVE PARAM ADDRESS
1237 053B EB 06CB R     CALL    NEC_INIT          ; INITIALIZE NEC
1238 053E 5B              POP     BX                   ; RESTORE ADDRESS
    
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SECTION 5

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1239 053F 72 08          JC      CHK_RET          ; IF ERROR DO NOT SEND MORE COMMANDS
1240 0541 E8 06F1 R      CALL   RWW_COM          ; OP CODE COMMON TO READ/WRITE/VERIFY
1241 0544 72 03          JC      CHK_RET          ; IF ERROR DO NOT SEND MORE COMMANDS
1242 0546 E8 0727 R      CALL   NEC_TERM         ; TERMINATE, GET STATUS, ETC.
1243
1244 0549
1245 0549 E8 07BE R      CHK_RET:                ; CHECK FOR SETUP RETRY
1246 054C 58            POP    AX               ; RESTORE READ/WRITE/VERIFY PARAMETER
1247 054D 73 03        JNC    RWW_END         ; CY = 0 NO RETRY
1248 054F E9 04BB R      JMP    DO_AGAIN        ; CY = 1 MEANS RETRY
1249
1250 0552
1251 0552 E8 077B R      CALL   DSTATE          ; ESTABLISH STATE IF SUCCESSFUL
1252 0555 E8 0805 R      CALL   NUM_TRANS       ; AL = NUMBER TRANSFERRED
1253
1254 0558
1255 0558 50            RWW_BAC:                ; BAD DMA ERROR ENTRY
1256 0559 E8 0432 R      PUSH  AX               ; SAVE NUMBER TRANSFERRED
1257 0560 58            CALL   XLAT_OLD        ; TRANSLATE STATE TO COMPATIBLE MODE
1258 0560 E8 0832 R      POP    AX              ; RESTORE NUMBER TRANSFERRED
1259 0560 C3            CALL   SETUP_END       ; VARIOUS CLEANUPS
1260 0561
1261
1262
1263
1264 0561
1265 0561 F6 06 00BF R 01  PROC   NEAR            ;
1266 0566 74 37        TEST   #HF_CNTRL_DUAL  ; TEST CONTROLLER I.D.
1267 0568 F6 85 0090 R 10 JZ     JIC              ;
1268 056D 75 49        TEST   #DSK_STATE[D1],MED_DET ; MEDIA DETERMINED ?
1269 056F B8 4080    MOV    AX,RATE_300*H+RATE_250 ; NO STATES IF DETERMINED
1270 0572 F6 85 0090 R 04 TEST   #DSK_STATE[D1],DRV_DET ; AH = START RATE, AL = END RATE
1271 0571 74 0C        JZ     AX_SET           ; DRIVE ?
1272 0579 B0 00        MOV    AL,RATE_500     ; DO NOT KNOW DRIVE
1273 057B F6 85 0090 R 02 TEST   #DSK_STATE[D1],FMT_CAPA ; SET UP FOR 1.2 M END RATE
1274 0580 75 03        JNZ    AX_SET           ; 1.2 M ?
1275 0582 B8 8080    MOV    AX,AX            ; JUMP WITH FIXED END RATE
1276
1277 0585
1278 0585 80 A5 0090 R 1F AND    #DSK_STATE[D1],NOT_RATE_MSK+DBL_STEP ; TURN OFF THE RATE
1279 058A 08 A5 0090 R 1F OR     #DSK_STATE[D1],AH ; RATE FIRST TO TRY
1280 058E 80 26 008B R F3 AND    #LASTRATE,NOT_STRT_MSK ; ERASE LAST TO TRY RATE BITS
1281 0593 D0 C8        ROR    AL,1            ; TO OPERATION LAST RATE LOCATION
1282 0595 D0 C8        ROR    AL,1
1283 0597 D0 C8        ROR    AL,1
1284 0599 D0 C8        ROR    AL,1
1285 059B 08 06 008B R OR     #LASTRATE,AL    ; LAST RATE
1286 059F
1287 059F C3            JIC:    RET
1288 05A0
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1292 05A0
1293 05A0 F6 06 00BF R 01  PROC   NEAR            ;
1294 05A5 74 49        TEST   #HF_CNTRL_DUAL  ; TEST CONTROLLER I.D.
1295 05A7 F6 85 0090 R 10 JZ     F1_OUT          ;
1296 05AC 75 42        TEST   #DSK_STATE[D1],MED_DET ; IS MEDIA ESTABLISHED
1297 05AE E8 08FC R    JC     CL_DRV         ; IF SO RETURN
1298 05B1 72 3E        JC     CL_DRV         ; RETURN DRIVE TYPE IN AL
1299 05B3 FE C8        DEC    AL              ; ERROR IN CMOS ASSUME NO DRIVE
1300 05B5 78 3A        JS    CL_DRV          ; MAKE ZERO ORIGIN
1301 05B7 8A A5 0090 R MOV    AH,#DSK_STATE[D1] ; NO DRIVE IF AL 0
1302 05BB 80 E4 0F    AND    AH,NOT_MED_DET+DBL_STEP+RATE_MSK ; AH = CURRENT STATE
1303 05BE 0A C0        OR     AL,AL           ; CLEAR
1304 05C0 75 05        JNZ    N_360          ; CHECK FOR 360
1305 05C2 80 CC 90    OR     AH,MED_DET+RATE_250 ; IF 360 WILL BE 0
1306 05C5 E8 25 13    JMP    SHORT_SKP_STATE ; ESTABLISH MEDIA
1307
1308 05C7
1309 05C7 FE C8        N_360:  DEC    AL              ; SKIP OTHER STATE PROCESSING
1310 05C9 75 05        JNZ    N_12           ; 1.2 M DRIVE
1311 05CB 80 CC 10    FI_RATE:OR AH,MED_DET+RATE_500 ; JUMP IF NOT
1312 05CE E8 1C 13    JMP    SHORT_SKP_STATE ; SET FORMAT RATE
1313
1314 05D0
1315 05D0 FE C8        N_12:   DEC    AL              ; SKIP OTHER STATE PROCESSING
1316 05D2 75 0F        JNZ    N_720          ; CHECK FOR TYPE 3
1317 05D4 F6 C4 04    TEST   AH,DRV_DET    ; JUMP IF NOT
1318 05D7 74 10        JZ     !SNT_12        ; IS DRIVE DETERMINED
1319 05D9 F6 C4 02    TEST   AH,FMT_CAPA   ; TREAT AS NON 1.2 DRIVE
1320 05DC 74 0B        JZ     !SNT_12        ; IS 1.2M
1321 05DE 80 CC 50    OR     AH,MED_DET+RATE_300 ; JUMP IF NOT
1322 05E1 E9 09        JMP    SHORT_SKP_STATE ; RATE 300
1323 05E3
1324 05E3 FE C8        N_720:  DEC    AL              ; CONTINUE
1325 05E5 75 0A        JNZ    CL_DRV         ; CHECK FOR TYPE 4
1326 05E7 E8 E2 13    JMP    SHORT_FI_RATE  ; NO DRIVE, CMOS BAD
1327 05E9
1328 05E9 80 CC 90        !SNT_12: OR     AH,MED_DET+RATE_250 ; MUST BE RATE 250
1329 05EC
1330 05EC 88 A5 0090 R  MOV    #DSK_STATE[D1],AH ; SKIP STATE
1331 05F0
1332 05F0 C3            FI_OUT:  RET
1333 05F1
1334 05F1 32 E4        CL_DRV:  XOR    AH,AH           ; STORE AWAY
1335 05F3 E8 F7 13    JMP    SHORT_SKP_STATE ; CLEAR STATE
1336 05F5
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1345 05F5
1346 05F5 F6 06 00BF R 01  PROC   NEAR            ;
1347 05FA 74 37        TEST   #HF_CNTRL_DUAL  ; TEST CONTROLLER I.D.
1348 05FC E8 0B21 R    CALL   READ_DSKCHNG   ; TEST CONTROLLER I.D.
1349 05FF 74 34        JZ     MC_OUT         ; READ DISK CHANGE LINE STATE
1350 0601 80 A5 0090 R EF AND    #DSK_STATE[D1],NOT_MED_DET ; BYPASS HANDLING DISK CHANGE LINE
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1353 ; ON THE NEXT OPERATION THE REQUIRED MOTOR START UP TIME WILL
1354 ; BE WAITED. (DRIVE MOTOR MAY GO OFF UPON DOOR OPENING).
1355
1356 0606 8B CF MOV CX,DI ; CL = DRIVE #
1357 0608 80 01 MOV AL,1 ; MOTOR ON BIT MASK
1358 060A D2 E0 SHL AL,CL ; TO APPROPRIATE POSITION
1359 060C F6 D0 NOT AL ; KEEP ALL BUT MOTOR ON
1360 060E FA CL,1 ; NO INTERRUPTS
1361 060F 20 06 003F R AND @MOTOR_STATUS,AL ; TURN MOTOR OFF INDICATOR
1362 0613 FB STI ; INTERRUPTS ENABLED
1363 0614 E8 0913 R CALL MOTOR_ON ; TURN MOTOR ON
1364
1365 ;----- THIS SEQUENCE OF SEKS IS USED TO RESET DISKETTE CHANGE SIGNAL
1366
1367 0617 E8 0092 R CALL DISK_RESET ; RESET NEC
1368 061A B5 01 MOV CH,0TH ; MOVE TO CYLINDER 1
1369 061C E8 0A14 R CALL SEEK ; ISSUE SEEK
1370 061F 32 ED XOR CH,CH ; MOVE TO CYLINDER 0
1371 0621 E8 0A14 R CALL SEEK ; NO INTERRUPTS
1372 0624 C6 06 0041 R MOV @DSKETTE_STATUS,MEDIA_CHANGE ; STORE IN STATUS
1373
1374 0629 E8 0B21 R OK1: CALL READ_DSKCHNG ; CHECK MEDIA CHANGED AGAIN
1375 062C 74 05 JZ OK2 ; IF ACTIVE, NO DISKETTE, TIMEOUT
1376
1377 062E C6 06 0041 R OK4: MOV @DSKETTE_STATUS,TIME_OUT; TIMEOUT IF DRIVE EMPTY
1378
1379 0633 F9 OK2: STC ; MEDIA CHANGED, SET CY
1380 0634 C3 RET
1381 0635 MC_OUT: CLC ; NO MEDIA CHANGED, CLEAR CY
1382 0635 FB RET
1383 0636 C3
1384 0637
1385
1386 ;-----
1387 ; SEND_RATE
1388 ; SENDS DATA RATE COMMAND TO NEC
1389 ; ON ENTRY: DI = DRIVE #
1390 ; ON EXIT: NONE
1391 ; REGISTERS ALTERED: NONE
1392
1392 0637 SEND_RATE PROC NEAR
1393 0637 F6 06 008F R 01 TEST @HF_CNTRL,DUAL ; TEST CONTROLLER I.D.
1394 063C 74 19 JZ C_S_OUT ; SAVE REG.
1395 063E 50 PUSH AX ; ELSE CLEAR LAST RATE ATTEMPTED
1396 063F 80 26 008B R 3F AND @LSTRATE,NOT SEND_MSK ; GET RATE STATE OF THIS DRIVE
1397 0644 8A 85 0090 R MOV AL,@DSK_STATE[DI] ; KEEP ONLY RATE BITS
1398 0648 24 C0 AND AL,SEND_MSK ; SAVE NEW RATE FOR NEXT CHECK
1399 064A 08 06 008B R OR @LSTRATE,AL ; MOVE TO BIT OUTPUT POSITIONS
1400 064E DD C0 ROL AL,1
1401 0650 DD C0 ROL AL,1
1402 0652 BA 03F7 MOV DX,03F7H ; OUTPUT NEW DATA RATE
1403 0655 EE OUT DX,AL
1404 0656 58 POP AX ; RESTORE REG.
1405 0657 C_S_OUT: RET
1406 0657 C3 SEND_RATE ENDP
1407 0658
1408
1409 ;-----
1410 ; CHK_LSTRATE
1411 ; CHECK PREVIOUS DATA RATE SENT TO THE CONTROLLER.
1412 ; ON ENTRY:
1413 ; DI = DRIVE #
1414 ; ON EXIT:
1415 ; ZF = 1 DATA RATE IS THE SAME AS LAST RATE SENT TO NEC
1416 ; ZF = 0 DATA RATE IS DIFFERENT FROM LAST RATE
1417 ; REGISTERS ALTERED: NONE
1418
1419 0658 CHK_LSTRATE PROC NEAR
1420 0658 50 PUSH AX ; SAVE REG
1421 0659 8A 26 008B R MOV AH,@LSTRATE ; GET LAST DATA RATE SELECTED
1422 065D 8A 85 0090 R MOV AL,@DSK_STATE[DI] ; GET RATE STATE OF THIS DRIVE
1423 0661 25 C0C0 AND AX,SEND_MSK*X ; KEEP ONLY RATE BITS OF BOTH
1424 0664 3A C4 CMP AL,AH ; COMPARE TO PREVIOUSLY TRIED
1425 ; ZF = 1 RATE IS THE SAME
1426 0666 58 POP AX ; RESTORE REG.
1427 0667 C3 RET
1428 0668 CHK_LSTRATE ENDP
1429

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1430 PAGE
1431 -----
1432 ; DMA_SETUP
1433 ; THIS ROUTINE SETS UP THE DMA FOR READ/WRITE/VERIFY
1434 ; OPERATIONS.
1435 ;
1436 ; ON ENTRY: AL = DMA COMMAND
1437 ;
1438 ; ON EXIT: 0DSKETTE_STATUS, CY REFLECT STATUS OF OPERATION ;
1439 ;-----
1440 DMA_SETUP PROC NEAR
1441 0668 FA ; DISABLE INTERRUPTS DURING DMA SET-UP
1442 0669 E6 0C ; SET THE FIRST/LAST F/F
1443 0668 EB 00 ; WAIT FOR I/O
1444 066D E6 0B ; OUTPUT THE MODE BYTE
1445 066F 3C 42 ; DMA VERIFY COMMAND
1446 0671 75 04 ; NO
1447 0673 93 C0 ; START ADDRESS
1448 0675 EB 15 ;
1449 0677 ;
1450 0677 8C 00 ; GET THE ES VALUE
1451 0679 D1 C0 ; ROTATE LEFT
1452 067B D1 C0 ;
1453 067D D1 C0 ;
1454 067F D1 C0 ;
1455 0681 8A E8 ;
1456 0683 24 F0 ; GET HIGHEST NIBBLE OF ES TO CH
1457 0685 03 46 02 ; ZERO THE LOW NIBBLE FROM SEGMENT
1458 0688 73 02 ; TEST FOR CARRY FROM ADDITION
1459 068A FE C5 ; CARRY MEANS HIGH 4 BITS MUST BE INC
1460 068C ;
1461 068C 50 ;
1462 068D E6 04 ; SAVE START ADDRESS
1463 068F EB 00 ; OUTPUT LOW ADDRESS
1464 0691 8A C4 ; WAIT FOR I/O
1465 0693 E6 04 ;
1466 0695 8A C5 ; OUTPUT HIGH ADDRESS
1467 0697 EB 00 ; GET HIGH 4 BITS
1468 0699 24 0F ; I/O WAIT STATE
1469 069B E6 81 ;
1470 ;
1471 ;----- DETERMINE COUNT
1472 ;
1473 069D BB C6 ; AL = # OF SECTORS
1474 069F 86 C4 ; AH = # OF SECTORS
1475 06A1 2A C0 ; AL = 0, AX = # OF SECTORS * 256
1476 06A3 D1 E8 ; AX = # SECTORS * 128
1477 06A5 50 ; SAVE # OF SECTORS * 128
1478 06A6 B2 03 ; GET BYTES/SECTOR PARAMETER
1479 06A8 EB 08FE R ;
1480 06AB 8A CC ; SHIFT COUNT (0=128, 1=256 ETC)
1481 06AD 58 E8 ; AX = # OF SECTORS * 128
1482 06AE D3 E0 ; SHIFT BY PARAMETER VALUE
1483 06B0 48 ; -1 FOR DMA VALUE
1484 06B1 50 ; SAVE COUNT VALUE
1485 06B2 E6 05 ; LOW BYTE OF COUNT
1486 06B4 EB 00 ; WAIT FOR I/O
1487 06B6 8A C4 ;
1488 06B8 E6 05 ; HIGH BYTE OF COUNT
1489 06BA FB ; RE-ENABLE INTERRUPTS
1490 06BB 59 ; RECOVER COUNT VALUE
1491 06BC 58 ; RECOVER ADDRESS VALUE
1492 06BD 03 C1 ; ADD, TEST FOR 64K OVERFLOW
1493 06BF B0 02 ; MODE FOR 8237
1494 06C1 E6 0A ; INITIALIZE THE DISKETTE CHANNEL
1495 ;
1496 06C3 73 05 ; JNC NO_BAD ; CHECK FOR ERROR
1497 06C5 C6 06 0041 R 09 ; MOV 0DSKETTE_STATUS,DMA_BOUNDARY ; SET ERROR
1498 ;
1499 06CA ;
1500 06CA C3 ; NO_BAD: RET ; CY SET BY ABOVE IF ERROR
1501 06CB ;
1502 ;-----
1503 ; DMA_SETUP ENDP
1504 ;-----
1505 ; NEC_INIT
1506 ; THIS ROUTINE SEEKS TO THE REQUESTED TRACK AND
1507 ; INITIALIZES THE NEC FOR THE READ/WRITE/VERIFY/FORMAT
1508 ; OPERATION.
1509 ;
1510 ; ON ENTRY: AH = NEC COMMAND TO BE PERFORMED
1511 ;
1512 ; ON EXIT: 0DSKETTE_STATUS, CY REFLECT STATUS OF OPERATION ;
1513 ;-----
1514 NEC_INIT PROC NEAR
1515 06CC 50 ; PUSH AX ; SAVE NEC COMMAND
1516 06CC E8 0913 R ; CALL MOTOR_ON ; TURN MOTOR ON FOR SPECIFIC DRIVE
1517 ;----- DO THE SEEK OPERATION
1518 06CF 8A 6E 01 ; MOV CH,[BP+1] ; CH = TRACK #
1519 06D2 EB 0A14 R ; CALL SEEK ; MOVE TO CORRECT TRACK
1520 06D5 58 ; POP AX ; RECOVER COMMAND
1521 06D6 72 18 ; JC ER_1 ; ERROR ON SEEK
1522 06D8 BB 06F0 R ; MOV BX,OFFSET ER_1 ; LOAD ERROR ADDRESS
1523 06DB 53 ; PUSH BX ; PUSH NEC_OUT ERROR RETURN
1524 ;----- SEND OUT THE PARAMETERS TO THE CONTROLLER
1525 ;
1526 ;
1527 06DC E8 09F0 R ; CALL NEC_OUTPUT ; OUTPUT THE OPERATION COMMAND
1528 06DF BB C6 ; MOV AX,S1 ; AH = HEAD #
1529 06E1 8B DF ; MOV BX,DI ; BL = DRIVE #
1530 06E3 D0 E4 ; SAL AH,1 ; MOVE IT TO BIT 2
1531 06E5 D0 E4 ; SAL AH,1 ;
1532 06E7 80 E4 04 ; AND AH,0000100B ; ISOLATE THAT BIT
1533 06EA 0A E3 ; OR AH,BL ; OR IN THE DRIVE NUMBER
1534 06EC EB 09F0 R ; CALL NEC_OUTPUT ; FALL THRU CY SET IF ERROR
1535 06EF 5B ; POP BX ; THROW AWAY ERROR RETURN
1536 06F0 ;
1537 06F0 C3 ; ER_1: RET
1538 06F1 ;
1539 ;-----
1540 ; RWV_COM
1541 ; THIS ROUTINE SENDS PARAMETERS TO THE NEC SPECIFIC
1542 ; TO THE READ/WRITE/VERIFY OPERATIONS.
1543 ;
1544 ;
    
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1544 ; ON ENTRY: CS:BX = ADDRESS OF MEDIA/DRIVE PARAMETER TABLE ;
1545 ; ON EXIT:  *DSKETTE_STATUS, CY REFLECT STATUS OF OPERATION ;
1546 ;-----
1547 R15: R15 COM PROC NEAR
1548 MOV AX,OFFSET ER_2 ; LOAD ERROR ADDRESS
1549 PUSH AX ; PUSH NEC_OUT ERROR RETURN
1550 MOV AH,[BP+1] ; OUTPUT TRACK #
1551 CALL NEC_OUTPUT
1552 MOV AX,S1 ; OUTPUT HEAD #
1553 CALL NEC_OUTPUT
1554 MOV AH,[BP] ; OUTPUT SECTOR #
1555 CALL NEC_OUTPUT
1556 MOV DL,3 ; BYTES/SECTOR PARAMETER FROM BLOCK
1557 CALL GET_PARM ; TO THE NEC
1558 CALL NEC_OUTPUT ; OUTPUT TO CONTROLLER
1559 MOV DL,4 ; EOT PARAMETER FROM BLOCK
1560 CALL GET_PARM ; TO THE NEC
1561 CALL NEC_OUTPUT ; OUTPUT TO CONTROLLER
1562 MOV AH,CS:[BX].MD_GAP ; GET GAP LENGTH
1563 R15:
1564 CALL NEC_OUTPUT
1565 MOV DL,6 ; DTL PARAMETER FROM BLOCK
1566 CALL GET_PARM ; TO THE NEC
1567 CALL NEC_OUTPUT ; OUTPUT TO CONTROLLER
1568 POP AX ; THROW AWAY ERROR EXIT
1569 ER_2:
1570 RET
1571 R15 COM PROC NEAR
1572 ;-----
1573 ; NEC_TERM ;
1574 ; THIS ROUTINE WAITS FOR THE OPERATION THEN ACCEPTS ;
1575 ; THE STATUS FROM THE NEC FOR THE READ/WRITE/VERIFY/ ;
1576 ; FORMAT OPERATION. ;
1577 ; ;
1578 ; ON EXIT: *DSKETTE_STATUS, CY REFLECT STATUS OF OPERATION ;
1579 ;-----
1580 NEC_TERM PROC NEAR
1581 ;----- LET THE OPERATION HAPPEN
1582
1583
1584 MOV SI ; SAVE HEAD #, # OF SECTORS
1585 CALL WAIT_INT ; WAIT FOR THE INTERRUPT
1586 PUSHF
1587 CALL RESULTS ; GET THE NEC STATUS
1588 JC SET_END_POP
1589 POPF
1590 JC SET_END ; LOOK FOR ERROR
1591
1592 ;----- CHECK THE RESULTS RETURNED BY THE CONTROLLER
1593
1594 CLD ; SET THE CORRECT DIRECTION
1595 MOV SI,OFFSET *NEC_STATUS ; POINT TO STATUS FIELD
1596 LODS ; GET ST0
1597 AND AL,1000000B ; TEST FOR NORMAL TERMINATION
1598 JZ SET_END ; TEST FOR ABNORMAL TERMINATION
1599 CMP AL,01000000B ; TEST FOR ABNORMAL TERMINATION
1600 JNB ; NOT ABNORMAL, BAD NEC
1601
1602 ;----- ABNORMAL TERMINATION, FIND OUT WHY
1603
1604 LODS *NEC_STATUS ; GET ST1
1605 SAL AL,1 ; TEST FOR EOT FOUND
1606 MOV AH,RECORD_NOT_FND
1607 JC J19
1608 SAL AL,1
1609 SAL AL,1
1610 MOV AH,BAD_CRC
1611 JC J19
1612 SAL AL,1 ; TEST FOR DMA OVERRUN
1613 MOV AH,BAD_DMA
1614 JC J19
1615 SAL AL,1 ; TEST FOR RECORD NOT FOUND
1616 SAL AL,1
1617 MOV AH,RECORD_NOT_FND
1618 JC J19
1619 SAL AL,1
1620 MOV AH,WRITE_PROTECT ; TEST FOR WRITE_PROTECT
1621 JC J19
1622 SAL AL,1 ; TEST MISSING ADDRESS MARK
1623 MOV AH,BAD_ADDR_MARK
1624 JC J19
1625
1626 ;----- NEC MUST HAVE FAILED
1627 J18: NEC MUST HAVE FAILED
1628 MOV AH,BAD_NEC
1629 J19:
1630 SET_END:OR *DSKETTE_STATUS,AH
1631 CMP *DSKETTE_STATUS,I ; SET ERROR CONDITION
1632 CMC ;
1633 POP SI ; RESTORE HEAD #, # OF SECTORS
1634 RET
1635
1636 SET_END_POP:
1637 POPF SHORT SET_END
1638 JMP END
1639 NEC_TERM
1640 ;-----
1641 ; DSTATE: ESTABLISH STATE UPON SUCCESSFUL OPERATION. ;
1642 ;-----
1643 DSTATE PROC NEAR
1644 TEST *HP_CNTRL,DUAL ; TEST CONTROLLER I.D.
1645 JZ SETBAC
1646 CMP *DSKETTE_STATUS,0 ; CHECK FOR ERROR
1647 JNZ IF_ERROR_JUMP ; IF ERROR JUMP
1648 OR *DSK_STATE[D1],MED_DET ; NO ERROR, MARK MEDIA AS DETERMINED
1649 TEST *DSK_STATE[D1],DRV_DET ; DRIVE DETERMINED ?
1650 JNZ SETBAC ; IF DETERMINED NO TRY TO DETERMINE
1651 MOV AL,*DSK_STATE[D1] ; LOAD STATE
1652 AND AL,RATE_MSK ; KEEP ONLY RATE
1653 CMP AL,RATE_250 ; RATE 250 ?
1654 JNE M_12 ; NO MUST BE 1.2M OR HI DATA RATE 80 TRK
1655
1656 ;--- CHECK FOR HIGH DATA RATE 80 TRACK
1657

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SECTION 5


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1658
1659 079F E8 08CF R      CALL   #DMSK_TYPE           ; RETURN DRIVE TYPE IN (AL)
1660 07A2 72 14          JMC    M_12                ; CMOS BAD ASSUME DEFAULT
1661 07A4 3C 02          JC     AL,2                ; TYPE 2 DRIVE ?
1662 07A6 74 10          JC     M_12                ; YES-->ASSUME MULTI FORMAT CAPABILITY
1663 07A8 3C 04          CMP    AL,4                ; TYPE 4 DRIVE ?
1664 07AA 74 0C          JE     M_12                ; YES-->ASSUME MULTI FORMAT CAPABILITY
1665 07AC
1666 07AC 80 A5 0090 R FD M_720: AND    #DMSK_STATE[D1],NOT FMT_CAPA ; TURN OFF DETERMINED CAPABILITY
1667 07B1 80 8D 0090 R 04 OR     #DMSK_STATE[D1],DRV_DET     ; MARK DRIVE FORMAT
1668 07B6 EB 05          JMP    SHORT SETBAC        ; BACK
1669
1670 07B8
1671 07B8 80 8D 0090 R 06 M_12: OR     #DMSK_STATE[D1],DRV_DET+FMT_CAPA ; TURN ON DETERMINED & FMT CAPA
1672
1673 07BD
1674 07C0 C3          SETBAC: RET
1675 07BE          DSTATE ENDP
-----
1676
1677 ; RETRY
1678 ; DETERMINES WHETHER A RETRY IS NECESSARY. IF RETRY IS
1679 ; REQUIRED THEN STATE INFORMATION IS UPDATED FOR RETRY.
1680
1681 ; ON EXIT:  CY = 1 FOR RETRY, CY = 0 FOR NO RETRY
-----
1682
1683 07BE          RETRY: PROC   NEAR
1684 07BE 80 3E 0041 R 00 CMP    #DSKETTE_STATUS,0       ; GET STATUS OF OPERATION
1685 07C3 74 3E          JZ     NO_RETRY              ; SUCCESSFUL OPERATION
1686 07C5 80 3E 0041 R 80 CMP    #DMSKETTE_STATUS,TIME_OUT ; IF TIME OUT NO RETRY
1687 07CA 74 37          JZ     NO_RETRY              ;
1688 07CC 8A A5 0090 R 8 MOV    AH,#DMSK_STATE[D1]     ; GET MEDIA STATE OF DRIVE
1689 07D0 F6 C4 10     TEST  AH,MEG_DET             ; ESTABLISHED/DETERMINED ?
1690 07D3 75 2E          JNZ    NO_RETRY              ; IF ESTABLISHED STATE THEN TRUE ERROR
1691 07D5 80 E4 C0     AND  AH,RATE_MSK            ; ISOLATE RATE
1692 07D8 8A 2E 00B8 R MOV    CH,RLA_STRATE         ; GET START OPERATION STATE
1693 07DC DD C5          ROL   CH,1                  ; TO CORRESPONDING BITS
1694 07DE DD C5          ROL   CH,1
1695 07E0 DD C5          ROL   CH,1
1696 07E2 DD C5          ROL   CH,1
1697 07E4 80 E5 C0     AND  CH,RATE_MSK            ; ISOLATE RATE BITS
1698 07E7 3A EC          CMP    CH,AH                ; ALL RATES TRIED
1699 07E9 74 18          JE     NO_RETRY              ; IF YES, THEN TRUE ERROR
1700
1701 ; SETUP STATE INDICATOR FOR RETRY ATTEMPT TO NEXT RATE
1702 ; 00000000B (500) -> 10000000B (250)
1703 ; 10000000B (250) -> 01000000B (100)
1704 ; 01000000B (300) -> 00000000B (500)
-----
1705
1706 07EB 80 FC 01     CMP    AH,RATE_500+1        ; SET CY FOR RATE 500
1707 07EE DD DC          RCR   AH,1                  ; TO NEXT STATE
1708 07F0 80 E4 C0     AND  AH,RATE_MSK            ; KEEP ONLY RATE BITS
1709 07F3 80 A5 0090 R IF OR     #DMSK_STATE[D1],NOT RATE_MSK+DBL_STEP ; RATE, DBL STEP OFF
1710 07F8 08 A5 0090 R AND    #DMSK_STATE[D1],AH    ; TURN ON NEW RATE
1711 07FC C6 0E 0041 R 00 MOV    #DMSKETTE_STATUS,0   ; RESET STATUS FOR RETRY
1712 0801 F9          STC
1713 0802 C3          RET
1714
1715 0803          NO_RETRY:
1716 0803 F8          CLC
1717 0804 C3          RET
1718 0805          RETRY ENDP
-----
1719
1720 ; NUM_TRANS
1721 ; THIS ROUTINE CALCULATES THE NUMBER OF SECTORS THAT
1722 ; WERE ACTUALLY TRANSFERRED TO/FROM THE DISKETTE.
1723
1724 ; ON ENTRY:  [BP+1] = TRACK
1725 ;           SI-HI = HEAD
1726 ;           [BP] = START SECTOR
1727
1728 ; ON EXIT:  AL = NUMBER ACTUALLY TRANSFERRED
-----
1729
1730 0805          NUM_TRANS: PROC   NEAR
1731 0805 32 C0          XOR   AL,AL                 ; CLEAR FOR ERROR
1732 0807 80 3E 0041 R 00 CMP    #DMSKETTE_STATUS,0     ; CHECK FOR ERROR
1733 080C 75 23          JNZ   NT_OUT                ; IF ERROR 0 TRANSFERRED
1734 080E B2 04          MOV   DL,4                  ; SECTORS/TRACK OFFSET TO DL
1735 0810 EB 08FE R     CALL  GET_PARM              ; AH = SECTORS/TRACK
1736 0813 8A 1E 0047 R MOV    BL,#NEC_STATUS+5      ; GET ENDING SECTOR
1737 0817 B5 CE          MOV   CX,SI                 ; CH = HEAD # STARTED
1738 0819 3A 2E 0046 R CMP    CH,#NEC_STATUS+4      ; GET HEAD ENDED UP ON
1739 081D 75 0B          JNZ   DIF_HD                 ; IF ON SAME HEAD, THEN NO ADJUST
1740
1741 081F 8A 2E 0046 R MOV    CH,#NEC_STATUS+3      ; GET TRACK ENDED UP ON
1742 0823 3A 6E 01     CMP    CH,[BP+1]            ; IS IT ASKED FOR TRACK
1743 0826 74 04          JZ    SAME_TRK               ; IF SAME TRACK NO INCREASE
1744
1745 0828 02 DC          ADD   BL,AH                 ; ADD SECTORS/TRACK
1746 082A          DIF_HD:
1747 082A 02 DC          ADD   BL,AH                 ; ADD SECTORS/TRACK
1748 082C          SAME_TRK:
1749 082C 2A 5E 00     SUB   BL,[BP]                ; SUBTRACT START FROM END
1750 082F 8A C3          MOV   AL,BL                 ; TO AL
1751
1752 0831          NT_OUT:
1753 0831 C3          RET
1754 0832          NUM_TRANS ENDP
-----
1755
1756 ; SETUP_END
1757 ; RESTORES #MOTOR COUNT TO PARAMETER PROVIDED IN TABLE
1758 ; AND LOADS #DSKETTE_STATUS TO AH, AND SETS CY.
1759
1760 ; ON EXIT:  AH, #DSKETTE_STATUS, CY REFLECT STATUS OF OPERATION
-----
1761
1762 0832          SETUP_END: PROC   NEAR
1763 0832 B2 02     PUSH  DL,2                   ; GET THE MOTOR WAIT PARAMETER
1764 0834 50          MOV   AX,AH                 ; SAVE NUMBER TRANSFERRED
1765 0835 EB 08FE R     CALL  GET_PARM              ; GET #MOTOR_COUNT,AH
1766 0838 B8 26 0040 R MOV    #MOTOR_COUNT,AH      ; STORE UPON RETURN
1767 083C 58          POP   AX                     ; RESTORE NUMBER TRANSFERRED
1768 083D 8A 26 0041 R MOV    AH,#DSKETTE_STATUS   ; GET STATUS OF OPERATION
1769 0841 0A E4          OR    AH,AH                 ; CHECK FOR ERROR
1770 0843 74 02          JZ    NUN_ERR                ; NO ERROR
1771 0845 32 C0          XOR   AL,AL                 ; CLEAR NUMBER RETURNED

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1773 0847
1774 0847 80 FC 01
1775 084A F5
1776 084B C3
1777 084C
-----
1778
1779
1780 ; CHECK DOUBLE STEP.
1781 ; ON ENTRY: DI = DRIVE
1782
1783 ; ON EXIT: CY = 1 MEANS ERROR
1784
1785 084C
1786 084C F6 06 008F R 01
1787 0851 74 65
1788 0853 8A 5A 0090 R
1789 0857 F6 C4 10
1790 085A 75 5C
1791
1792
1793 ;-----
1794 085C C6 06 003E R 00
1795 0861 EB 0913 R
1796 0864 B5 00
1797 0866 EB 0A14 R
1798 0869 EB 08BA R
1799 086C 72 35
1800
1801 ;-----
1802
1803 086E B9 0450
1804 0871 F6 C5 0090 R 01
1805 0876 74 02
1806 0878 B1 A0
1807
1808 ; ATTEMPT READ ID OF ALL TRACKS, ALL HEADS UNTIL SUCCESS; UPON SUCCESS,
1809 ; MUST SEE IF ASKED FOR TRACK IN SINGLE STEP MODE = TRACK ID READ; IF NOT
1810 ; THEN SET DOUBLE STEP ON.
1811
1812 087A
1813 087A 51
1814 087B C6 06 0041 R 00
1815 0880 33 C0
1816 0882 00 D0
1817 0884 00 D0
1818 0886 00 D0
1819 0888 00 D0
1820 088A 50
1821 088B EB 0A14 R
1822 088E 58
1823 089F 08 F8
1824 0891 EB 08BA R
1825 0894 9C
1826 0895 81 E7 00FB
1827 0899 9D
1828 089A 59
1829 089B 73 08
1830 089D FE C5 0094 R
1831 089F 3A E9
1832 08A1 75 D7
1833
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1836 08A3
1837 08A3 F9
1838 08A4 C3
1839
1840 08A5
1841 08A5 8A 0E 0045 R
1842 08A9 88 8D 0094 R
1843 08AD 00 ED
1844 08AF 3A E9
1845 08B1 74 05
1846 08B3 80 8D 0090 R 20
1847
1848 08B8
1849 08B8 F8
1850 08B9 C3
1851 08BA
-----
1852
1853 ; READ ID
1854 ; READ ID FUNCTION.
1855 ; ON ENTRY: DI = BIT 2 = HEAD; BITS 1,0 = DRIVE
1856
1857 ; ON EXIT: DI = BIT 2 IS RESET, BITS 1,0 = DRIVE
1858 ; *DSKETTE_STATUS, CY REFLECTS STATUS OF OPERATION:
1859
1860 08BA
1861 08BA B8 08CE R
1862 08BD 50
1863 08BE B4 4A
1864 08C0 EB 09F0 R
1865 08C3 B8 C7
1866 08C5 8A E0
1867 08C7 EB 09F0 R
1868 08CA EB 0727 R
1869 08CD 58
1870 08CE
1871 08CE C3
1872 08CF
-----
1873
1874 ; CMOS_TYPE
1875 ; RETURNS DISKETTE TYPE FROM CMOS
1876
1877 ; ON ENTRY: DI = DRIVE #
1878
1879 ; ON EXIT: AL = TYPE; CY REFLECTS STATUS
1880
1881 08CF
1882 08CF A0 0010 R
1883 08D2 24 C1
1884 08D4 D0 E8
1885 08D6 73 20
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SECTION 5

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1886 08D8 D0 C0      ROL     AL,1          ; ROTATE TO ORIGINAL POSITION
1887 08DA D0 C0      ROL     AL,1          ; ROTATE BITS 6 AND 7 TO 0 AND 1
1888 08DC D0 C0      ROL     AL,1
1889 08DE 32 E4      XOR     AH,AH         ; AX=NUMBER OF DRIVES
1890 08E0 3B C7      CMP     AX,DI         ; IS DRIVE REQUESTED PRESENT
1891 08E2 72 14      JC      TYP_ZERO     ; C--REQUESTED DRIVE NOT PRESENT
1892 08E4 F6 06 008F R 01  TEST   08F_CNTRL,DUAL ; TEST CONTROLLER I.D.
1893 08E9 75 10      JNZ     CR2          ; TEST FOR 80 TRACKS
1894 08EB F6 05 0090 R 01  TEST   08SK_STATE[DI],TRK_CAPA ; DRIVE TYPE HAS 40 TRACKS
1895 08F0 B0 01      MOV     AL,1
1896 08F2 74 06      JZ      CR1          ; DRIVE TYPE HAS 80 TRACKS
1897 08F4 B0 03      MOV     AL,3
1898 08F6 EB 02      JMP     SHORT CR1
1899 08F8
1900 08F8 32 C0      XOR     AL,AL        ; DRIVE TYPE 0
1901 08FA              CR1:   RET           ; EXIT WITH AL=TYPE ACCORDING TO TRACKS
1902 08FA C3
1903 08FB              CR2:   STC
1904 08FB F9              JMP     CR1          ; EXIT WITH CARRY IF DUAL CARD
1905 08FC EB FC
1906 08FE
-----
1907
1908      GET_PARM
1909      ; THIS ROUTINE FETCHES THE INDEXED POINTER FROM THE
1910      ; DISK BASE BLOCK POINTED TO BY THE DATA VARIABLE
1911      ; *DISK POINTER. A BYTE FROM THAT TABLE IS THEN MOVED
1912      ; INTO AH, THE INDEX OF THAT BYTE BEING THE PARAMETER
1913      ; IN DL.
1914
1915      ; ON ENTRY: DL = INDEX OF BYTE TO BE FETCHED
1916
1917      ; ON EXIT: AH = THAT BYTE FROM BLOCK
1918      ; AL,DH DESTROYED
1919
-----
1920 08FE      GET_PARM      PROC      NEAR
1921 08FE IE      PUSH     DS
1922 08FF 56      PUSH     SI
1923 0900 2B C0      SUB     AX,AX        ; DS = 0 , BIOS DATA AREA
1924 0902 8E D8      MOV     DS,AX
1925 0904 87 D3      XCHG   DX,BX        ; BL = INDEX
1926 0906 2A FF      SUB     BH,BH        ; BX = INDEX
1927
1928 0908 C5 36 0078 R  LDS     SI,08ISK_POINTER ; POINT TO BLOCK
1929 090C 8A 20      MOV     AH,[SI+BX]  ; GET THE WORD
1930 090E 87 D3      XCHG   DX,BX        ; RESTORE BX
1931 0910 5E          POP     SI
1932 0911 1F          POP     DS
1933 0912 C3          RET
1934
1935 0913      GET_PARM      ASSUME  DS:DATA
1936
1937      ;-----
1938      ; MOTOR_ON
1939      ; TURN MOTOR ON AND WAIT FOR MOTOR START UP TIME. THE *MOTOR COUNT:
1940      ; IS REPLACED WITH A SUFFICIENTLY HIGH NUMBER (0FFFH) TO ENSURE
1941      ; THAT THE MOTOR DOES NOT GO OFF DURING THE OPERATION. IF THE
1942      ; MOTOR NEEDED TO BE TURNED ON, THE MULTITASKING HOOK FUNCTION
1943      ; (AX=90FDH, INT 15H) IS CALLED TELLING THE OPERATING SYSTEM
1944      ; THAT THE BIOS IS ABOUT TO WAIT FOR MOTOR START UP. IF THIS
1945      ; FUNCTION RETURNS WITH CY = 1, IT MEANS THAT THE MINIMUM WAIT
1946      ; HAS BEEN COMPLETED. AT THIS POINT A CHECK IS MADE TO ENSURE
1947      ; THAT THE MOTOR WASN'T TURNED OFF BY THE TIMER. IF THE HOOK DID
1948      ; NOT WAIT, THE WAIT FUNCTION (AH=086H) IS CALLED TO WAIT THE
1949      ; PRESCRIBED AMOUNT OF TIME. IF THE CARRY FLAG IS SET ON RETURN,
1950      ; IT MEANS THAT THE FUNCTION IS IN USE AND DID NOT PERFORM THE
1951      ; WAIT. A TIMER WAIT LOOP WILL THEN DO THE WAIT.
1952
1953      ; ON ENTRY: DI = DRIVE #
1954
1955      ; ON EXIT: AX,CX,DX DESTROYED
1956
-----
1956 0913      MOTOR_ON      PROC      NEAR
1957 0913 53      PUSH     BX
1958 0914 E8 095E R  CALL   TURN_ON
1959 0917 72 43      JC      MOT_TS_ON
1960 0919 E8 0432 R  CALL   XLAT_OLD
1961 091C B8 90FD MOV     AX,090FDH
1962 091F CD 15      INT     15H
1963 0921 9C      PUSHF
1964 0922 E8 0404 R  CALL   XLAT_NEW
1965 0925 9D      POPF
1966 0926 73 05      JNC     M_WAIT
1967 0928 E8 095E R  CALL   TURN_ON
1968 092B 72 2F      JC      MOT_TS_ON
1969
1970 092D      M_WAIT:
1971 092D B2 0A      MOV     DL,10
1972 092F E8 08FE R  CALL   GET_PARM
1973 0932 BA CA      MOV     AL,AH
1974 0934 32 E4      XOR     AH,AH
1975 0936 3C 08      CMP     AL,8
1976 0938 73 02      JAE     GP2
1977 093A B0 08      MOV     AL,8
1978
1979      ;----- AX CONTAINS NUMBER OF 1/8 SECONDS (125000 MICROSECONDS) TO WAIT
1980
1981 093C 50      ; SAVE WAIT PARAMETER
1982 093D BA F424  GP2:   PUSH     AX
1983 0940 F7 E2      MOV     DX,62500
1984 0942 B8 CA      MUL     DX
1985 0944 B8 D0      MOV     CX,DX
1986 0946 F8      CLC
1987 0947 D1 D2      RCL     CX,1
1988 0949 D1 D1      RCL     CX,DX
1989 094B B4 86      MOV     AH,86H
1990 094D CD 15      INT     15H
1991 094F 58      POP     AX
1992 0950 73 0A      JNC     MOT_IS_ON
1993
1994      ;----- FOLLOWING LOOPS REQUIRED WHEN RTC WAIT FUNCTION IS ALREADY IN USE
1995
1996 0952      ; WAIT FOR 1/8 SECOND PER (AL)
1997 0952 B9 205E  J13:   MOV     CX,8286
1998 0955 E8 0000 E  CALL   WAIT
1999 0958 FE C8      DEC     AL
    
```

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2000 095A 75 F6          JNZ     J13          ; ARE WE DONE YET
2001
2002 095C              MOT_IS_ON:
2003 095C 5B          ; POP
2004 095D C3          RET             ; RESTORE REG.
2005 095E              MOTOR_ON      ENDP
2006
2007          ;-----
2008          ; TURN_ON
2009          ; TURN MOTOR ON AND RETURN WAIT STATE.
2010
2011          ; ON ENTRY:  DI = DRIVE #
2012          ; ON EXIT:  CY = 0 MEANS WAIT REQUIRED
2013          ;           CY = 1 MEANS NO WAIT REQUIRED
2014          ;           AX,BX,CX,DX DESTROYED
2015          ;-----
2016 095E              TURN_ON PROC
2017 095E 8B DF          MOV     BX,DI          ; BX = DRIVE #
2018 0960 8A CB          MOV     CL,BL         ; CL = DRIVE #
2019 0962 D0 C3          ROL     BL,1         ; BL = DRIVE SELECT
2020 0964 D0 C3          ROL     BL,1
2021 0966 D0 C3          ROL     BL,1
2022 0968 D0 C3          ROL     BL,1
2023 096A FA          CLI
2024 096B C6 06 0040 R FF ; NO INTERRUPTS WHILE DETERMINING STATUS
2025 0970 A0 003F R      MOV     AL,*MOTOR_STATUS ; ENSURE MOTOR STAYS ON FOR OPERATION
2026 0973 24 50          AND     AL,00110000B    ; GET DIGITAL OUTPUT REGISTER REFLECTION
2027 0975 B4 01          MOV     AH,1           ; GET ONLY DRIVE SELECT BITS
2028 0977 D2 E4          SHL     AH,CL         ; MASK FOR DETERMINING MOTOR BIT
2029
2030          ; AH = MOTOR ON, A=00000001, B=00000010
2031          ; AL = DRIVE SELECT FROM *MOTOR_STATUS
2032          ; BL = DRIVE SELECT DESIRED
2033          ; AH = MOTOR ON MASK DESIRED
2034 0979 3A C3          CMP     AL,BL         ; REQUESTED DRIVE ALREADY SELECTED ?
2035 097B 75 06          JNZ     TURN_IT_ON    ; IF NOT SELECTED JUMP
2036 097D 84 26 003F R  TEST    AH,*MOTOR_STATUS ; TEST MOTOR ON BIT
2037 0981 75 31          JNZ     NO_MOT_WAIT   ; JUMP IF MOTOR ON AND SELECTED
2038
2039 0983              TURN_IT_ON:
2040 0983 0A E3          OR     AH,BL          ; AH = DRIVE SELECT AND MOTOR ON
2041 0985 8A 3E 003F R  MOV     BH,*MOTOR_STATUS ; KEEP COPY OF *MOTOR_STATUS BEFORE
2042 0989 80 E7 0F          MOV     BH,00001111B    ; KEEP ONLY MOTOR BITS
2043 098C 80 26 003F R C0 AND     *MOTOR_STATUS,11000000B ; CLEAR OUT DRIVE SELECT AND MOTORS
2044 0991 08 26 003F R  OR     *MOTOR_STATUS,AH ; OR IN DRIVE SELECTED
2045 0995 A0 003F R      MOV     AL,*MOTOR_STATUS ; GET DIGITAL OUTPUT REGISTER REFLECTION
2046 0998 8A D8          MOV     BL,AL         ; BL=*MOTOR_STATUS AFTER, BH=BEFORE
2047 099A 80 E3 0F          AND     BL,00001111B    ; KEEP ONLY MOTOR BITS
2048 099D FB          STI
2049 099E 24 3F          AND     AL,00111111B    ; ENABLE INTERRUPTS AGAIN
2050 09A0 D0 C0          ROL     AL,1         ; STRIP AWAY UNWANTED BITS
2051 09A2 D0 C0          ROL     AL,1
2052 09A4 D0 C0          ROL     AL,1
2053 09A6 D0 C0          ROL     AL,1
2054 09A8 0C 0C          OR     AL,00001100B    ; NO RESET, ENABLE DMA/INTERRUPT
2055 09AA BA 03F2        MOV     DX,03F2H       ; SELECT DRIVE AND TURN ON MOTOR
2056 09AD E8 0F          OUT
2057 09AE 3A DF          CMP     BL,BH         ; NEW MOTOR TURNED ON ?
2058 09B0 74 02          JZ     NO_MOT_WAIT    ; NO WAIT REQUIRED IF JUST SELECT
2059 09B2 F8          CLC
2060 09B3 C3          RET             ; SET CARRY MEANING WAIT
2061
2062 09B4              NO_MOT_WAIT:
2063 09B4 F9          STC
2064 09B5 FB          STI
2065 09B6 C3          RET             ; INTERRUPTS BACK ON
2066 09B7
2067          ;-----
2068          ; HD_WAIT
2069          ; WAIT FOR HEAD SETTLE TIME.
2070
2071          ; ON ENTRY:  DI = DRIVE #
2072          ; ON EXIT:  AX,BX,CX,DX DESTROYED
2073          ;-----
2074 09B7              HD_WAIT PROC NEAR
2075 09B7 B2 09          MOV     DL,9          ; POINT TO HEAD SETTLE PARAMETER
2076 09B9 E8 08FE R      CALL    GET_PARM      ; GET PARAMETER
2077 09BC F6 06 003F R 80 TEST    *MOTOR_STATUS,10000000B ; SEE IF A WRITE OPERATION
2078 09C1 74 09          JZ     !SNT_WRITE     ; IF NOT DO NOT ENFORCE ANY VALUES
2079 09C3 80 FC 0F          CMP     AH,15         ; IS WAIT 15 MILLISECOUNDS OR GREATER?
2080 09C6 73 08          JAE     DO_WAIT       ; IF THERE DO NOT ENFORCE
2081 09C8 B4 0F          MOV     AH,15         ; HEAD SETTLE MINIMUM
2082 09CA EB 04          JMP     SHORT_DO_WAIT ; DO WAIT OPERATION
2083 09CC
2084 09CC              !SNT_WRITE:
2085 09CC 0A E4          OR     AH,AH         ; CHECK FOR WAIT TO BE ZERO
2086 09CE 74 1F          JZ     HW_DONE        ; IF NOT WRITE AND 0 THEN EXIT
2087
2088          ;-----
2089          ; AH CONTAINS NUMBER OF MILLISECOUNDS TO WAIT
2090 09D0
2091 09D0              DO_WAIT:
2092 09D0 8A C4          MOV     AL,AH         ; AL = # MILLISECOUNDS
2093 09D2 32 E4          XOR     AH,AH         ; AX = # MILLISECOUNDS
2094 09D4 50          PUSH    AH            ; SAVE HEAD SETTLE PARAMETER
2095 09D5 BA 03E8        MOV     DX,1000       ; SET UP FOR MULTIPLY TO MICROSECONDS
2096 09D8 F7 E2          MUL     DX            ; DX,AX = # MICROSECONDS
2097 09DA 8B CA          MOV     CX,DX         ; CX,AX = # MICROSECONDS
2098 09DC 8B B6          MOV     DX,AX         ; CX,DX = # MICROSECONDS
2099 09DE B4 86          MOV     AH,86H       ; LOAD WAIT CODE
2100 09E0 CD 15          INT     15H          ; PERFORM WAIT
2101 09E2 58          POP     AX            ; RESTORE HEAD SETTLE PARAMETER
2102 09E3 73 0A          JNC     HW_DONE       ; CHECK FOR EVENT WAIT ACTIVE
2103 09E5
2104 09E5              J29:
2105 09E5 B9 0042        MOV     CX,66         ; 1 MILLISECOND LOOP
2106 09E8 80 00 00 00 E  CALL    WAITF         ; COUNT AT 15.085737 US PER COUNT
2107 09EB FE C8          DEC     AL            ; DELAY FOR 1 MILLISECOND
2108 09ED 75 F6          JNZ     J29          ; DECREMENT THE COUNT
2109 09EF              HW_DONE:
2110 09F0              HD_WAIT RET
2111          ;-----
2112          ; NEC_OUTPUT
2113          ; THIS ROUTINE SENDS A BYTE TO THE NEC CONTROLLER AFTER
    
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SECTION 5

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2114 ; TESTING FOR CORRECT DIRECTION AND CONTROLLER READY THIS ;
2115 ; ROUTINE WILL TIME OUT IF THE BYTE IS NOT ACCEPTED WITHIN ;
2116 ; A REASONABLE AMOUNT OF TIME, SETTING THE DISKETTE STATUS ;
2117 ; ON COMPLETION. ;
2118 ; ;
2119 ; ON ENTRY: ;
2120 ; AH = BYTE TO BE OUTPUT ;
2121 ; ON EXIT: ;
2122 ; CY = 0 SUCCESS ;
2123 ; CY = 1 FAILURE -- DISKETTE STATUS UPDATED ;
2124 ; IF A FAILURE HAS OCCURRED, THE RETURN IS MADE ;
2125 ; ONE LEVEL HIGHER THAN THE CALLER OF NEC_OUTPUT. ;
2126 ; THIS REMOVES THE REQUIREMENT OF TESTING AFTER ;
2127 ; EVERY CALL OF NEC_OUTPUT. ;
2128 ; AX,CX,DX DESTROYED ;
-----
2130 09F0 ;
2131 09F0 53 ;
2132 09F1 BA 03F4 ; ; SAVE REG. ;
2133 09F4 B3 02 ; ; STATUS PORT ;
2134 09F6 33 C9 ; ; HIGH ORDER COUNTER ;
2135 ; ; ; COUNT FOR TIME OUT ;
2136 09FB EC ;
2137 09F9 24 C0 ; J23: IN AL,DX ; GET STATUS ;
2138 09FB 3C 80 ; AND AL,11000000B ; KEEP STATUS AND DIRECTION ;
2139 09FD 74 0F ; CMP AL,10000000B ; STATUS AND DIRECTION ? ;
2140 09FF E2 F7 ; JZ J27 ; STATUS AND DIRECTION OK ;
2141 ; LOOP J23 ; CONTINUE TILL CX EXHAUSTED ;
2142 0A01 FE CB ; DEC BL ; DECREMENT COUNTER ;
2143 0A03 75 F3 ; JNZ J23 ; REPEAT TILL DELAY FINISHED, CX = 0 ;
2144 ;
2145 ;----- FALL THRU TO ERROR RETURN ;
2146 ;
2147 0A05 80 0E 0041 R 80 ; OR #DSKETTE_STATUS,TIME_OUT ;
2148 ;
2149 0A0A 5B ; POP BX ; RESTORE REG. ;
2150 ;
2151 0A0B 58 ; POP AX ; DISCARD THE RETURN ADDRESS ;
2152 0A0C F9 ; STC ; INDICATE ERROR TO CALLER ;
2153 0A0D C3 ; RET ;
2154 ;
2155 ;----- DIRECTION AND STATUS OK; OUTPUT BYTE ;
2156 ;
2157 0A0E 8A C4 ; J27: MOV AL,AH ; GET BYTE TO OUTPUT ;
2158 0A10 42 ; INC DX ; DATA PORT = STATUS PORT + 1 ;
2159 0A11 EE ; OUT DX,AL ; OUTPUT THE BYTE ;
2160 ;
2161 0A12 5B ; POP BX ; RESTORE REG. ;
2162 0A13 C3 ; RET ; CY = 0 FROM TEST INSTRUCTION ;
2163 0A14 ;
-----
2164 ; SEEK ;
2165 ; THIS ROUTINE WILL MOVE THE HEAD ON THE NAMED DRIVE ;
2166 ; TO THE NAMED TRACK. IF THE DRIVE HAS NOT BEEN ACCESSED ;
2167 ; SINCE THE DRIVE RESET COMMAND WAS ISSUED, THE DRIVE ;
2168 ; WILL BE RECALIBRATED. ;
2169 ;
2170 ; ON ENTRY: ;
2171 ; D1 = DRIVE # ;
2172 ; CH = TRACK # ;
2173 ;
2174 ; ON EXIT: ;
2175 ; #DSKETTE_STATUS, CY REFLECT STATUS OF OPERATION. ;
2176 ; AX,BX,CX,DX DESTROYED ;
-----
2177 0A14 ; SEEK PROC NEAR ;
2178 0A14 8B DF ; MOV BX,D1 ; BX = DRIVE # ;
2179 0A16 8A 0A7B R ; MOV DX,OFFSET NEC_ERR ; LOAD RETURN ADDRESS ;
2180 0A19 52 ; PUSH DX ; ON STACK FOR NEC_OUTPUT ERROR ;
2181 0A1A 80 01 ; MOV AL,1 ; ESTABLISH MASK FOR RECALIBRATE TEST ;
2182 0A1C 86 CB ; XCHG CL,BL ; GET DRIVE VALUE INTO CL ;
2183 0A1E 02 C0 ; ROL AL,CL ; SHIFT MASK BY THE DRIVE VALUE ;
2184 0A20 86 CB ; XCHG CL,BL ; RECOVER TRACK VALUE ;
2185 0A22 84 06 003E R ; TEST AL,#SEEK_STATUS ; TEST FOR RECALIBRATE REQUIRED ;
2186 0A26 75 21 ; JZ8A ; JUMP IF RECALIBRATE NOT REQUIRED ;
2187 ;
2188 0A28 08 06 003E R ; OR #SEEK_STATUS,AL ; TURN ON THE NO RECALIBRATE BIT IN FLAG ;
2189 0A2C E8 0A7C R ; CALL RECAL ; RECALIBRATE DRIVE ;
2190 0A2F 73 0A ; JNC AFT_RECAL ; RECALIBRATE DONE ;
2191 ;
2192 ;----- ISSUE RECALIBRATE FOR 80 TRACK DISKETTES ;
2193 ;
2194 0A31 C6 06 0041 R 00 ; MOV #DSKETTE_STATUS,0 ; CLEAR OUT INVALID STATUS ;
2195 0A36 E8 0A7C R ; CALL RECAL ; RECALIBRATE DRIVE ;
2196 0A39 72 3F ; JC RB ; IF RECALIBRATE FAILS TWICE THEN ERROR ;
2197 ;
2198 0A3B ; AFT_RECAL: ;
2199 0A3B 83 FF 01 ; CMP D1,1 ; IF REQUEST FOR DRIVE > 2 ;
2200 0A3E 77 21 ; JA RB ; DO SEEK EVERY TIME ;
2201 0A40 C6 85 0094 R 00 ; MOV #DSK_TRK[D1],0 ; SAVE NEW CYLINDER AS PRESENT POSITION ;
2202 0A45 0A ED ; OR CH,CH ; CHECK FOR SEEK TO TRACK 0 ;
2203 0A47 74 2C ; JZ DO_WAIT ; HEAD SETTLE, CY = 0 IF JUMP ;
2204 ;
2205 ;----- DRIVE IS IN SYNCHRONIZATION WITH CONTROLLER, SEEK TO TRACK ;
2206 ;
2207 0A49 ; J28A: ;
2208 0A49 83 FF 01 ; CMP D1,1 ; IF REQUEST FOR DRIVE > 2 ;
2209 0A4C 77 13 ; JA RB ; DO SEEK EVERY TIME ;
2210 0A4E 76 85 0090 R 20 ; TEST #DSK_STATE[D1],DBL_STEP ; CHECK FOR DOUBLE STEP REQUIRED ;
2211 0A53 74 02 ; JZ RT ; SINGLE STEP REQUIRED BYPASS DOUBLE ;
2212 0A55 0D E5 ; SHL CH,1 ; DOUBLE NUMBER OF STEP TO TAKE ;
2213 ;
2214 0A57 3A AD 0094 R ; RT: CMP CH,#DSK_TRK[D1] ; SEE IF ALREADY AT THE DESIRED TRACK ;
2215 0A5B 74 1D ; JE RB ; IF YES, DO NOT NEED TO SEEK ;
2216 ;
2217 0A5D 88 AD 0094 R ; MOV #DSK_TRK[D1],CH ; SAVE NEW CYLINDER AS PRESENT POSITION ;
2218 0A61 ; RB: ;
2219 0A61 51 ; PUSH CX ;
2220 0A62 B4 0F ; MOV AH,0FH ; SEEK COMMAND TO NEC ;
2221 0A64 EB 09F0 R ; CALL NEC_OUTPUT ;
2222 0A67 B8 DF ; MOV BX,D1 ; BX = DRIVE # ;
2223 0A69 BA E3 ; MOV AH,BL ; OUTPUT DRIVE NUMBER ;
2224 0A6B EB 09F0 R ; CALL NEC_OUTPUT ;
2225 0A6E 58 ; POP AX ; RESTORE CYLINDER # FOR NEC_OUTPUT ;
2226 0A6F E8 09F0 R ; CALL NEC_OUTPUT ;
2227 0A72 E8 0A93 R ; CALL CHK_STAT_2 ; ENDING INTERRUPT AND SENSE STATUS ;

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2228
2229 ;----- WAIT FOR HEAD SETTLE
2230
2231 0A75 DO_WAIT:
2232 0A75 9C          PUSHF          ; SAVE STATUS
2233 0A76 E8 09B7 R CALL    HD_WAIT ; WAIT FOR HEAD SETTLE TIME
2234 0A79 9D          POPF           ; RESTORE STATUS
2235 0A7A          RB:          POP    AX          ; CLEAR ERROR RETURN FROM NEC_OUTPUT
2236 0A7A 58          NEC_ERR:     RET           ; RETURN TO CALLER
2237 0A7B          SEEK      ENDP
2238 0A7B C3          ;-----
2239 0A7C          ; RECAL
2240          ; RECALIBRATE DRIVE
2241          ;
2242          ; ON ENTRY    DI = DRIVE #
2243          ;
2244          ; ON EXIT:   CY REFLECTS STATUS OF OPERATION.
2245          ;-----
2246
2247 0A7C          RECAL      PROC    NEAR
2248 0A7C 51          PROC    NEAR
2249 0A7C 51          PUSH    CX
2250 0A7D B8 0A91 R MOV    AX,OFFSET RC_BACK ; LOAD NEC_OUTPUT ERROR
2251 0A80 50          PUSH    AX
2252 0A81 B4 07          MOV    AH,07H          ; RECALIBRATE COMMAND
2253 0A83 E8 09F0 R CALL    NEC_OUTPUT
2254 0A86 B8 0F          MOV    BX,DI          ; BX = DRIVE #
2255 0A88 8A E3          MOV    AH,BL
2256 0A8A E8 09F0 R CALL    NEC_OUTPUT
2257 0A8D E8 0A93 R CALL    CHK_STAT_2
2258 0A90 58          POP    AX          ; THROW AWAY ERROR
2259 0A91          RC_BACK:     POP    CX
2260 0A91 59          RET
2261 0A92 C3          RECAL      ENDP
2262 0A93          ;-----
2263          ; CHK_STAT_2
2264          ; THIS ROUTINE HANDLES THE INTERRUPT RECEIVED AFTER
2265          ; RECALIBRATE, SEEK, OR RESET TO THE ADAPTER. THE
2266          ; INTERRUPT IS WAITED FOR, THE INTERRUPT STATUS SENSED,
2267          ; AND THE RESULT RETURNED TO THE CALLER.
2268          ;
2269          ; ON EXIT:   *DSKETTE_STATUS, CY REFLECT STATUS OF OPERATION.
2270          ;-----
2271
2272 0A93          CHK_STAT_2 PROC    NEAR
2273 0A93 B8 0AB1 R MOV    AX,OFFSET CS_BACK ; LOAD NEC_OUTPUT ERROR ADDRESS
2274 0A96 50          PUSH    AX
2275 0A97 E8 0ABA R CALL    WAIT_INT        ; WAIT FOR THE INTERRUPT
2276 0A9A 72 14          JC     J34             ; IF ERROR, RETURN IT
2277 0A9C B4 08          MOV    AH,08H         ; SENSE INTERRUPT STATUS COMMAND
2278 0A9E E8 09F0 R CALL    NEC_OUTPUT
2279 0AA1 E8 0AE2 R CALL    RESULTS        ; READ IN THE RESULTS
2280 0AA4 72 0A          JC     J34             ; IF ERROR, RETURN IT
2281 0AA6 A5 0042 R MOV    AL,*NEC_STATUS
2282 0AA9 24 60          AND    AL,01100000B   ; ISOLATE THE BITS
2283 0AAB 3C 60          CMP    AL,01100000B   ; TEST FOR CORRECT VALUE
2284 0AAD 74 03          JZ     J35             ; IF ERROR, GO MARK IT
2285 0AAF F8          CLC
2286 0AB0          J34:          POP    AX          ; THROW AWAY ERROR RETURN
2287 0AB0 58          RET
2288 0AB1          CS_BACK:     RET
2289 0AB1 C3
2290
2291 0AB2          J35:          OR     *DSKETTE_STATUS,BAD_SEEK ; ERROR RETURN CODE
2292 0AB2 80 0E 0041 R 40 STC
2293 0AB7 F9          JMP    SHORT J34
2294 0ABB EB F6          J35:          OR     *DSKETTE_STATUS,BAD_SEEK ; ERROR RETURN CODE
2295 0ABA          CHK_STAT_2 ENDP
2296          ;-----
2297          ; WAIT_INT
2298          ; THIS ROUTINE WAITS FOR AN INTERRUPT TO OCCUR A TIME OUT ;
2299          ; ROUTINE TAKES PLACE DURING THE WAIT, SO THAT AN ERROR ;
2300          ; MAY BE RETURNED IF THE DRIVE IS NOT READY.
2301          ;
2302          ; ON EXIT:   *DSKETTE_STATUS, CY REFLECT STATUS OF OPERATION.
2303          ;-----
2304
2304 0ABA          WAIT_INT  PROC    NEAR
2305 0ABA FB          STI
2306 0ABB F8          CLC
2307 0ABC B8 0901 H MOV    AX,09001H
2308 0ABF CD 15          INT    15H
2309 0AC1 72 11          JC     J36A
2310          ;
2311 0AC3 B3 04          MOV    BL,4
2312 0AC5 33 C9          XOR    CX,CX          ; CLEAR THE COUNTERS
2313 0AC7          ; FOR 2 SECOND WAIT
2314 0AC7 F6 06 003E R 80 J36: TEST *SEEK_STATUS,INT_FLAG ; TEST FOR INTERRUPT OCCURRING
2315 0ACC 75 0C          JNZ   J36
2316 0ACE E2 F7          LOOP  J36
2317 0ADD F6 CB          DEC   BL
2318 0AD2 75 F3          JNZ   J36
2319          ;
2320 0AD4 80 0E 0041 R 80 J36A: OR     *DSKETTE_STATUS,TIME_OUT ; NOTHING HAPPENED
2321 0AD9 F9          STC
2322 0ADA          J37:          OR     *DSKETTE_STATUS,TIME_OUT ; ERROR RETURN
2323 0ADA 9C          AND
2324 0ADB 80 26 003E R 7F AND *SEEK_STATUS,NOT_INT_FLAG ; TURN OFF INTERRUPT FLAG
2325 0AED 9D          POPF
2326 0AE1 C3          RET
2327 0AE2          WAIT_INT  ENDP
2328          ;
2329          ; RESULTS
2330          ; THIS ROUTINE WILL READ ANYTHING THAT THE NEC CONTROLLER ;
2331          ; RETURNS FOLLOWING AN INTERRUPT.
2332          ;
2333          ; ON EXIT:   *DSKETTE_STATUS, CY REFLECT STATUS OF OPERATION.
2334          ; AX,BX,CX,DX DESTROYED
2335          ;-----
2336
2336 0AE2          RESULTS  PROC    NEAR
2337 0AE2 57          PROC    NEAR
2338 0AE3 BF 0042 R MOV    DI,OFFSET *NEC_STATUS ; POINTER TO DATA AREA
2339 0AE6 B3 07          MOV    BL,7
2340 0AE8 BA 03F4 MOV    DX,03F4H          ; MAX STATUS BYTES
2341          ; STATUS PORT

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SECTION 5

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2342 ;----- WAIT FOR REQUEST FOR MASTER
2343
2344 0AEB B7 02          R10:  MOV    BH,2           ; HIGH ORDER COUNTER
2345 0AED 33 C9          XOR    CX,CX           ; COUNTER
2346 0AEF                J39:                ; WAIT FOR MASTER
2347 0AEE EC            IN     AL,DX           ; GET STATUS
2348 0AF0 24 C0        AND    AL,11000000B    ; KEEP ONLY STATUS AND DIRECTION
2349 0AF2 3C C0        CMP    AL,11000000B    ; STATUS 1 AND DIRECTION 0 ?
2350 0AF4 74 0E        JZ     J42            ; STATUS AND DIRECTION OK
2351 0AF6 E2 F7        LOOP   J39            ; LOOP TILL TIMEOUT
2352
2353 0AF8 FE CF        DEC    BH             ; DECREMENT HIGH ORDER COUNTER
2354 0AFA 75 F3        JNZ   J39            ; REPEAT TILL DELAY DONE
2355
2356 0AFC 80 0E 0041 R 80  STC    #DSKETTE_STATUS,TIME_OUT
2357 0B01 F9          OR     SHORT POPRES   ; SET ERROR RETURN
2358 0B02 EB 1B          JMP    SHORT POPRES   ; POP REGISTERS AND RETURN
2359
2360 ;----- READ IN THE STATUS
2361
2362 0B04                J42:                ; POINT AT DATA PORT
2363 0B04 42          INC    DX             ; GET THE DATA
2364 0B05 EC          IN     AL,DX         ; STORE THE BYTE
2365 0B06 B6 05        MOV    [DI],AL        ; INCREMENT THE POINTER
2366 0B08 47          INC    DI
2367
2368 0B09 B9 0002      MOV    CX,2           ; MINIMUM 12 MICROSECONDS FOR NEC
2369 0B0C EB 0000 E    CALL  WAITF          ; WAIT 15 TO 30 MICROSECONDS
2370 0B0F 4A          DEC    DX             ; POINT AT STATUS PORT
2371 0B10 EC          IN     AL,DX         ; GET STATUS
2372 0B11 A8 10        TEST   AL,00010000B  ; TEST FOR NEC STILL BUSY
2373 0B13 74 0A        JZ     POPRES         ; RESULTS DONE ?
2374
2375 0B15 FE CB        DEC    BL             ; DECREMENT THE STATUS COUNTER
2376 0B17 75 D2        JNZ   R10            ; GO BACK FOR MORE
2377 0B19 80 0E 0041 R 20  OR     #DSKETTE_STATUS,BAD_NEG
2378 0B1E F9          STC                    ; SET ERROR FLAG
2379
2380 ;----- RESULT OPERATION IS DONE
2381
2382 0B1F                POPRES:
2383 0B1F 5F          POP    DI             ; RETURN WITH CARRY SET
2384 0B20 C3          RET
2385 0B21
2386 -----
2387 ; READ_DSKCHNG
2388 ; READS THE STATE OF THE DISK CHANGE LINE.
2389 ;
2390 ; ON ENTRY:  DI = DRIVE #
2391 ;
2392 ; ON EXIT:  DI = DRIVE #
2393 ;           ZF = 0 : DISK CHANGE LINE INACTIVE
2394 ;           ZF = 1 : DISK CHANGE LINE ACTIVE
2395 ;           AX,CX,DX DESTROYED
2396 -----
2397 0B21                READ_DSKCHNG
2398 0B21 EB 0913 R    PROC  NEAR
2399 0B24 BA 03F7      CALL  MOTOR_ON       ; TURN ON THE MOTOR IF OFF
2400 0B27 EC          IN     DX,03F7H     ; ADDRESS DIGITAL INPUT REGISTER
2401 0B28 A8 80        TEST   AL,DSK_CHG   ; CHECK FOR DISK CHANGE LINE ACTIVE
2402 0B2A C3          RET                  ; RETURN TO CALLER WITH ZERO FLAG SET
2403 0B2B
2404 -----
2405 ; DRIVE_DET
2406 ; DETERMINES WHETHER DRIVE IS 80 OR 40 TRACKS AND
2407 ; UPDATES STATE INFORMATION ACCORDINGLY.
2408 ;
2409 ; ON ENTRY:  DI = DRIVE #
2410 -----
2411 0B2B                DRIVE_DET
2412 0B2B EB 0913 R    PROC  NEAR
2413 0B2E EB 0A7C R    CALL  RECAL_        ; TURN ON MOTOR IF NOT ALREADY ON
2414 0B31 72 3E        JC    DD_BAC        ; RECALIBRATE DRIVE
2415 0B33 B5 30        MOV    CH,TRK_SLAP  ; ASSUME NO DRIVE PRESENT
2416 0B35 E8 0A14 R   CALL  SEEK          ; SEEK TO TRACK 48
2417 0B38 72 37        JC    DD_BAC        ; "
2418 0B3A B5 0B        MOV    CH,QUIET_SEEK+1 ; ERROR NO DRIVE
2419 0B3C                ; SEEK TO TRACK 10
2420 0B3C FE CD        DEC    CH             ; DECREMENT TO NEXT TRACK
2421 0B3E 78 26        JS    IS_40          ; END LOOP IF CYLINDER COUNT NEGATIVE
2422 0B40 61          CX    PUSH           ; SAVE TRACK
2423 0B41 EB 0A14 R   CALL  SEEK          ;
2424 0B44 72 2C        JC    POP_BAC        ; POP AND RETURN
2425 0B46 B8 0B71 R   MOV    AX,OFFSET DD_BAC ; LOAD NEC OUTPUT ERROR ADDRESS
2426 0B49 50          PUSH  AX
2427 0B4A B4 04        MOV    AH,SENSE_DRY_ST ; SENSE DRIVE STATUS COMMAND BYTE
2428 0B4C EB 09F0 R   CALL  NEC_OUTPUT    ; OUTPUT TO NEC
2429 0B4F B8 C7        MOV    AX,DI         ; AL = DRIVE
2430 0B51 8A E0        MOV    AH,AL         ; AH = DRIVE
2431 0B53 E8 09F0 R   CALL  NEC_OUTPUT    ; OUTPUT TO NEC
2432 0B56 EB 0AE2 R   CALL  RESULTS       ; GO GET STATUS
2433 0B59 58          POP    AX             ; THROW AWAY ERROR ADDRESS
2434 0B5A 59          POP    CX             ; RESTORE TRACK
2435 0B5B F6 06 0042 R 10  TEST  #NEC_STATUS,HOME ; TRACK 0 ?
2436 0B60 74 DA        JZ    SK_GIN         ; GO TILL TRACK 0
2437 0B62 0A ED        OR     CH,CH         ; IS HOME AT TRACK 0 ?
2438 0B64 74 06        JZ    IS_80          ; MUST BE 80 TRACK DRIVE
2439
2440 ; DRIVE IS A 360; SET DRIVE TO DETERMINED;
2441 ; SET MEDIA TO DETERMINED AT RATE 250.
2442 IS_40:
2443 0B66 80 8D 0090 R 94  OR     #DSK_STATE[DI],DRV_DET+MED_DET+RATE_250
2444 0B6B C3          RET                  ; ALL INFORMATION SET
2445
2446 IS_80:
2447 0B6C 80 8D 0090 R 01  OR     #DSK_STATE[DI],TRK_CAPA ; SETUP 80 TRACK CAPABILITY
2448 0B71                DD_BAC:
2449 0B71 C3          RET
2450
2451 POP_BAC:
2452 0B72 59          POP    CX             ; THROW AWAY
2453 0B73 C3          RET
2454
2455 0B74                DRIVE_DET
2456                ENDP
    
```

```

2456 ;-----
2457 ; DISK_INT
2458 ; THIS ROUTINE HANDLES THE DISKETTE INTERRUPT.
2459 ;
2460 ; ON EXIT: THE INTERRUPT FLAG IS SET IN *SEEK_STATUS.
2461 ;-----
2462 DB74 DISK_INT | PROC FAR ; ENTRY POINT FOR ORG 0EF57H
2463 DB74 FB STI AX ; RE-ENABLE INTERRUPTS
2464 DB75 50 PUSH BX ; SAVE WORK REGISTER
2465 DB76 1E PUSH DS ; SAVE REGISTERS
2466 DB77 E8 0000 E CALL DDS ; SETUP DATA ADDRESSING
2467 DB7A 80 0E 003E R 80 OR *SEEK_STATUS,INT_FLAG ; TURN ON INTERRUPT OCCURRED
2468 DB7F 1F POP DS ; RESTORE USER (DS)
2469 DB80 B0 20 MOV AL,EOI ; END OF INTERRUPT MARKER
2470 DB82 E6 20 OUT INTA00,AL ; INTERRUPT CONTROL PORT
2471 DB84 B8 9101 MOV AX,09101H ; INTERRUPT POST CODE AND TYPE
2472 DB87 C7 15 INT 15H ; GO PERFORM OTHER TASK
2473 DB89 58 POP AX ; RECOVER REGISTER
2474 DB8A CF IRET ; RETURN FROM INTERRUPT
2475 DB8B
2476 ;-----
2477 ; DSKETTE_SETUP
2478 ; THIS ROUTINE DOES A PRELIMINARY CHECK TO SEE WHAT TYPE
2479 ; OF DISKETTE DRIVES ARE ATTACHED TO THE SYSTEM.
2480 ;-----
2481 DB8B DSKETTE_SETUP PROC NEAR
2482 DB8B 50 PUSH AX ; SAVE REGISTERS
2483 DB8C 53 PUSH BX
2484 DB8D 51 PUSH CX
2485 DB8E 52 PUSH DX
2486 DB8F 57 PUSH DI
2487 DB90 56 PUSH SI
2488 DB91 1E PUSH DS
2489 DB92 E8 0000 E CALL DDS ; POINT DATA SEGMENT TO BIOS DATA AREA
2490 DB95 80 0E 00A0 R 01 OR *RTC_WAIT_FLAG,01 ; NO RTC WAIT, FORCE USE OF LOOP
2491 DB9A C7 06 0090 R 0000 MOV WORD PTR *DSK_STATE,0 ; INITIALIZE STATES
2492 DBA0 80 26 008B R 33 AND *LAstrate,*NOT_STRT_MSK+*SEND_MSK ; CLEAR START & SEND
2493 DBA5 80 0E 008B R C0 OR *LAstrate,*SEND_MSK ; INITIALIZE SENT TO IMPOSSIBLE
2494 DBAA C6 06 003E R 00 MOV *SEEK_STATUS,0 ; INDICATE RECALIBRATE NEEDED
2495 DBAF 56 06 0040 R 00 MOV *MOTOR_COUNT,0 ; INITIALIZE MOTOR COUNT
2496 DBB4 C6 06 003F R 00 MOV *MOTOR_STATUS,0 ; INITIALIZE DRIVES TO OFF STATE
2497 DBB9 C6 06 0041 R 00 MOV *DSKETTE_STATUS,0 ; NO ERRORS
2498 DBBE AD 0010 R MOV AL,BYTE PTR *EQUIP_FLAG ; GET EQUIPMENT STATUS
2499 DBC1 D0 C0 ROL AL,1 ; SHIFT BITS 7,6 TO 1,0
2500 DBC3 D0 C0 ROL AL,1
2501 DBC5 24 03 AND AL,3 ; MASK DRIVE BITS
2502 DBC7 32 E4 XOR AH,AH ; AX=NUMBER OF DRIVES(RELATIVE ZERO)
2503 DBC9 33 FF XOR DI,DI ; DI=INITIAL DRIVE TO BE ESTABLISHED
2504 DBCB BE 0010 MOV SI,*HOME ; SI=HOME MASK FOR ALL DRIVES
2505 DBCC
2506 DBCE F6 06 00BF R 01 SUP0: TEST *HF_CNTRL,*DUAL ; TEST CONTROLLER TYPE
2507 DBD3 75 05 JNZ SUPT
2508 DBD5 C6 85 0090 R 94 MOV *DSK_STATE[DI],*DRV_DET+*MED_DET+*RATE_250
2509 DBDA SUP1:
2510 DBDA 50 PUSH AX ; SAVE DRIVE COUNT
2511 DBDB E8 082B R CALL DRIVE_DET ; DETERMINE DRIVE
2512 DBDE E8 0432 R CALL XLAT_OLD ; TRANSLATE STATE TO COMPATIBLE MODE
2513 DBE1 23 36 0042 R AND SI,*WORD PTR *NEC_STATUS ; AND NEC STATUS WITH HOME MASK
2514 DBE5 58 POP AX ; RESTORE DRIVE COUNT
2515 DBE6 47 INC DI ; POINT TO NEXT DRIVE
2516 DBE7 3B F8 CMP DI,AX
2517 DBE9 76 E3 JNA SUP0 ; REPEAT FOR EACH DRIVE
2518 DBEB SUP2:
2519 DBEB C6 06 003E R 00 MOV *SEEK_STATUS,0 ; FORCE RECALIBRATE
2520 DBF0 80 26 00A0 R FE AND *RTC_WAIT_FLAG,0FEH ; ALLOW FOR RTC WAIT
2521 DBF5 E8 0832 R CALL SETUP_END ; VARIOUS CLEANUPS
2522 DBF8 72 05 JC HOME_OK ; EXIT WITH CY_FLAG FROM SETUP_END
2523 DBFA 0B F6 OR SI,*ST ; TEST HOME INDICATORS FOR ALL DRIVES
2524 DBFC 75 01 JNZ HOME_OK
2525 DBFE F9 STC ; ERROR-->HOME INDICATOR BAD
2526 DBFF HOME_OK:
2527 DBFF 1F POP DS ; RESTORE CALLERS REGISTERS
2528 DC00 5E POP SI
2529 DC01 5F POP DI
2530 DC02 5A POP DX
2531 DC03 59 POP CX
2532 DC04 5B POP BX
2533 DC05 58 POP AX
2534 DC06 C3 RET
2535 DC07 DSKETTE_SETUP END
2536 DC07 CODE TENOS
2537 END
    
```

SECTION 5


```

115 ;----- ASCII STATUS
116
117 003B EB 00C4 R K2E: CALL K2S ; TEST FOR CHARACTER IN BUFFER (EXTENDED)
118 003E 74 1B JZ K2B ; RETURN IF BUFFER EMPTY
119 0040 9C PUSHF ; SAVE ZF FROM TEST
120 0041 E8 00D1 R CALL K10_E_XLAT ; ROUTINE TO XLATE FOR EXTENDED CALLS
121 0044 EB 11 JMP SHORT K2A ; GIVE IT TO THE CALLER
122
123 0046 E8 00C4 R K2: CALL K2S ; TEST FOR CHARACTER IN BUFFER
124 0049 74 0D JZ K2B ; RETURN IF BUFFER EMPTY
125 004B 9C PUSHF ; SAVE ZF FROM TEST
126 004C E8 00DC R CALL K10_S_XLAT ; ROUTINE TO XLATE FOR STANDARD CALLS
127 004F 73 06 JNC K2A ; CARRY CLEAR MEANS PASS VALID CODE
128 0051 9D POPF ; INVALID CODE FOR THIS TYPE OF CALL
129 0052 E8 009E R CALL K10 ; THROW THE CHARACTER AWAY
130 0055 EB EF JMP K2 ; GO LOOK FOR NEXT CHAR, IF ANY
131
132 0057 9D K2A: POPF ; RESTORE ZF FROM TEST
133 0058 59 K2B: POP CX
134 0059 5B POP BX ; RECOVER REGISTER
135 005A 1F POP DS ; RECOVER SEGMENT
136 005B CA 0002 RET 2 ; THROW AWAY FLAGS
137
138 ;----- SHIFT STATUS
139
140 005E K3E: MOV AH, *KB_FLAG_1 ; GET THE EXTENDED SHIFT STATUS FLAGS
141 005E 8A 26 0018 R AND AH, SYS_SHIFT ; GET SYSTEM SHIFT KEY STATUS
142 0062 80 E4 04 AND AH, SYS_SHIFT ; MASK ALL BUT SYS KEY BIT
143 0065 B1 05 MOV CL, 5 ; SHIFT THE SYSTEM KEY BIT OVER TO
144 0067 D2 E4 SHL AH, CL ; BIT 7 POSITION
145 0069 A0 0018 R MOV AL, *KB_FLAG_1 ; GET SHIFT STATES BACK
146 006C 24 73 AND AL, 01110011B ; ELIMINATE SYS_SHIFT, HOLD_STATE, AND INS_SHIFT
147 006E 0A E0 OR AH, AL ; MERGE THE REMAINING BITS INTO AH
148 0070 A0 0096 R MOV AL, *KB_FLAG_3 ; GET RIGHT CTL AND ALT
149 0073 24 0C AND AL, 00001100B ; ELIMINATE LC_E0 AND LC_E1
150 0075 0A E0 OR AH, AL ; OR THE SHIFT_FLAGS TOGETHER
151 0077 A0 0017 R K3: MOV AL, *KB_FLAG ; GET THE SHIFT STATUS FLAGS
152 007A EB A9 JMP K10_EXIT ; RETURN TO CALLER
153
154 ;----- WRITE TO KEYBOARD BUFFER
155
156 007C K500: PUSH SI
157 007C 56 MOV SI, BX ; GET THE "IN TO" POINTER TO THE BUFFER
158 007D FA CALL BX, [ *BUFFER_TAIL ] ; BUMP THE POINTER TO SEE IF BUFFER IS FULL
159 007E 8B 1E 001C R MOV SI, BX ; SAVE A COPY IN CASE BUFFER NOT FULL
160 0082 8B F3 CALL K4 ; WILL THE BUFFER OVERRUN IF WE STORE THIS?
161 0084 E8 0114 R CMP BX, [ *BUFFER_HEAD ] ; YES - INFORM CALLER OF ERROR
162 0087 3B 1E 001A R JE K502 ; NO - PUT THE ASCII/SCAN CODE INTO BUFFER
163 008B 74 0B JNE K504 ; TELL CALLER THAT OPERATION WAS SUCCESSFUL
164 008D 89 0C MOV [SI], CX ; SUB INSTRUCTION ALSO RESETS CARRY FLAG
165 008F 89 1E 001C R MOV [ *BUFFER_TAIL ], BX ; BUFFER FULL INDICATION
166 0093 2A C0 SUB AL, AL
167 0095 EB 03 90 JMP K504
168 0098 K502: MOV AL, 01H
169 0098 B0 01 K504: STI
170 009A FB POP SI
171 009A FB POP SI
172 009B 5E POP SI
173 009C EB 87 JMP K10_EXIT ; RETURN TO CALLER WITH STATUS IN AL
174
175 009E KEYBOARD_IO_1 ENDP

```

SECTION 5

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176                                     PAGE
177                                     |----- READ THE KEY TO FIGURE OUT WHAT TO DO -----|
178
179 009E                                K15  PROC  NEAR
180 009E 8B 1E 001A R                    MOV  BX,#BUFFER_HEAD ; GET POINTER TO HEAD OF BUFFER
181 00A2 3B 1E 001C R                    CMP  BX,#BUFFER_TAIL ; TEST END OF BUFFER
182 00A6 75 05                            JNE  KIT              ; IF ANYTHING IN BUFFER DONT DO INTERRUPT
183
184 00A8 B8 9002                          MOV  AX,09002H       ; MOVE IN WAIT CODE & TYPE
185 00AB CD 15                            INT  15H             ; PERFORM OTHER FUNCTION
186 00AD                                K1T:  INT  15H             ; ASCII READ
187 00AD FB                                STI  15H             ; INTERRUPTS BACK ON DURING LOOP
188 00AE 90                                NOP                  ; ALLOW AN INTERRUPT TO OCCUR
189 00AF FA                                CLI  15H             ; INTERRUPTS BACK OFF
190 00B0 8B 1E 001A R                    MOV  BX,#BUFFER_HEAD ; GET POINTER TO HEAD OF BUFFER
191 00B4 3B 1E 001C R                    CMP  BX,#BUFFER_TAIL ; TEST END OF BUFFER
192 00B8 74 F3                            JE   KIT             ; LOOP UNTIL SOMETHING IN BUFFER
193 00BA 8B 07                            MOV  AX,[BX]         ; GET SCAN CODE AND ASCII CODE
194 00BC EB 0114 R                        CALL  K4              ; MOVE POINTER TO NEXT POSITION
195 00BF 89 1E 001A R                    MOV  #0,BUFFER_HEAD, BX ; STORE VALUE IN VARIABLE
196 00C3 C3                                RET                  ; RETURN
197 00C4                                K15  ENDP
198
199
200                                     |----- READ THE KEY TO SEE IF ONE IS PRESENT -----|
201
202 00C4                                K25  PROC  NEAR
203 00C4 FA                                CLI  15H             ; INTERRUPTS OFF
204 00C5 8B 1E 001A R                    MOV  BX,#BUFFER_HEAD ; GET POINTER
205 00C9 3B 1E 001C R                    CMP  BX,#BUFFER_TAIL ; IF EQUAL (Z=1) THEN NOTHING THERE
206 00CD 8B 07                            MOV  AX,[BX]         ;
207 00CF FB                                STI  15H             ; INTERRUPTS BACK ON
208 00D0 C3                                RET                  ; RETURN
209 00D1                                K25  ENDP
210
211
212                                     |----- ROUTINE TO TRANSLATE SCAN CODE PAIRS FOR EXTENDED CALLS
213
214 00D1                                K1O_E_XLAT:
215 00D1 3C F0                            CMP  AL,0F0h         ; IS IT ONE OF THE FILL-INs?
216 00D3 75 06                            JNE  K1O_E_RET       ; NO, PASS IT ON
217 00D5 0A E4                            OR   AH,AH           ; AH = 0 IS SPECIAL CASE
218 00D7 74 02                            JZ   K1O_E_RET       ; PASS THIS ON UNCHANGED
219 00D9 32 C0                            XOR  K1O_E_RET,AL    ; GET SCAN CODE AND ASCII CODE
220 00DB                                K1O_E_RET:          AL,AL                ; OTHERWISE SET AL = 0
221 00DB C3                                RET                  ; GO BACK
222
223
224                                     |----- ROUTINE TO TRANSLATE SCAN CODE PAIRS FOR STANDARD CALLS
225
226 00DC                                K1O_S_XLAT:
227 00DC 80 FC E0                          CMP  AH,0E0h         ; IS IT KEYPAD ENTER OR / ?
228 00DF 75 12                            JNE  K1O_S2          ; NO, CONTINUE
229 00E1 3C 0D                            CMP  AL,0Dh          ; KEYPAD ENTER CODE?
230 00E3 74 09                            JE   K1O_S1          ; YES, MESSAGE A BIT
231 00E5 3C 0A                            CMP  AL,0Ah          ; CTRL KEYPAD ENTER CODE?
232 00E7 74 05                            JE   K1O_S1          ; YES, MESSAGE THE SAME
233 00E9 B4 35                            MOV  AH,35h          ; NO, MUST BE KEYPAD /
234 00EB EB 23 90                          JMP  K1O_USE         ; GIVE TO CALLER
235 00EE B4 1C                            K1O_S1: MOV  AH,Tch   ; CONVERT TO COMPATIBLE OUTPUT
236 00F0 EB 1E 90                          JMP  K1O_USE         ; GIVE TO CALLER
237
238 00F3 80 FC 84                          K1O_S2: CMP  AH,84h   ; IS IT ONE OF THE EXTENDED ONES?
239 00F6 77 1A                            JA   K1O_DIS         ; YES, THROW AWAY AND GET ANOTHER CHAR
240
241 00F8 3C F0                            CMP  AL,0F0h         ; IS IT ONE OF THE FILL-INs?
242 00FA 75 07                            JNE  K1O_S3          ; NO, TRY LAST TEST
243 00FC 0A E4                            OR   AH,AH           ; AH = 0 IS SPECIAL CASE
244 00FE 74 10                            JZ   K1O_USE         ; PASS THIS ON UNCHANGED
245 0100 EB 10 90                          JMP  K1O_DIS         ; THROW AWAY THE REST
246
247 0103 3C E0                            K1O_S3: CMP  AL,0E0h   ; IS IT AN EXTENSION OF A PREVIOUS ONE?
248 0105 75 09                            JNE  K1O_USE         ; NO, MUST BE A STANDARD CODE
249 0107 0A E4                            OR   AH,AH           ; AH = 0 IS SPECIAL CASE
250 0109 74 05                            JZ   K1O_USE         ; JUMP IF AH = 0
251 010B 32 C0                            XOR  K1O_USE,AL      ; CONVERT TO COMPATIBLE OUTPUT
252 010D EB 01 90                          JMP  K1O_USE         ; PASS IT ON TO CALLER
253
254 0110                                K1O_USE:
255 0110 F8                                CLC                  ; CLEAR CARRY TO INDICATE GOOD CODE
256 0111 C3                                RET                  ; RETURN
257 0112                                K1O_DIS:
258 0112 F9                                STC                  ; SET CARRY TO INDICATE DISCARD CODE
259 0113 C3                                RET                  ; RETURN

```

```

260
261
262
263
264
265 0114
266 0114 43
267 0115 43
268
269 0116 3B 1E 0082 R
270 011A 72 04
271 011C 8B 1E 0080 R
272 0120 C3
273 0121
274
275
276
277
278 0121
279 0121 50
280 0122 53
281 0123 51
282 0124 52
283 0125 56
284 0126 57
285 0127 1E
286 0128 06
287 0129 FC
288 012A E8 0000 E
289 012D E4 60
290 012F 93
291
292
293
294
295 0130 E4 61
296 0132 8A E0
297 0134 0C 80
298 0136 E6 61
299 0138 86 E0
300 013A E6 61
301 013C FB
302 013D 93
303
304
305
306 013E B4 4F
307 0140 F9
308 0141 CD 15
309
310 0143 72 03
311 0145 E9 02CA R
312
313 0148
314 0148 8A E0
315
316
317
318 014A 3C FF
319 014C 75 03
320 014E E9 0540 R
321
322 0151 0E
323 0152 07
324 0153 8A 3E 0096 R
325
326 0157
327 0157 3C E0
328 0159 75 07
329 015B 80 0E 0096 R 12
330 0160 EB 09
331
332 0162
333 0162 3C E1
334 0164 75 08
335 0166 80 0E 0096 R 11
336 016B E9 02CF R
337
338 016E
339 016E 24 7F
340 0170 F6 C7 02
341 0173 74 0C
342
343 0175 B9 0002
344 0178 9F 0555 R
345 017B F2/ AE
346 017D 75 54
347 017F EB 3D
348
349 0181
350 0181 F6 C7 01
351 0184 74 16
352
353 0186 B9 0004
354 0189 9F 0553 R
355 018C F2/ AE
356 018E 74 DB
357
358 0190 3C 45
359 0192 75 2A
360 0194 F6 C4 80
361 0197 75 25
362 0199 E9 03FF R

```

PAGE
 ;-----
 INCREMENT BUFFER POINTER ROUTINE
 ;-----

```

K4 PROC NEAR
INC BX
INC BX ; MOVE TO NEXT WORD IN LIST
CMP BX,#BUFFER_END ; AT END OF BUFFER?
JB K5 ; NO, CONTINUE
MOV BX,#BUFFER_START ; YES, RESET TO BUFFER BEGINNING
K5: RET
K4: ENDP

```

;----- KEYBOARD INTERRUPT ROUTINE

```

KB_INT_1 PROC FAR
PUSH AX ; SAVE THE STI UNTIL AFTER KEYBOARD RESET
PUSH BX
PUSH CX
PUSH DX
PUSH SI
PUSH DI
PUSH ES
PUSH DS
CLD ; FORWARD DIRECTION
CALL DDS ; SET UP ADDRESSING TO DATA SEGMENT
IN AL,KB_DATA ; READ IN THE CHARACTER
XCHG BX,AX ; SAVE IT

```

;----- RESET THE SHIFT REGISTER ON THE PLANAR IF ENABLED, OR DO NOTHING IF IT IS DISABLED

```

IN AL,KB_CTL ; GET THE CONTROL PORT
MOV AH,AL ; SAVE VALUE
OR AL,80H ; RESET BIT FOR KEYBOARD
OUT KB_CTL,AL
XCHG AH,AL ; GET BACK ORIGINAL CONTROL
OUT KB_CTL,AL ; KB HAS BEEN RESET
STI
XCHG AX,BX ; RESTORE DATA IN

```

;----- SYSTEM HOOK INT 15H - FUNCTION 4FH (ON HARDWARE INTERRUPT LEVEL 9H)

```

MOV AH,04FH ; SYSTEM INTERCEPT - KEY CODE FUNCTION
STC ; SET CY= 1 (IN CASE OF IRET)
INT 15H ; CASSETTE CALL (AL)= KEY SCAN CODE
; RETURNS CY= 1 FOR INVALID FUNCTION
JC KB_INT_PC ; CONTINUE IF CARRY FLAG SET ((AL)=CODE)
JMP K26 ; EXIT IF SYSTEM HANDLED SCAN CODE
; EXIT HANDLES HARDWARE EOI AND ENABLE

```

KB_INT_PC: MOV AH,AL ; SAVE SCAN CODE IN AH ALSO

;----- TEST FOR OVERRUN SCAN CODE FROM KEYBOARD

```

CMP AL,OFFH ; IS THIS AN OVERRUN CHAR
JNZ K16 ; NO, TEST FOR SHIFT KEY
JMP K62 ; BUFFER_FULL_BEEP

```

K16: PUSH CS ; ESTABLISH ADDRESS OF TABLES
POP ES ; LOAD FLAGS FOR TESTING
MOV BH,#KB_FLAG_3

TEST_E0: CMP AL,MC_E0 ; IS THIS THE GENERAL MARKER CODE?
JNE TEST_E1 ;
OR #KB_FLAG_3,LC_E0+KBX ; SET FLAG BIT, SET KBX, AND
JMP SHORT EXIT_K ; THROW AWAY THIS CODE

TEST_E1: CMP AL,MC_E1 ; IS THIS THE PAUSE KEY?
JNE NOT_HC ;
OR #KB_FLAG_3,LC_E1+KBX ; SET FLAG, PAUSE KEY MARKER CODE
EXIT_K: JMP K26A ; THROW AWAY THIS CODE

NOT_HC: AND AL,07FH ; TURN OFF THE BREAK BIT
TEST BH,LC_E0 ; WAS LAST CODE THE E0 MARKER CODE?
JZ ; JUMP IF NOT
MOV CX,2 ; LENGTH OF SEARCH
MOV DI,OFFSET K6+6 ; IS THIS A SHIFT KEY?
REPNE SCASB ; CHECK IT
JNE K16A ; NO, CONTINUE KEY PROCESSING
JMP SHORT K16B ; YES, THROW AWAY & RESET FLAG

NOT_LC_E0: TEST BH,LC_E1 ; WAS LAST CODE THE E1 MARKER CODE?
JZ ; JUMP IF NOT
MOV CX,4 ; LENGTH OF SEARCH
MOV DI,OFFSET K6+4 ; IS THIS AN ALT, CTL, OR SHIFT?
REPNE SCASB ; CHECK IT
JE EXIT_K ; THROW AWAY IF SO
CMP AL,NUM_KEY ; IS IT THE PAUSE KEY?
JNE K16B ; NO, THROW AWAY & RESET FLAG
TEST AH,80H ; YES, IS IT THE BREAK OF THE KEY?
JNZ K16B ; YES, THROW THIS AWAY, TOO
JMP K39P ; NO, THIS IS THE REAL PAUSE STATE

```

363          PAGE
364          I----- TEST FOR SYSTEM KEY
365
366          T_SYS_KEY:
367          CMP     AL,SYS_KEY          ; IS IT THE SYSTEM KEY?
368          JNE     K16A                ; CONTINUE IF NOT
369
370          01A0 F6 C4 80              TEST     AH,080H                    ; CHECK IF THIS A BREAK CODE
371          01A3 75 1C                  JNZ     K16C                        ; DONT TOUCH SYSTEM INDICATOR IF TRUE
372
373          01A5 F6 06 0018 R 04        TEST     0KB_FLAG_1,SYS_SHIFT      ; SEE IF IN SYSTEM KEY HELD DOWN
374          01AA 75 12                  JNZ     K16B                        ; IF YES, DONT PROCESS SYSTEM INDICATOR
375
376          01AC 80 0E 0018 R 04        OR      0KB_FLAG_1,SYS_SHIFT      ; INDICATE SYSTEM KEY DEPRESSED
377          01B1 80 20                    MOV     AL,E01                      ; END OF INTERRUPT COMMAND
378          01B3 E6 20                    OUT    020H,AL                    ; SEND COMMAND TO INTERRUPT CONTROL PORT
379
380          01B5 B8 8500                 MOV     AX,08500H                  ; FUNCTION VALUE FOR MAKE OF SYSTEM KEY
381          01B8 FB                       STI                                     ; MAKE SURE INTERRUPTS ENABLED
382          01B9 CD 15                   INT    15H                         ; USER INTERRUPT
383          01BB E9 02D4 R               JMP     K27                          ; END PROCESSING
384
385          01BE E9 02CA R               K16B:  JMP     K26                    ; IGNORE SYSTEM KEY
386
387          01C1 80 26 0018 R FB        K16C:  AND     0KB_FLAG_1,NOT SYS_SHIFT ; TURN OFF SHIFT KEY HELD DOWN
388          01C6 80 20                    MOV     AL,E01                      ; END OF INTERRUPT COMMAND
389          01C8 E6 20                    OUT    020H,AL                    ; SEND COMMAND TO INTERRUPT CONTROL PORT
390
391          01CA B8 8501                 MOV     AX,08501H                  ; FUNCTION VALUE FOR BREAK OF SYSTEM KEY
392          01CD FB                       STI                                     ; MAKE SURE INTERRUPTS ENABLED
393          01CE CD 15                   INT    15H                         ; USER INTERRUPT
394          01D0 E9 02D4 R               JMP     K27                          ; IGNORE SYSTEM KEY
395
396          I----- TEST FOR SHIFT KEYS
397
398          01D3 8A 1E 0017 R           K16A:  MOV     BL,0KB_FLAG              ; PUT STATE FLAGS IN BL
399          01D7 BF 054F R               MOV     DI,OFFSET K6              ; SHIFT KEY TABLE
400          01DA B9 0008 90              MOV     CX,K6L                    ; LENGTH
401          01DE F2 AE                   RDPNE  SCA                         ; LOOK THROUGH THE TABLE FOR A MATCH
402          01E0 8A C4                   MOV     AL,AH                      ; RECOVER SCAN CODE
403          01E2 74 03                   JE     K17                          ; JUMP IF MATCH FOUND
404          01E4 E9 02B6 R               JMP     K25                          ; IF NO MATCH, THEN SHIFT NOT FOUND
405
406          I----- SHIFT KEY FOUND
407
408          01E7 81 EF 0550 R           K17:   SUB     DI,OFFSET K6+1            ; ADJUST PTR TO SCAN CODE MTC
409          01EB 2E: BA A5 0557 R        MOV     AH,CS:K7[DI]              ; GET MASK INTO AH
410          01F0 B1 02                   MOV     CL,2                       ; SET UP COUNT FOR FLAG SHIFTS
411          01F2 AB 80                   TEST   AL,80H                     ; TEST FOR KEY BREAK
412          01F4 74 03                   JZ     K17C                        ; JUMP IF BREAK
413          01F6 EB 6E 90               JMP     K23                          ; JUMP IF BREAK
414
415          I----- SHIFT MAKE FOUND, DETERMINE SET OR TOGGLE
416
417          01F9 80 FC 10               K17C:  CMP     AH,SCROLL_SHIFT          ; IF SCROLL SHIFT OR ABOVE, TOGGLE KEY
418          01FC 73 21                   JAE    K18                          ;
419
420          I----- PLAIN SHIFT KEY, SET SHIFT ON
421
422          01FE 08 26 0017 R           OR      0KB_FLAG,AH                ; TURN ON SHIFT BIT
423          0202 F6 C4 0C               TEST   AH,CTL_SHIFT+ALT_SHIFT     ; IS IT ALT OR CTRL?
424          0205 75 03                   JNZ    K17D                        ; YES, MORE FLAGS TO SET
425          0207 E9 02CA R               JMP     K26                          ; NO, INTERRUPT RETURN
426          020A F6 C7 02               K17D:  TEST   BH,LC_E0                  ; IS THIS ONE OF THE NEW KEYS?
427          020D 74 07                   JZ     K17E                        ; NO, JUMP
428          020F 08 26 009E R          OR      0KB_FLAG_3,AH              ; SET BITS FOR RIGHT CTRL, ALT
429          0213 E9 02CA R               JMP     K26                          ; INTERRUPT_RETURN
430          0216 D2 EC                   K17E:  SHR     AH,CL                    ; MOVE FLAG BITS TWO POSITIONS
431          0218 08 26 0018 R          OR      0KB_FLAG_1,AH              ; SET BITS FOR LEFT CTRL, ALT
432          021C E9 02CA R               JMP     K26                          ; INTERRUPT_RETURN
433
434          I----- TOGGLED SHIFT KEY, TEST FOR 1ST MAKE OR NOT
435
436          021F                       K18:   TEST   BL,CTL_SHIFT              ; SHIFT-TOGGLE
437          0221 F6 C3 04               JZ     K18A                        ; CHECK CTL SHIFT STATE
438          0222 74 03                   JZ     K25                          ; JUMP IF NOT CTL STATE
439          0224 E9 02B6 R               JMP     K25                          ; JUMP IF CTL STATE
440          0227 3C 52                   K18A:  CMP     AL,INS_KEY                ; CHECK FOR INSERT KEY
441          0229 75 21                   JNE    K22                          ; JUMP IF NOT INSERT KEY
442          022B F6 C3 08               TEST   BL,ALT_SHIFT              ; CHECK FOR ALTERNATE SHIFT
443          022E 74 03                   JZ     K18B                        ; JUMP IF NOT ALTERNATE SHIFT
444          0230 E9 02B6 R               JMP     K25                          ; JUMP IF ALTERNATE SHIFT
445          0233 F6 C7 02               K18B:  TEST   BH,LC_E0                  ; IS THIS THE NEW INSERT KEY?
446          0236 75 14                   JNE    K22                          ; YES, THIS ONE'S NEVER A "0"
447          0238 F6 C3 20               K19:   TEST   BL,NUM_STATE              ; CHECK FOR BASE STATE
448          023B 75 0A                   JNZ    K21                          ; JUMP IF NUM LOCK IS ON
449          023D F6 C3 03               TEST   BL,LEFT_SHIFT+RIGHT_SHIFT ; TEST FOR SHIFT STATE
450          0240 74 0A                   JZ     K22                          ; JUMP IF BASE STATE
451          0242
452          0242 8A E0                   K20:   MOV     AH,AL                      ; PUT SCAN CODE BACK INTO AH
453          0244 EB 70 90               JMP     K25                          ; NUMERAL "0", STNRDR. PROCESSING
454
455          0247 F6 C3 03               K21:   TEST   BL,LEFT_SHIFT+RIGHT_SHIFT ; MIGHT BE NUMERIC
456          024A 74 F6                   JZ     K20                          ; IS NUMERIC, STD. PROC.
457
458          024C                       K22:   TEST   AH,0KB_FLAG_1             ; SHIFT TOGGLE KEY HIT; PROCESS IT
459          024C 84 26 0018 R          JZ     K22A                        ; IS KEY ALREADY DEPRESSED
460          0250 74 03                   TEST   K22A                        ; JUMP IF KEY ALREADY DEPRESSED
461          0252 EB 76 90               JMP     K22A                        ; INDICATE THAT THE KEY IS DEPRESSED
462          0255 08 26 0018 R          OR      0KB_FLAG_1,AH              ; TOGGLE THE SHIFT STATE
463          0259 30 26 0017 R          XOR     0KB_FLAG,AH                ; TEST FOR 1ST MAKE OF INSERT KEY
464          026D 3C 52                   CMP     AL,INS_KEY                ; JUMP IF NOT INSERT KEY
465          026F 75 69                   JNE    K26                          ; SCAN CODE IN BOTH HALVES OF AX
466          0261 8A E0                   MOV     AH,AL                      ; FLAGS UPDATED, PROC. FOR BUFFER
467          0263 EB 78 90               JMP     K28
468
469          I----- BREAK SHIFT FOUND
470
471          0266 80 FC 10               K23:   CMP     AH,SCROLL_SHIFT          ; BREAK-SHIFT-FOUND
472          0269 F6 D4                   NOT    AH                          ; INVERT MASK
473          026B 73 43                   JAE    K24                          ; YES, HANDLE BREAK TOGGLE
474          026D 20 26 0017 R          AND     0KB_FLAG,AH                ; TURN OFF SHIFT BIT
475          0271 80 FC FB               CMP     AH,NOT_CTL_SHIFT          ; IS THIS ALT OR CTL?

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477 0274 77 26          JA      K23D          ; NO, ALL DONE
478
479 0276 F6 C7 02      TEST   BH,LC_E0          ; NO, ALT OR CTL?
480 0279 74 06          JZ     K23A          ; NO, HANDLE NORMALLY
481 027B 20 26 0096 R  AND    *KB_FLAG_3,AH   ; RESET BIT FOR RIGHT ALT OR CTL
482 027F EB 06          JMP    SHORT K23B      ; CONTINUE
483 0281 D2 FC          K23A: SAR   AH,CL       ; MOVE THE MASK BIT TWO POSITIONS
484 0283 20 26 0018 R  AND    *KB_FLAG_1,AH   ; RESET BIT FOR LEFT ALT OR CTL
485 0287 8A E0          K23B: MOV   AH,AL        ; SAVE SCAN CODE
486 0289 A0 0096 R     MOV   AL,*KB_FLAG_3   ; GET RIGHT ALT & CTRL FLAGS
487 028C D2 E8          SHR   AL,CL           ; MOVE TO BITS 1 & 0
488 028E 0A 06 0018 R  OR    AL,*KB_FLAG_1   ; PUT IN LEFT ALT & CTL FLAGS
489 0292 D2 E0          SHL   AL,CL           ; MOVE BACK TO BITS 3 & 2
490 0294 24 0C          AND   AL,ALT_SHIFT+CTL_SHIFT ; FILTER OUT OTHER GARBAGE
491 0296 08 06 0017 R  OR    *KB_FLAG,AL     ; PUT RESULT IN THE REAL FLAGS
492 029A 8A C4          MOV   AL,AH          ; RECOVER SAVED SCAN CODE
493
494 029C 3C B8          K23D: CMP   AL,ALT_KEY+80H ; IS THIS ALTERNATE SHIFT RELEASE
495 029E 75 2A          JNE   K26             ; INTERRUPT_RETURN
496
497 ;----- ALTERNATE SHIFT KEY RELEASED, GET THE VALUE INTO BUFFER
498
499 02A0 A0 0019 R     MOV   AL,*ALT_INPUT   ;
500 02A3 B4 00          MOV   AH,0            ; SCAN CODE OF 0
501 02A5 B8 26 0019 R  MOV   *ALT_INPUT,AH   ; ZERO OUT THE FIELD
502 02A9 3C 00          CMP   AL,0            ; WAS THE INPUT = 0?
503 02AB 74 1D          JE    K26             ; INTERRUPT_RETURN
504 02AD E9 0519 R     JMP   K61             ; IT WASN'T, SO PUT IN BUFFER
505
506 02B0          K24:          ; BREAK-TOGGLE
507 02B2 20 26 0018 R  AND   *KB_FLAG_1,AH   ; INDICATE NO LONGER DEPRESSED
508 02B4 EB 14          JMP   SHORT K26       ; INTERRUPT_RETURN
509
510 ;----- TEST FOR HOLD STATE
511
512 02B6          K25:          ; AL, AH = SCAN CODE
513 02B8 3C 80          CMP   AL,80H         ; NO-SHIFT-FOUND
514 02BB 73 10          JAE   K26             ; TEST FOR BREAK KEY
515 02BA F6 06 0018 R 08 TEST  *KB_FLAG_1,HOLD_STATE ; NOTHING FOR BREAK CHARS FROM HERE ON
516 02BF 74 1C          JZ    K28             ; ARE WE IN HOLD STATE
517 02C1 3C 45          CMP   AL,NUM_KEY     ; BRANCH AROUND TEST IF NOT
518 02C3 74 05          JE    K26             ; CAN'T END HOLD ON NUM LOCK
519 02C5 80 26 0018 R FT AND   *KB_FLAG_1,NOT_HOLD_STATE ; TURN OFF THE HOLD STATE BIT
520
521 02CA          K26:          ;
522 02CC 80 26 0096 R FC AND   *KB_FLAG_3,NOT_LC_E0+LC_E1 ; RESET LAST CHAR H.C. FLAG
523
524 02CF          K26A:        ; INTERRUPT-RETURN
525 02CF FA          CLI                    ; TURN OFF INTERRUPTS
526 02D0 B0 20          MOV   AL,E01         ; END OF INTERRUPT COMMAND
527 02D2 E6 20          OUT  020H,AL         ; SEND COMMAND TO INTERRUPT CONTROL PORT
528
529 02D4          K27:          ; INTERRUPT-RETURN-NO-E01
530 02D4 07          POP   ES              ; RESTORE REGISTERS
531 02D5 1F          POP   DS
532 02D6 5F          POP   DI
533 02D7 5E          POP   SI
534 02D8 5A          POP   DX
535 02D9 59          POP   CX
536 02DA 58          POP   BX
537 02DB 58          POP   AX
538 02DC CF          IRET                  ; RETURN, INTERRUPTS BACK ON
539 ; WITH FLAG CHANGE

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540                                     PAGE
541                                     :----- NOT IN HOLD STATE
542
543 02DD                                : AL, AH = SCAN CODE (ALL MAKES)
544 02DD 3C 58                          : NO-HOLD-STATE
545 02DF 77 E9                          : TEST FOR OUT-OF-RANGE SCAN CODES
546                                     : IGNORE IF OUT-OF-RANGE
547 02E1 F6 C3 08                       : ARE WE IN ALTERNATE SHIFT?
548 02E4 74 0C                          : JUMP IF NOT ALTERNATE
549
550 02E6 F6 C7 10                       : IS THIS THE ENHANCED KEYBOARD?
551 02E9 74 0A                          : NO, ALT STATE IS REAL
552
553 02EB F6 06 0018 R 04                 : YES, IS SYSREQ KEY DOWN?
554 02F0 74 03                          : NO, ALT STATE IS REAL
555 02F2 E9 03CC R                      : YES, THIS IS PHONY ALT STATE
556                                     : DUE TO PRESSING SYSREQ
557
558                                     ;----- TEST FOR RESET KEY SEQUENCE (CTL ALT DEL)
559
560 02F5                                : TEST-RESET
561 02F5 F6 C3 04                       : ARE WE IN CONTROL SHIFT ALSO?
562 02F8 74 07                          : NO-RESET
563 02FA 3C 53                          : SHIFT STATE IS THERE, TEST KEY
564 02FC 75 33                          : NO-RESET, IGNORE
565
566                                     ;----- CTL-ALT-DEL HAS BEEN FOUND, DO I/O CLEANUP
567
568 02FE C7 06 0072 R 1234              : SET FLAG FOR RESET FUNCTION
569 0304 81 26 0096 R 0010              : CLEAR ALL FLAG BITS EXCEPT KBX
570 030A E9 0000 E                      : JUMP TO POWER ON DIAGNOSTICS
571
572                                     ;----- ALT-INPUT-TABLE
573 030D                                K30: LABEL BYTE
574 030D 52 4F 50 51 4B                DB 82,79,80,81,75
575 0312 4C 4D 47 48 49                DB 76,77,71,72,73
576                                     : 10 NUMBERS ON KEYPAD
577                                     ;----- SUPER-SHIFT-TABLE
578 0317 10 11 12 13 14 15            DB 16,17,18,19,20,21
579 031D 16 17 18 19 1E 1F            DB 22,23,24,25,30,31
580 0323 20 21 22 23 24 25            DB 32,33,34,35,36,37
581 0329 26 2C 2D 2E 2F 30            DB 38,44,45,46,47,48
582                                     DB 49,50
583
584                                     ;----- IN ALTERNATE SHIFT, RESET NOT FOUND
585
586 0331                                K31: : NO-RESET
587 0331 3C 39                          : TEST FOR SPACE KEY
588 0333 75 05                          : NOT THERE
589 0337 E9 050D R                      : SET SPACE CHAR
590 033A                                K311: : BUFFER_FILL
591 033A 3C 0F                          : TEST FOR TAB KEY
592 033C 75 06                          : NOT THERE
593 033E BB A500                        : SET SPECIAL CODE FOR ALT-TAB
594 0341 E9 050D R                      : BUFFER_FILL
595 0344
596 0344 3C 4A                          K312: : TEST FOR KEYPAD -
597 0346 74 79                          : GO PROCESS
598 0348 3C 4E                          : TEST FOR KEYPAD +
599 034A 74 75                          : GO PROCESS
600
601                                     ;----- LOOK FOR KEY PAD ENTRY
602
603 034C                                K32: : ALT-KEY-PAD
604 034C BF 030D R                      : ALT-INPUT-TABLE
605 034F B9 000A                        : LOOK FOR ENTRY USING KEYPAD
606 0352 F2/ AE                        : LOOK FOR MATCH
607 0354 75 18                          : NO ALT-KEYPAD
608 0356 F6 C7 02                      : TEST BH,LC_E0
609 0359 75 6B                          : IS THIS ONE OF THE NEW KEYS?
610 035B 81 EF 030E R                  : YES, JUMP, NOT NUMPAD KEY
611 035F A0 0019 R                      : DI NOW HAS ENTRY VALUE
612 0362 B4 0A                          : GET THE CURRENT BYTE
613 0364 F6 E4                          : MULTIPLY BY 10
614 0366 03 C7                          : ADD IN THE LATEST ENTRY
615 0368 A2 0019 R                      : STORE IT AWAY
616 036B E9 02CA R                    K32A: : THROW AWAY THAT KEYSTROKE
617                                     JMP K26
618
619                                     ;----- LOOK FOR SUPERSHIFT ENTRY
620
621 036E                                K33: : NO-ALT-KEYPAD
622 036E C6 06 0019 R 00                : ZERO ANY PREVIOUS ENTRY INTO INPUT
623 0373 B9 001A                        : DI,ES ALREADY POINTING
624 0376 F2/ AE                        : LOOK FOR MATCH IN ALPHABET
625 0378 74 42                          : MATCH FOUND, GO FILL THE BUFFER
626
627                                     ;----- LOOK FOR TOP ROW OF ALTERNATE SHIFT
628
629 037A                                K34: : ALT-TOP-ROW
630 037A 3C 02                          : KEY WITH '1' ON IT
631 037E 72 43                          : NOT ONE OF INTERESTING KEYS
632 0380 77 05                          : IS IT IN THE REGION
633 0382 80 C4 76                      : NO, BRANCH
634 0385 EB 35                          : CONVERT PSEUDO SCAN CODE TO RANGE
635                                     : GO FILL THE BUFFER
636
637                                     ;----- TRANSLATE ALTERNATE SHIFT PSEUDO SCAN CODES
638
639 0387                                K35: : ALT-FUNCTION
640 0387 3C 57                          : IS IT F11?
641 0389 72 09                          : NO, BRANCH
642 038D 77 05                          : IS IT F12?
643 038F 80 C4 34                      : NO, BRANCH
644 0392 EB 28                          : CONVERT TO PSEUDO SCAN CODE
645                                     : GO FILL THE BUFFER
646
647 0394                                K35A: : DO WE HAVE ONE OF THE NEW KEYS?
648 0394 F6 C7 02                      : NO, JUMP
649 0397 74 18                          : TEST FOR KEYPAD ENTER
650 0399 3C 1C                          : NOT THERE
651 039B 75 06                          : SPECIAL CODE
652 039D BB A600                        : BUFFER_FILL
653 03A0 E9 050D R                      : TEST FOR DELETE KEY
654 03A3 3C 53                          : HANDLE WITH OTHER EDIT KEYS
655 03A5 74 1F                          :

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654 03A7 3C 35      CMP     AL,53      ; TEST FOR KEYPAD /
655 03A9 74 00      JNE     K32A      ; NOT THERE, NO OTHER E0 SPECIALS
656 03AB BB A400    MOV     AX,0A400h ; SPECIAL CODE
657 03AE E9 050D R  JMP     K5T       ; BUFFER_FILL
658
659 03B1 3C 3B      K37:   CMP     AL,59      ; TEST FOR IN TABLE
660 03B3 72 0C      JB     K37B      ; ALT-CONTINUE
661 03B5 3C 44      CMP     AL,68      ; IN KEYPAD REGION?
662
663 03B7 77 B2      JA     K32A      ; OR NUMLOCK, SCROLLLOCK?
664 03B9 80 C4 2D   ADD     AH,45     ; IF SO, IGNORE
665
666 03BC B0 00      K37A:  MOV     AL,0      ; ASCII CODE OF ZERO
667 03BE E9 050D R  JMP     K5T       ; PUT IT IN THE BUFFER
668
669 03C1 B0 F0      K37B:  MOV     AL,0F0h   ; USE SPECIAL ASCII CODE
670 03C3 E9 050D R  JMP     K5T       ; PUT IT IN THE BUFFER
671
672 03C6 04 50      K37C:  ADD     AL,80     ; CONVERT SCAN CODE (EDIT KEYS)
673 03C8 8A E0      MOV     AH,AL     ; (SCAN CODE NOT IN AH FOR INSERT)
674 03CA EB F0      JMP     K37A      ; PUT IT IN THE BUFFER
675
676
677
678 03CC
679
680 03CC F6 C3 04
681 03CF 75 03
682 03D1 E9 0454 R  K38:   TEST    BL,CTL_SHIFT ; NOT-ALT-SHIFT
683
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687
688 03D4 3C 46      TEST    BL,CTL_SHIFT ; BL STILL HAS SHIFT FLAGS
689 03D6 74 0E      JZ     K38A      ; ARE WE IN CONTROL SHIFT?
690 03D8 F6 C7 10   JZ     K38B      ; YES, START PROCESSING
691 03DA 74 05      JZ     K38B      ; NOT-CTL-SHIFT
692 03DD F6 C7 02   TEST    BH,LC_E0  ; YES, WAS LAST CODE AN E0?
693 03E0 74 14      JZ     K39       ; NO-BREAK, TEST FOR PAUSE
694
695 03E2 8B 1E 001A R K38B:  MOV     BX,#BUFFER_HEAD ; RESET BUFFER TO EMPTY
696 03E6 89 1E 001C R MOV     #BUFFER_TAIL,BX ; TURN ON BIOS_BREAK BIT
697 03EA C6 06 0071 R 80 MOV     #BIOS_BREAK,80H ; BREAK INTERRUPT VECTOR
698 03EF C0 1B      INT     1BH       ; BREAK INTERRUPT VECTOR
699 03F1 2B C0      SUB     AX,AX     ; PUT OUT DUMMY CHARACTER
700 03F3 E9 050D R  JMP     K5T       ; BUFFER_FILL
701
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704 03F6
705 03F6 F6 C7 10   K39:   TEST    BH,KBX    ; NO-BREAK
706 03F9 75 25      JNZ    K41       ; IS THIS THE ENHANCED KEYBOARD?
707 03FB 3C 45      CMP     AL,NUM_KEY ; YES, THEN THIS CAN'T BE PAUSE
708 03FD 75 21      JNE    K41       ; LOOK FOR PAUSE KEY
709 03FF 80 0E 0018 R 80 K39P:  OR     #KB_FLAG_1,HOLD_STATE ; NO-PAUSE
710 0404 B0 20      MOV     AL,E0I    ; TURN ON THE HOLD FLAG
711 0406 E6 20      OUT    020H,AL   ; END OF INTERRUPT TO CONTROL PORT
712
713
714
715 0408 80 3E 0049 R 08 ; ALLOW FURTHER KEYSTROKE INTS
716 040D 74 07      JE     K40       ; DURING PAUSE INTERVAL, TURN CRT BACK ON
717 040F BA 03D8      MOV     DX,03D8H  ; IS THIS BLACK AND WHITE CARD
718 0412 A0 0665 R 08 JE     K40       ; YES, NOTHING TO DO
719 0415 EE          MOV     AL,#CRT_MODE_SET ; PORT FOR COLOR CARD
720 0416 20          OUT    DX,AL     ; GET THE VALUE OF THE CURRENT MODE
721 0418 F6 06 0018 R 80 K40:   TEST    #KB_FLAG_1,HOLD_STATE ; SET THE CRT MODE, SO THAT CRT IS ON
722 041B 75 F9      JNZ    K40       ; PAUSE-LOOP
723 041D E9 02D4 R  JMP     K2T       ; LOOP UNTIL FLAG TURNED OFF
724
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728 0420
729 0420 3C 37      K41:   CMP     AL,55     ; NO-PAUSE
730 0422 75 10      JNE    K42       ; TEST FOR */PRTSK KEY
731 0424 F6 C7 10   TEST    BH,KBX    ; NOT-KEY-55
732 0427 74 05      JB     K41A      ; IS THIS THE ENHANCED KEYBOARD?
733 0429 F6 C7 02   TEST    BH,LC_E0  ; IS IT THE TAB KEY?
734 042C 74 20      JB     K42B      ; YES, XLTATE TO FUNCTION CODE
735 042E B8 7200    MOV     AX,114*256 ; IS IT THE KEY?
736 0431 E9 050D R  JMP     K5T       ; NO, TRANSLATE TO A FUNCTION
737
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739
740 0434
741 0434 3C 0F      K41A:  CMP     AL,15     ; START/STOP PRINTING SWITCH
742 0438 3C 35      JE     K42B      ; BUFFER_FILL
743 043A 75 0B      JNE    K42A      ; NO, TRANSLATE *
744 043C F6 C7 02   TEST    BH,LC_E0  ; YES, SPECIAL CODE FOR THIS ONE
745 043F 74 06      JZ     K42A      ; BUFFER_FILL
746 0441 BB 9500    MOV     AX,9500h  ;
747 0444 E9 050D R  JMP     K5T       ;
748
749 0447 BB 055F R  K42A:  MOV     BX,OFFSET K8 ; SET UP TO TRANSLATE CTL
750 044A 4A 59      MOV     AH,59     ; IS IT IN CHARACTER TABLE?
751 044C 72 57      JB     K45F      ; YES, GO TRANSLATE CHAR
752 044E BB 055F R  K42B:  MOV     BX,OFFSET K8 ; SET UP TO TRANSLATE CTL
753 0451 E9 04FC R  JMP     K64      ; NO, GO TRANSLATE_SCAN
754
755
756
757 0454 3C 37      K43:   CMP     AL,55     ; NOT IN CONTROL SHIFT
758 0456 75 1F      JNE    K44       ; PRINT SCREEN KEY?
759 0458 F6 C7 10   TEST    BH,KBX    ; NOT-PRINT-SCREEN
760 045B 74 07      JB     K44A      ; IS THIS ENHANCED KEYBOARD?
761 045D F6 C7 02   TEST    BH,LC_E0  ; IS IT THE TAB KEY?
762 0460 75 07      JZ     K44B      ; NO, TEST FOR SHIFT STATE
763 0462 EB 34      JMP     SHORT K45C ; YES, LAST CODE A MARKER?
764 0464 F6 C3 03   K44A:  TEST    BL,LEFT_SHIFT+RIGHT_SHIFT ; YES, IS PRINT SCREEN
765 0467 74 2F      JZ     K45C      ; NO, XLTATE TO ** CHARACTER
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SECTION 5


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768 0469 B0 20      K44B: MOV     AL,E01          ; END OF CURRENT INTERRUPT
769 046B E6 20      OUT     020H,AL        ; SO FURTHER THINGS CAN HAPPEN
770 046D CD 05      INT     $H             ; ISSUE PRINT SCREEN INTERRUPT
771 046F 80 26 0096 R FC AND     0KB_FLAG_3,NOT LC_E0-LC_E1; ZERO OUT THESE FLAGS
772 0474 E9 02D4 R  JMP     K27            ; GO BACK WITHOUT EOI OCCURRING
773
774
775
776 0477            ;----- HANDLE THE IN-CORE KEYS
777 0477 3C 3A      K45:  CMP     AL,58        ; NOT-PRINT-SCREEN
778 0479 77 2C      JA     K46            ; TEST FOR IN-CORE AREA
779                                ; JUMP IF NOT
780 047B 3C 35      CMP     AL,53        ; IS THIS THE "/" KEY?
781 047D 75 05      JNE    K45A          ; NO, JUMP
782 047F F6 C7 02   TEST   BH,LC_E0      ; WAS LAST CODE THE MARKER?
783 0482 75 14      JNZ    K45C          ; YES, TRANSLATE TO CHARACTER
784
785 0484 B9 001A     K45A: MOV     CX,26        ; LENGTH OF SEARCH
786 0487 BF 0317 R  MOV     DI,OFFSET K30+10 ; POINT TO TABLE OF A-Z CHARS
787 048A F2/ AE     REPNE  SCASB         ; IS THIS A LETTER KEY?
788 048C 75 05      JNE    K45B          ; NO, SYMBOL KEY
789
790 048E F6 C3 40     TEST   BL,CAPS_STATE ; ARE WE IN CAPS_LOCK?
791 0491 75 0A      JNZ    K45D          ; TEST FOR SURE
792 0493 F6 C3 03   K45B: TEST   BL,LEFT_SHIFT+RIGHT_SHIFT ; ARE WE IN SHIFT STATE?
793 0496 75 0A      JNZ    K45E          ; YES, UPPERCASE
794                                ; NO, LOWERCASE
795 0498 BB 05B7 R  K45C: MOV     BX,OFFSET K10 ; TRANSLATE TO LOWERCASE LETTERS
796 049B EB 50      JMP     SHORT K56     ;
797 049D            K45D: MOV     BX,LEFT_SHIFT+RIGHT_SHIFT ; ALMOST-CAPS-STATE
798 049D F6 C3 03   TEST   BL,LEFT_SHIFT+RIGHT_SHIFT ; CL ON. IS SHIFT ON, TOO?
799 04A0 75 F6      JNZ    K45C          ; SHIFTED TEMP OUT OF CAPS STATE
800 04A2 BB 050F R  K45E: MOV     BX,OFFSET K11 ; TRANSLATE TO UPPERCASE LETTERS
801 04A5 EB 46      JMP     SHORT K56     ;
802
803
804
805 04A7            ;----- TEST FOR KEYS F1 - F10
806 04A7 3C 44      K46:  CMP     AL,68        ; NOT IN-CORE AREA
807 04A9 77 02      JA     K47            ; TEST FOR F1 - F10
808 04AB EB 36      JMP     SHORT K53     ; JUMP IF NOT
809                                ; YES, GO DO FN KEY PROCESS
810
811
812
813 04AD            ;----- HANDLE THE NUMERIC PAD KEYS
814 04AD 3C 53      K47:  CMP     AL,83        ; NOT F1 - F10
815 04AF 77 2C      JA     K52            ; TEST FOR NUMPAD KEYS
816                                ; JUMP IF NOT
817
818 04B1 3C 4A      K48:  CMP     AL,74        ; KEYPAD KEYS, MUST TEST NUM LOCK FOR DETERMINATION
819 04B3 74 ED      JE     K45E          ; SPECIAL CASE FOR MINUS
820 04B5 3C 4E      CMP     AL,78        ; GO TRANSLATE
821 04B7 74 E9      JE     K45E          ; SPECIAL CASE FOR PLUS
822 04B9 F6 C7 02   TEST   BH,LC_E0      ; GO TRANSLATE
823 04BC 75 0A      JNZ    K49           ; IS THIS ONE OF THE NEW KEYS?
824                                ; YES, TRANSLATE TO BASE STATE
825 04BE F6 C3 20     TEST   BL,NUM_STATE  ; ARE WE IN NUM_LOCK?
826 04C1 75 13      JNZ    K50           ; TEST FOR SURE
827 04C3 F6 C3 03   TEST   BL,LEFT_SHIFT+RIGHT_SHIFT ; ARE WE IN SHIFT STATE?
828 04C6 75 13      JNZ    K51           ; IF SHIFTED, REALLY NUM STATE
829
830
831 04C8 3C 4C      ;----- BASE CASE FOR KEYPAD
832 04CA 75 05      K49:  CMP     AL,76        ; SPECIAL CASE FOR BASE STATE 5
833 04CC B0 F0      JNE    K49A         ; CONTINUE IF NOT KEYPAD 5
834 04CE EB 30      MOV     AL,0F0h      ; SPECIAL ASCII CODE
835 04D1 BB 05B7 R  JMP     K57           ; BUFFER FILL
836 04D4 EB 26      K49A: MOV     BX,OFFSET K10 ; BASE CASE TABLE
837                                ; CONVERT TO PSEUDO SCAN
838
839 04D6 F6 C3 03   ;----- MIGHT BE NUM LOCK, TEST SHIFT STATUS
840 04D9 75 ED      K50:  TEST   BL,LEFT_SHIFT+RIGHT_SHIFT ; ALMOST-NUM-STATE
841 04DB EB C5      JNZ    K49           ; SHIFTED TEMP OUT OF NUM STATE
842                                ; REALLY_NUM_STATE
843
844
845
846 04DD            ;----- TEST FOR THE NEW KEY ON WT KEYBOARDS
847 04DD 3C 56      K52:  CMP     AL,86        ; NOT A NUMPAD KEY
848 04DF 75 02      JNE    K53           ; IS IT THE NEW WT KEY?
849 04E1 EB B0      JMP     SHORT K45B   ; JUMP IF NOT
850                                ; HANDLE WITH REST OF LETTER KEYS
851
852
853
854 04E3 F6 C3 03   ;----- MUST BE F11 OR F12
855 04E6 74 E0      K53:  TEST   BL,LEFT_SHIFT+RIGHT_SHIFT ; F1 - F10 COME HERE, TOO
856                                ; TEST SHIFT STATE
857 04E8 BB 060F R  JZ     K49           ; JUMP, LOWERCASE PSEUDO SC'S
858 04EB EB 0F      MOV     BX,OFFSET K11 ; UPPER CASE PSEUDO SCAN CODES
859 04ED EB 0F      JMP     SHORT K64     ; TRANSLATE_SCAN
860
861
862 04ED            ;----- TRANSLATE THE CHARACTER
863 04ED FE C8      K56:  DEC     AL            ; TRANSLATE-CHAR
864 04EF 2E/ D7     XLAT   CS:K11        ; CONVERT ORIGIN
865 04F1 F6 06 0096 R 02 TEST   0KB_FLAG_3,LC_E0 ; CONVERT THE SCAN CODE TO ASCII
866 04F6 74 15      JZ     K57           ; IS THIS A NEW KEY?
867 04FB B4 E0      MOV     AH,MC_E0     ; NO, GO FILL BUFFER
868 04FA EB 11      JMP     SHORT K57     ; YES, PUT SPECIAL MARKER IN AH
869                                ; PUT IT INTO THE BUFFER
870
871
872 04FC            ;----- TRANSLATE SCAN FOR PSEUDO SCAN CODES
873 04FC FE C8      K64:  DEC     AL            ; TRANSLATE-SCAN-ORGD
874 04FE 2E/ D7     XLAT   CS:K8         ; CONVERT ORIGIN
875 0500 BA E0      MOV     AH,AL        ; CTL TABLE SCAN
876 0502 B0 00      MOV     AL,0          ; PUT VALUE INTO AH
877 0504 F6 06 0096 R 02 TEST   0KB_FLAG_3,LC_E0 ; IS THIS A NEW KEY?
878 0509 74 02      JZ     K57           ; NO, GO FILL BUFFER
879 050B B0 E0      MOV     AL,MC_E0     ; YES, PUT SPECIAL MARKER IN AL
880
881
882
883            ;----- PUT CHARACTER INTO BUFFER

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882
883 050D          K57:  CMP     AL,-1          ; BUFFER-FILL
884 050D 3C FF    ; IS THIS AN IGNORE CHAR
885 050F 74 05    ; YES, DO NOTHING WITH IT
886 0511 80 FC FF  CMP     AH,-1          ; LOOK FOR -1 PSEUDO SCAN
887 0514 75 03    ; NEAR_INTERRUPT_RETURN
888
889 0516          K59:  JMP     K26          ; NEAR_INTERRUPT_RETURN
890 0516 E9 02CA R ; INTERRUPT_RETURN
891
892 0519          K61:  CLI                     ; TURN OFF INTERRUPTS
893 0519 FA        MOV     BX,@BUFFER_TAIL ; GET THE END POINTER TO THE BUFFER
894 051A 8B 1E 001C R ; SAVE THE VALUE
895 051E 8B F3     MOV     SI,BX
896 0520 EB 0114 R ; ADVANCE THE TAIL
897 0523 3B 1E 001A R ; HAS THE BUFFER WRAPPED AROUND
898 0527 74 17    ; BUFFER_FULL_BEEP
899 0529 89 04    ; STORE THE VALUE
900 052B 89 1E 001C R ; MOVE THE POINTER UP
901 052F B0 20    ; END OF INTERRUPT COMMAND
902 0531 E6 20    ; SEND COMMAND TO INTERRUPT CONTROL PORT
903 0533 B8 9102  MOV     AX,09102H ; MOVE IN POST CODE & TYPE
904 0536 CD 15    INT     15H       ; PERFORM OTHER FUNCTION
905 0538 80 26 0096 R FC AND     #KB_FLAG_3,NOT LC_E0+LC_E1 ; RESET LAST CHAR H.C. FLAG
906 053D E9 02D4 R ; INTERRUPT_RETURN
907
908 ;----- BUFFER IS FULL SOUND THE BEEPER
909
910 0540          K62:  MOV     AL,E01          ; ENABLE INTERRUPT CONTROLLER CHIP
911 0540 B0 20    OUT     INTA00,AL ; DIVISOR FOR 1760 HZ
912 0542 E6 20    MOV     CX,678     ; DIVISOR FOR 1760 HZ
913 0544 B9 02A6  MOV     BL,4      ; SHORT BEEP COUNT (1/16 + 1/64 DELAY)
914 0547 B3 04    CALL    BEEP      ; GO TO COMMON BEEP HANDLER
915 0549 E8 0000 E JMP     K27       ; EXIT
916 054C E9 02D4 R
917
918 054F          KB_INT_1  ENDP

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919          PAGE
920          |
921          |----- KEY IDENTIFICATION SCAN TABLES -----|
922          |
923          |----- TABLE OF SHIFT KEYS AND MASK VALUES -----|
924          |----- KEY TABLE -----|
925          |
926          | K6 LABEL BYTE
927          | 054F 52 DB 30,-1,-1,-1,1,31 ; INSERT KEY
928          | 0550 34 45 46 38 1D DB CAPS_KEY,NUM_KEY,SCROLL_KEY,ALT_KEY,CTL_KEY
929          | 0555 2A 36 DB LEFT_KEY,RIGHT_KEY
930          | = 0008 K6L EQU $-K6
931          |
932          |----- MASK TABLE -----|
933          | K7 LABEL BYTE
934          | 0557 80 DB INS_SHIFT ; INSERT MODE SHIFT
935          | 0558 40 20 10 08 04 DB CAPS_SHIFT,NUM_SHIFT,SCROLL_SHIFT,ALT_SHIFT,CTL_SHIFT
936          | 055D 02 01 DB LEFT_SHIFT,RIGHT_SHIFT
937          |
938          |----- TABLES FOR CTRL CASE -----|
939          |
940          | K8 LABEL BYTE ;----- CHARACTERS -----|
941          | 055F 1B FF 0D FF FF FF FF DB 27,-1,00,-1,-1,-1 ; Esc, 1, 2, 3, 4, 5
942          | 0565 1E FF FF FF FF 1F DB 30,-1,-1,-1,1,31 ; 6, 7, 8, 9, 0, -
943          | 056B FF 7F 94 11 17 05 DB -1,127,148,17,23,5 ; = Bksp, Tab, Q, W, E
944          | 0571 12 14 19 15 09 0F DB 18,20,25,21,09,15 ; R, T, Y, U, I, O
945          | 0577 10 1B 1D 0A FF 01 DB 16,27,29,10,-1,01 ; P, [, ], Enter, Ctrl, A
946          | 057D 13 04 06 07 08 0A DB 19,04,06,07,08,10 ; S, D, F, G, H, J
947          | 0583 08 0C FF FF FF FF DB 11,12,-1,-1,-1,-1 ; K, L, ;, ', LShift
948          | 0589 1C 1A 18 03 16 02 DB 28,26,24,03,22,02 ; Z, X, C, V, B
949          | 058F 0E 0D FF FF FF FF DB 14,13,-1,-1,-1,-1 ; N, M, , ., /, RShift
950          | 0595 96 FF 20 FF DB 150,-1,'*,-1 ; *, Alt, Space, CL
951          | ;----- FUNCTIONS -----|
952          | 0599 5E 5F 60 61 62 63 DB 94,95,96,97,98,99 ; F1 - F6
953          | 059F 64 65 66 67 FF FF DB 100,101,102,103,-1,-1 ; F7 - F10, NL, SL
954          | 05A5 77 8D 84 8E 73 8F DB 119,141,132,142,115,143 ; Home, Up, PgUp, -, Left, Pad5
955          | 05AB 74 90 75 91 76 92 DB 116,144,117,145,118,146 ; Right, +, End, Down, PgDn, Ina
956          | 05B1 93 FF FF FF 89 8A DB 147,-1,-1,-1,137,138 ; Del, SysReq, Undef, WT, F11, F12
957          |----- TABLES FOR LOWER CASE -----|
958          |
959          | K10 LABEL BYTE
960          | 05B7 1B 31 32 33 34 35 DB 27,12345*
961          | 05BD 36 37 38 39 2D DB '67890*
962          | 05C3 3D 08 09 71 77 65 DB '*,08,09,'qwe'
963          | 05C9 72 74 79 75 69 6F DB 'rtyuiol'
964          | 05CF 70 5B 5D 0D FF 61 DB 'p[ ]',ODH,-1,'A' ; LETTERS, Return, Ctrl
965          | 05D5 73 64 66 67 68 6A DB 'adfgj'
966          | 05DB 6B 6C 3B 27 60 FF DB 'kl';'-1 ; LETTERS, L Shift
967          | 05E1 5C 7A 7B 63 76 62 DB '\zxcvb'
968          | 05E7 6E 6D 2C 2E 2F DB 'nm;>'
969          | 05EC FF 2A FF 20 FF DB -1,'*,-1,'*,-1 ; R Shift,* , Alt, Space, CL
970          |
971          |----- LC TABLE SCAN -----|
972          | 05F1 3B 3C 3D 3E 3F DB 59,60,61,62,63 ; BASE STATE OF F1 - F10
973          | 05F6 40 41 42 43 44 DB 64,65,66,67,68
974          | 05FB FF FF DB -1,-1 ; NL, SL
975          |
976          |----- KEYPAD TABLE -----|
977          | K15 LABEL BYTE
978          | 05FD 47 48 49 FF 4B FF DB 71,72,73,-1,75,-1 ; BASE STATE OF KEYPAD KEYS
979          | 0603 4D FF 4F 50 51 52 DB 77,-1,79,80,81,82
980          | 0609 53 DB 83
981          | 060A FF FF 5C 85 86 DB -1,-1,'\',133,134 ; SysRq, Undef, WT, F11, F12
982          |
983          |----- TABLES FOR UPPER CASE -----|
984          |
985          | K11 LABEL BYTE
986          | 060F 1B 21 40 23 24 25 DB 27,'!@#$%'
987          | 0615 5E 26 2A 28 29 5F DB 'X'@'[ ]'
988          | 061B 2B 08 00 51 57 45 DB '*,08,00,'QWE'
989          | 0621 52 54 59 55 49 4F DB 'RTYUIO'
990          | 0627 50 7B 7D 0D FF 41 DB 'P[ ]',ODH,-1,'A' ; LETTERS, Return, Ctrl
991          | 062D 53 44 46 47 48 4A DB 'SDFGJ'
992          | 0633 4B 4C 3A 22 7E FF DB 'KL';'-1 ; LETTERS, L Shift
993          | 0639 7C 5A 58 43 56 42 DB '\ZXCVB'
994          | 063F 4E 4D 3C 3E 3F DB '\NM;>'
995          | 0644 FF 2A FF 20 FF DB -1,'*,-1,'*,-1 ; R Shift,* , Alt, Space, CL
996          |
997          |----- UC TABLE SCAN -----|
998          | K12 LABEL BYTE
999          | 0649 54 55 56 57 58 DB 84,85,86,87,88 ; SHIFTED STATE OF F1 - F10
1000          | 064E 59 5A 5B 5C 5D DB 89,90,91,92,93
1001          | 0653 FF FF DB -1,-1 ; NL, SL
1002          |
1003          |----- NUM STATE TABLE -----|
1004          | K14 LABEL BYTE
1005          | 0655 37 38 39 2D 34 35 DB '789-456+1230.' ; NUMLOCK STATE OF KEYPAD KEYS
1006          | 066 3E 2B 31 32 33 30
1007          | 2E
1008          | 0662 FF FF 7C 87 88 DB -1,-1,'|',135,136 ; SysRq, Undef, WT, F11, F12
1009          | 0667
1010          | 010
          |
          | CODE ENDS
          | END
  
```

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1 PAGE 118,121
2 TITLE PRT ----- 01/10/86 PRINTER ADAPTER BIOS
3 .LIST
4
5 0000 CODE SEGMENT BYTE PUBLIC
6 PUBLIC PRINTER_IO_1
7
8 EXTRN DDS:NEAR
9
10
11 ;--- INT 17 H -----
12 ; PRINTER_IO
13 ; THIS ROUTINE PROVIDES COMMUNICATION WITH THE PRINTER
14 ; INPUT
15 ; (AH) = 00H PRINT THE CHARACTER IN (AL)
16 ; ON RETURN, (AH) = 1 IF CHARACTER NOT PRINTED (TIME OUT)
17 ; OTHER BITS SET AS ON NORMAL STATUS CALL
18 ; (AH) = 01H INITIALIZE THE PRINTER PORT
19 ; RETURNS WITH (AH) SET WITH PRINTER STATUS
20 ; (AH) = 02H READ THE PRINTER STATUS INTO (AH)
21 ;
22 ; 7 6 5 4 3 2-1 0
23 ; | | | | | | |
24 ; | | | | | | |
25 ; | | | | | | |
26 ; | | | | | | |
27 ; | | | | | | |
28 ; | | | | | | |
29 ; | | | | | | |
30 ; | | | | | | |
31 ; | | | | | | |
32 ; | | | | | | |
33 ; | | | | | | |
34 ; | | | | | | |
35 ; | | | | | | |
36 ; (DX) = PRINTER TO BE USED (0,1,2) CORRESPONDING TO ACTUAL VALUES
37 ; IN #PRINTER_BASE AREA
38 ; DATA AREA #PRINTER_BASE CONTAINS THE BASE ADDRESS OF THE PRINTER CARD(S)
39 ; AVAILABLE (LOCATED AT BEGINNING OF DATA SEGMENT, 40BH ABSOLUTE, 3 WORDS)
40 ; DATA AREA #PRINT_TIM_OUT (BYTE) MAY BE CHANGE TO CAUSE DIFFERENT
41 ; TIME OUT WAITS. DEFAULT=20
42 ;
43 ; REGISTERS (AH) IS MODIFIED WITH STATUS INFORMATION
44 ; ALL OTHERS UNCHANGED
45 ;-----
46
47 ASSUME CS:CODE,DS:DATA
48
49
50 0000 PRINTER_IO_1 PROC FAR ; ENTRY POINT FOR ORG 0EFD2H
51
52 0000 FB STI DX ; INTERRUPTS BACK ON
53 0001 E2 PUSH DX ; SAVE WORK REGISTERS
54 0002 E3 PUSH BX
55 0003 83 FA 03 CMP DX,03H ; CHECK FOR PRINTER NUMBER VALID 0-3
56 0006 77 25 JA B10 ; ERROR EXIT IF OUT OF RANGE
57
58 0008 8A F8 MOV BH,AL ; SAVE CHARACTER TO BE PRINTED
59 000A 1E PUSH DS ; SAVE SEGMENT
60 000B E8 0000 E CALL DDS ; ADDRESS DATA SEGMENT
61
62 000E 56 PUSH SI ; SAVE WORK POINTER REGISTER
63 000F BB F2 MOV SI,DX ; GET PRINTER PARAMETER
64 0011 8A 9C 007B R MOV BL,#PRINT_TIM_OUT[SI] ; LOAD TIMEOUT VALUE
65 0015 D1 E6 SHL SI,1 ; WORD OFFSET INTO TABLE INTO (SI)
66 0017 8B 94 000B R MOV DX,#PRINTER_BASE[SI] ; GET BASE ADDRESS FOR PRINTER CARD
67 001B 5E POP SI ; RECOVER CALLERS (SI) REGISTER
68 001C 1F POP DS ; AND (DS) SEGMENT REGISTER
69
70 001D 0B D2 OR DX,DX ; TEST DX = ZERO, INDICATING NO PRINTER
71 001F 74 0C JZ B10 ; EXIT, NO PRINTER ADAPTER AT OFFSET
72
73 0021 0A E4 OR AH,AH ; TEST FOR (AH) = 00H
74 0023 74 0D JZ B30 ; PRINT CHARACTER IN (AL)
75
76 0025 FE CC DEC AH ; TEST FOR (AH) = 01H
77 0027 74 4B JZ B80 ; INITIALIZE PRINTER
78
79 0029 FE CC DEC AH ; TEST FOR (AH) = 02H
80 002B 74 39 JZ B60 ; GET PRINTER STATUS
81
82 002D B10: MOV AH,029H ; RETURN ERROR BITS FOR INVALID CALLS
83 002D B4 29
84
85 002F B20: POP BX ; RETURN
86 002F 5B POP DX ; RECOVER REGISTERS
87 0030 5A IRET ; RETURN TO CALLING PROGRAM (AH) = STATUS
88 0031 CF
89
90
91 ;----- PRINT THE CHARACTER IN (AL)
92
93 0032 B30: OUT DX,AL ; OUTPUT CHARACTER TO DATA PORT
94 0032 EE INC DX ; POINT TO STATUS PORT
95 0033 42

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SECTION 5

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96                                     PAGE
97                                     ;----- CHECK FOR PRINTER BUSY
98
99 0034 EC                               IN    AL,DX
100 0035 EC                               IN    AL,DX
101 0036 A8 80                           TEST   AL,80H
102 0038 75 05                            JNZ   B40
103
104                                     ;----- INT 15 H -- DEVICE BUSY
105
106 003A B8 90FE                          MOV    AX,90FEH
107 003D CD 15                            INT    15H
108
109                                     ;----- WAIT BUSY
110
111 003F                                     B40:
112 003F 51                               PUSH   CX
113 0040 2B C9                            SUB    CX,CX
114 0042                                     B45:
115 0042 EC                               IN    AL,DX
116 0043 8A E0                            MOV    AH,AL
117 0045 A8 80                            TEST   AL,80H
118 0047 75 0F                            JNZ   B50
119
120 0049 E2 F7                            LOOP  B45
121
122 004B FE CB                            DEC    BL
123 004D 75 F3                            JNZ   B45
124
125 004F 59                               POP    CX
126 0050 80 CC 01                         OR     AH,1
127 0053 80 E4 F9                         AND    AH,0F9H
128 0056 EB 15                            JMP    SHORT B70
129
130 0058                                     B50:
131 0058 59                               POP    CX
132 0059 B0 0D                            MOV    AL,0DH
133 005B 42                               INC    DX
134 005C FA                               CLI
135 005D EE                               OUT    DX,AL
136 005E EB 00                            JMP
137
138 0060 B0 0C                            MOV    AL,0CH
139 0062 EE                               OUT    DX,AL
140 0063 FB                               STI
141 0064 4A                               DEC    DX
142 0065 4A                               DEC    DX
143
144                                     ;----- PRINTER STATUS
145
146                                     B60:
147 0066
148 0066 42                               INC    DX
149 0067 EC                               IN    AL,DX
150 0068 EC                               IN    AL,DX
151 0069 24 F8                            AND    AL,0F8H
152 006B 8A E0                            MOV    AH,AL
153 006D                                     B70:
154 006D 8A C7                            MOV    AL,8H
155 006F 80 F4 48                         XOR    AH,48H
156 0072 EB BB                            JMP
157
158                                     ;----- INITIALIZE THE PRINTER PORT
159
160
161 0074                                     B80:
162 0074 42                               INC    DX
163 0075 42                               INC    DX
164 0076 B0 08                            MOV    AL,8
165 0078 EE                               OUT    DX,AL
166 0079 B8 03E8                          MOV    AX,1000
167 007C                                     B90:
168 007C 48                               DEC    AX
169 007D 75 FD                            JNZ   B90
170
171 007F B0 0C                            MOV    AL,0CH
172 0081 EE                               OUT    DX,AL
173 0082 4A                               DEC    DX
174 0083 4A                               DEC    DX
175 0084 EB E0                            JMP    B60
176
177 0086                                     PRINTER_IO_1 ENDP
178
179 0086                                     CODE ENDS
180                                     END

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1      PAGE 118,121
2      TITLE RS232 ---- 01/10/86 COMMUNICATIONS BIOS (RS232)
3      .LIST
4      .CODE SEGMENT BYTE PUBLIC
5      0000
6
7      PUBLIC RS232_IO_1
8      EXTRN A1:NEAR
9      EXTRN D0S:NEAR
10
11     ;----- INT 14 H -----
12     ;RS232_IO_1
13     ; THIS ROUTINE PROVIDES BYTE STREAM I/O TO THE COMMUNICATIONS
14     ; PORT ACCORDING TO THE PARAMETERS:
15     ;
16     ; (AH)= 00H INITIALIZE THE COMMUNICATIONS PORT
17     ; (AL) HAS PARAMETERS FOR INITIALIZATION
18     ;
19     ;
20     ;          7          6          5          4          3          2          1          0
21     ;          ---- BAUD RATE -- -- PARITY-- -- STOPBIT --WORD LENGTH--
22     ;          000 - 110          X0 - NONE          0 - 1          10 - 7 BITS
23     ;          001 - 150          01 - ODD           1 - 2          11 - 8 BITS
24     ;          010 - 300          11 - EVEN
25     ;          011 - 600
26     ;          100 - 1200
27     ;          101 - 2400
28     ;          110 - 4800
29     ;          111 - 9600
30     ; ON RETURN, CONDITIONS SET AS IN CALL TO COMMO STATUS (AH=03H)
31     ;
32     ; (AH)= 01H SEND THE CHARACTER IN (AL) OVER THE COMMO LINE
33     ; (AL) REGISTER IS PRESERVED
34     ; ON EXIT, BIT 7 OF AH IS SET IF THE ROUTINE WAS UNABLE TO
35     ; TO TRANSMIT THE BYTE OF DATA OVER THE LINE.
36     ; IF BIT 7 OF AH IS NOT SET, THE
37     ; REMAINDER OF (AH) IS SET AS IN A STATUS REQUEST,
38     ; REFLECTING THE CURRENT STATUS OF THE LINE.
39     ; (AH)= 02H RECEIVE A CHARACTER IN (AL) FROM COMMO LINE BEFORE
40     ; RETURNING TO CALLER
41     ; ON EXIT, (AH) HAS THE CURRENT LINE STATUS, AS SET BY THE
42     ; STATUS ROUTINE, EXCEPT THAT THE ONLY BITS
43     ; LEFT ON ARE THE ERROR BITS (7,4,3,2,1)
44     ; IF (AH) HAS BIT 7 ON (TIME OUT) THE REMAINING
45     ; BITS ARE NOT PREDICTABLE.
46     ; THUS, (AH) IS NON ZERO ONLY WHEN AN ERROR OCCURRED.
47     ; (AH)= 03H RETURN THE COMMO PORT STATUS IN (AX)
48     ; (AH) CONTAINS THE LINE CONTROL STATUS
49     ; BIT 7 = TIME OUT
50     ; BIT 6 = TRANSMIT SHIFT REGISTER EMPTY
51     ; BIT 5 = TRANSMIT HOLDING REGISTER EMPTY
52     ; BIT 4 = BREAK DETECT
53     ; BIT 3 = FRAMING ERROR
54     ; BIT 2 = PARITY ERROR
55     ; BIT 1 = OVERRUN ERROR
56     ; BIT 0 = DATA READY
57     ; (AL) CONTAINS THE MODEM STATUS
58     ; BIT 7 = RECEIVE LINE SIGNAL DETECT
59     ; BIT 6 = RING INDICATOR
60     ; BIT 5 = DATA SET READY
61     ; BIT 4 = CLEAR TO SEND
62     ; BIT 3 = DELTA RECEIVE LINE SIGNAL DETECT
63     ; BIT 2 = TRAILING EDGE RING DETECTOR
64     ; BIT 1 = DELTA DATA SET READY
65     ; BIT 0 = DELTA CLEAR TO SEND
66     ;
67     ; (DX) = PARAMETER INDICATING WHICH RS232 CARD (0,1 ALLOWED)
68     ;
69     ; DATA AREA @RS232_BASE CONTAINS THE BASE ADDRESS OF THE 8250 ON THE CARD
70     ; LOCATION 400H CONTAINS UP TO 4 RS232 ADDRESSES POSSIBLE
71     ; DATA AREA LABEL @RS232_TIM_OUT (BYTE) CONTAINS OUTER LOOP COUNT
72     ; VALUE FOR TIMEOUT (DEFAULT=1)
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SECTION 5

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115 PAGE
116 I----- INITIALIZE THE COMMUNICATIONS PORT
117
118 0039 A4: MOV AH,AL ; SAVE INITIALIZATION PARAMETERS IN (AH)
119 0039 BA E0 ADD DX,3 ; POINT TO 8250 CONTROL REGISTER
120 003B 83 C2 03 MOV AL,80H ; SET DLAB=1
121 003E B0 80 OUT DX,AL
122 0040 EE
123
124 I----- DETERMINE BAUD RATE DIVISOR
125
126 0041 BA D4 MOV DL,AH ; GET PARAMETERS TO (DL)
127 0043 B1 04 MOV CL,4
128 0045 D2 C2 ROL DL,CL
129 0047 81 E2 000E AND DX,0EH ; ISOLATE THEM
130 004B BF 0000 E MOV DI,OFFSET A1 ; BASE OF TABLE
131 004E 03 FA DI,DX ; PUT INTO INDEX REGISTER
132 0050 BB 94 0000 R MOV DX,®RS232_BASE[S1] ; POINT TO HIGH ORDER OF DIVISOR
133 0054 42 INC DX
134 0055 2E: BA 45 01 MOV AL,CS:[DI]+1 ; GET HIGH ORDER OF DIVISOR
135 0059 EE OUT DX,AL ; SET ms OF DIVISOR TO 0
136 005A 4A DEC DX
137 005B 90 NOP ; I/O DELAY
138 005C 2E: BA 05 MOV AL,CS:[DI] ; GET LOW ORDER OF DIVISOR
139 005F EE OUT DX,AL ; SET LOW OF DIVISOR
140 0060 83 C2 03 ADD DX,3
141 0063 8A C4 MOV AL,AH ; GET PARAMETERS BACK
142 0065 24 1F AND AL,01FH ; STRIP OFF THE BAUD BITS
143 0067 EE OUT DX,AL ; LINE CONTROL TO 8 BITS
144 0068 4A DEC DX
145 0069 4A DEC DX
146 006A 90 NOP ; I/O DELAY
147 006B B0 00 MOV AL,0
148 006D EE OUT DX,AL ; INTERRUPT ENABLES ALL OFF
149 006E EB 4B JMP SHORT A18 ; COM STATUS
150
151 I----- SEND CHARACTER IN (AL) OVER COMMO LINE
152
153 0070 A5: PUSH AX ; SAVE CHAR TO SEND
154 0070 50 ADD DX,4 ; MODEM CONTROL REGISTER
155 0071 83 C2 04 MOV AL,3 ; DTR AND RTS
156 0074 B0 03 OUT DX,AL ; DATA TERMINAL READY, REQUEST TO SEND
157 0076 EE INC DX ; MODEM STATUS REGISTER
158 0077 42 INC DX
159 0078 42 INC DX
160 0079 B7 30 MOV BH,30H ; DATA SET READY & CLEAR TO SEND
161 007B E8 00CA R CALL WAIT_FOR_STATUS ; ARE BOTH TRUE
162 007E 74 08 JE A9 ; YES, READY TO TRANSMIT CHAR
163 0080
164 0080 59 POP CX
165 0081 8A C1 MOV AL,CL ; RELOAD DATA BYTE
166 0083
167 0083 80 CC 80 A8: OR AH,80H ; INDICATE TIME OUT
168 0086 EB AA JMP A3 ; RETURN
169
170 0088 A9: DEC DX ; CLEAR TO SEND
171 0088 4A DEC DX ; LINE STATUS REGISTER
172 0089 59 WAIT SEND ; WAIT SEND
173 0089 B7 20 MOV BH,20H ; IS TRANSMITTER READY
174 008B E8 00CA R CALL WAIT_FOR_STATUS ; TEST FOR TRANSMITTER READY
175 008E 75 F0 JNZ A7 ; RETURN WITH TIME OUT SET
176 0090
177 0090 83 EA 05 A11: SUB DX,5 ; OUT_CHAR
178 0093 59 POP CX ; DATA PORT
179 0094 8A C1 MOV AL,CL ; RECOVER IN CX TEMPORARILY
180 0096 EE OUT DX,AL ; MOVE CHAR TO AL FOR OUT, STATUS IN AH
181 0097 EB 99 JMP A3 ; RETURN
182
183 I----- RECEIVE CHARACTER FROM COMMO LINE
184
185 0099 A12: ADD DX,4 ; MODEM CONTROL REGISTER
186 0099 83 C2 04 MOV AL,1 ; DATA TERMINAL READY
187 009C B0 01 OUT DX,AL ; MODEM STATUS REGISTER
188 009E EE INC DX
189 009F 42 INC DX
190 00A0 42 INC DX
191 00A1
192 00A1 B7 20 A13: MOV BH,20H ; WAIT_DSR
193 00A3 E8 00CA R CALL WAIT_FOR_STATUS ; DATA SET READY
194 00A6 75 DB JNZ A8 ; TEST FOR DSR
195 00A8 A15: DEC DX ; RETURN WITH ERROR
196 00A8 4A DEC DX ; LINE STATUS REGISTER
197 00A9 A16: MOV BH,1 ; WAIT_RECV
198 00A9 B7 01 CALL WAIT_FOR_STATUS ; RECEIVE_BUFFER_FULL
199 00AB E8 00CA R CALL WAIT_FOR_STATUS ; TEST FOR RECEIVE_BUFFER_FULL
200 00AE 75 D3 JNZ A8 ; SET TIME OUT ERROR
201 00B0 A17: AND AH,0001110B ; GET_CHAR
202 00B0 80 E4 1E AND AL,DX ; TEST FOR ERROR CONDITIONS ON RECEIVE
203 00B3 BB 94 0000 R MOV DX,®RS232_BASE[S1] ; DATA PORT
204 00B7 EC IN AL,DX ; GET CHARACTER FROM LINE
205 00B8 E9 0032 R JMP A3 ; RETURN
206
207 I----- COMMO PORT STATUS ROUTINE
208
209 A18: MOV DX,®RS232_BASE[S1]
210 00BB ADD DX,5 ; CONTROL PORT
211 00BB BB 94 0000 R IN AL,DX ; GET LINE CONTROL STATUS
212 00BF 83 C2 05 IN AH,AL ; PUT IN (AH) FOR RETURN
213 00C2 EC MOV AL,AH ; POINT TO MODEM STATUS REGISTER
214 00C3 8A E0 INC DX ; GET MODEM CONTROL STATUS
215 00C5 42 INC DX
216 00C6 EC IN AL,DX
217 00C7 E9 0032 R JMP A3 ; RETURN

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218                                     PAGE
219                                     ;-----;
220                                     ; WAIT FOR STATUS ROUTINE ;
221 ;ENTRY: (BH) = STATUS BIT(S) TO LOOK FOR ;
222 ;       (DX) = ADDRESS OF STATUS REG ;
223 ;EXIT:  ZERO FLAG ON = STATUS FOUND ;
224 ;       ZERO FLAG OFF = TIMEOUT. ;
225 ;       (AH) = LAST STATUS READ ;
226                                     ;-----;
227                                     WAIT_FOR_STATUS PROC NEAR
228 00CA                                     MOV     BL,0RS232_TIM_OUT[D1] ; LOAD OUTER LOOP COUNT
229
230 00CA 8A 9D 007C R                       MOV     BL,0RS232_TIM_OUT[D1] ; LOAD OUTER LOOP COUNT
231 00CE                                     WFS0:  SUB     CX,CX
232 00CE 2B C9                               SUB     CX,CX
233 00D0                                     WFS1:
234 00D0 EC                                 IN      AL,DX ; GET STATUS
235 00D1 8A E0                               MOV     AH,AL ; MOVE TO (AH)
236 00D3 22 C7                               AND     AL,BH ; ISOLATE BITS TO TEST
237 00D5 3A C7                               CMP     AL,BH ; EXACTLY = TO MASK
238 00D7 74 08                               JE      WFS_END ; RETURN WITH ZERO FLAG ON
239
240 00D9 E2 F5                               LOOP   WFS1 ; TRY AGAIN
241
242 00DB FE CB                               DEC     BL ; DECREMENT LOOP COUNTER
243 00DD 75 EF                               JNZ    WFS0
244
245 00DF 0A FF                               OR      BH,BH ; SET ZERO FLAG OFF
246 00E1                                     WFS_END:
247 00E1 C3                                 RET
248
249 00E2                                     WAIT_FOR_STATUS ENDP
250
251 00E2                                     RS232_IO_1 ENDP
252
253 00E2                                     CODE ENDS
254 254                                     END

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SECTION 5


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1 PAGE 118,121
2 TITLE VIDEO ---- 01/10/86 VIDEO DISPLAY BIOS
3 .LIST
4 CODE SEGMENT BYTE PUBLIC
5
6 PUBLIC ACT_DISP_PAGE
7 PUBLIC READ_AC_CURRENT
8 PUBLIC READ_CURSOR
9 PUBLIC READ_DOT
10 PUBLIC READ_LPEN
11 PUBLIC SCROLL_DOWN
12 PUBLIC SCROLL_UP
13 PUBLIC SET_COLOR
14 PUBLIC SET_CPOS
15 PUBLIC SET_CTYPE
16 PUBLIC SET_MODE
17 PUBLIC WRITE_AC_CURRENT
18 PUBLIC WRITE_C_CURRENT
19 PUBLIC WRITE_DOT
20 PUBLIC WRITE_TTY
21 PUBLIC VIDEO_IO_1
22 PUBLIC VIDEO_STATE
23
24 PUBLIC SET_MODE
25 PUBLIC SET_CTYPE
26 PUBLIC SET_CPOS
27 PUBLIC READ_CURSOR
28 PUBLIC READ_LPEN
29 PUBLIC ACT_DISP_PAGE
30 PUBLIC SCROLL_UP
31 PUBLIC SCROLL_DOWN
32 PUBLIC READ_AC_CURRENT
33 PUBLIC WRITE_AC_CURRENT
34 PUBLIC WRITE_C_CURRENT
35 PUBLIC SET_COLOR
36 PUBLIC WRITE_DOT
37 PUBLIC READ_DOT
38 PUBLIC WRITE_TTY
39 PUBLIC VIDEO_STATE
40 PUBLIC VIDEO_RETURN
41 PUBLIC VIDEO_RETURN
42 PUBLIC VIDEO_RETURN
43 PUBLIC WRITE_STRING
44
45 EXTRN BEEP:NEAR ; SPEAKER BEEP ROUTINE
46 EXTRN CRT_CHAR_GEN:NEAR ; CHARACTER GENERATOR GRAPHICS TABLE
47 EXTRN DDS:NEAR ; LOAD (DS) WITH DATA SEGMENT SELECTOR
48 EXTRN MS:WORD ; REGEN BUFFER LENGTH TABLE
49 EXTRN M6:BYTE ; COLUMNS PER MODE TABLE
50 EXTRN M7:BYTE ; MODE SET VALUE PER MODE TABLE
51
52
53
54 INT 10 H
55
56 VIDEO_IO
57
58 ; THESE ROUTINES PROVIDE THE CRT DISPLAY INTERFACE
59 ; THE FOLLOWING FUNCTIONS ARE PROVIDED:
60
61 (AH) = 00H SET MODE (AL CONTAINS MODE VALUE
62 (AL) = 00H 40X25 BW MODE (POWER ON DEFAULT)
63 (AL) = 01H 40X25 COLOR
64 (AL) = 02H 80X25 BW
65 (AL) = 03H 80X25 COLOR
66 GRAPHICS MODES
67 (AL) = 04H 320X200 COLOR
68 (AL) = 05H 320X200 BW MODE
69 (AL) = 06H 640X200 BW MODE
70 (AL) = 07H 80X25 MONOCHROME (USED INTERNAL TO VIDEO ONLY)
71 *** NOTES -BW MODES OPERATE SAME AS COLOR MODES, BUT COLOR
72 BURST IS NOT ENABLED
73 -CURSOR IS NOT DISPLAYED IN GRAPHICS MODE
74
75 (AH) = 01H SET CURSOR TYPE
76 (CH) = BITS 4-0 = START LINE FOR CURSOR
77 ** HARDWARE WILL ALWAYS CAUSE BLINK
78 ** SETTING BIT 5 OR 6 WILL CAUSE ERRATIC BLINKING
79 OR NO CURSOR AT ALL
80 (CL) = BITS 4-0 = END LINE FOR CURSOR
81
82 (AH) = 02H SET CURSOR POSITION
83 (DH,DL) = ROW,COLUMN (00H,00H) IS UPPER LEFT
84 (BH) = PAGE NUMBER (MUST BE 00H FOR GRAPHICS MODES)
85
86 (AH) = 03H READ CURSOR POSITION
87 (BH) = PAGE NUMBER (MUST BE 00H FOR GRAPHICS MODES)
88 ON EXIT (CH,CL) = CURSOR MODE CURRENTLY SET
89
90 (AH) = 04H READ LIGHT PEN POSITION
91 ON EXIT:
92 (AH) = 00H -- LIGHT PEN SWITCH NOT DOWN/NOT TRIGGERED
93 (AH) = 01H -- VALID LIGHT PEN VALUE IN REGISTERS
94 (DH,DL) = ROW,COLUMN OF CHARACTER LP POSITION
95 (CH) = RASTER LINE (0-199)
96 (BX) = PIXEL COLUMN (0-319,639)
97
98 (AH) = 05H SELECT ACTIVE DISPLAY PAGE (VALID ONLY FOR ALPHA MODES)
99 (AL) = NEW PAGE VALUE (0-7 FOR MODES 041, 0-3 FOR MODES 2&3)
100
101 (AH) = 06H SCROLL ACTIVE PAGE UP
102 (AL) = NUMBER OF LINES, (L LINES BLANKED AT BOTTOM OF WINDOW )
103 (AL) = 00H MEANS BLANK ENTIRE WINDOW
104 (CH,CL) = ROW,COLUMN OF UPPER LEFT CORNER OF SCROLL
105 (DH,DL) = ROW,COLUMN OF LOWER RIGHT CORNER OF SCROLL
106 (BH) = ATTRIBUTE TO BE USED ON BLANK LINE
107
108 (AH) = 07H SCROLL ACTIVE PAGE DOWN
109 (AL) = NUMBER OF LINES, INPUT LINES BLANKED AT TOP OF WINDOW
110 (AL) = 00H MEANS BLANK ENTIRE WINDOW
111 (CH,CL) = ROW,COLUMN OF UPPER LEFT CORNER OF SCROLL
112 (DH,DL) = ROW,COLUMN OF LOWER RIGHT CORNER OF SCROLL
113 (BH) = ATTRIBUTE TO BE USED ON BLANK LINE
114
115 CHARACTER HANDLING ROUTINES
116
117 (AH) = 08H READ ATTRIBUTE/CHARACTER AT CURRENT CURSOR POSITION
118 (BH) = DISPLAY PAGE (VALID FOR ALPHA MODES ONLY)
119 ON EXIT:
120 (AL) = CHAR READ
121 (AH) = ATTRIBUTE OF CHARACTER READ (ALPHA MODES ONLY)
122
123 (AH) = 09H WRITE ATTRIBUTE/CHARACTER AT CURRENT CURSOR POSITION
124 (BH) = DISPLAY PAGE (VALID FOR ALPHA MODES ONLY)

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115 ; (CX) = COUNT OF CHARACTERS TO WRITE
116 ; (AL) = CHAR TO WRITE
117 ; (BL) = ATTRIBUTE OF CHARACTER (ALPHA)/COLOR OF CHAR (GRAPHICS)
118 ; SEE NOTE ON WRITE DOT FOR BIT 7 OF BL = 1.
119 ; (AH) = 0AH WRITE CHARACTER ONLY AT CURRENT CURSOR POSITION
120 ; (BH) = DISPLAY PAGE (VALID FOR ALPHA MODES ONLY)
121 ; (CX) = COUNT OF CHARACTERS TO WRITE
122 ; (AL) = CHAR TO WRITE
123 ; NOTE: USE FUNCTION (AH) = 09H IN GRAPHICS MODES
124 ; FOR READ/WRITE CHARACTER INTERFACE WHILE IN GRAPHICS MODE. THE
125 ; CHARACTERS ARE FORMED FROM A CHARACTER GENERATOR IMAGE
126 ; MAINTAINED IN THE SYSTEM ROM. ONLY THE 1ST 128 CHARS
127 ; ARE CONTAINED THERE. TO READ/WRITE THE SECOND 128 CHARS,
128 ; THE USER MUST INITIALIZE THE POINTER AT INTERRUPT 1FH
129 ; (LOCATION 0007CH) TO POINT TO THE 1K BYTE TABLE CONTAINING
130 ; THE CODE POINTS FOR THE SECOND 128 CHARS (128-255).
131 ; FOR WRITE CHARACTER INTERFACE IN GRAPHICS MODE, THE REPLICATION FACTOR
132 ; CONTAINED IN (CX) ON ENTRY WILL PRODUCE VALID RESULTS ONLY
133 ; FOR CHARACTERS CONTAINED ON THE SAME ROW. CONTINUATION TO
134 ; SUCCEEDING LINES WILL NOT PRODUCE CORRECTLY.
135 ;
136 ; GRAPHICS INTERFACE
137 ;
138 ; (AH) = 0BH SET COLOR PALETTE
139 ; (BH) = PALETTE COLOR ID BEING SET (0-127)
140 ; (BL) = COLOR VALUE TO BE USED WITH THAT COLOR ID
141 ; NOTE: FOR THE CURRENT COLOR CARD, THIS ENTRY POINT HAS
142 ; MEANING ONLY FOR 320X200 GRAPHICS.
143 ; COLOR ID = 0 SELECTS THE BACKGROUND COLOR (0-15)
144 ; COLOR ID = 1 SELECTS THE PALETTE TO BE USED:
145 ; 0 = GREEN(1)/RED(2)/YELLOW(3)
146 ; 1 = CYAN(1)/MAGENTA(2)/WHITE(3)
147 ; IN 40X25 OR 80X25 ALPHA MODES, THE VALUE SET FOR
148 ; PALETTE COLOR 0 INDICATES THE BORDER COLOR
149 ; TO BE USED (VALUES 0-31, WHERE 16-31 SELECT
150 ; THE HIGH INTENSITY BACKGROUND SET.
151 ; (AH) = 0CH WRITE DOT
152 ; (DX) = ROW NUMBER
153 ; (CX) = COLUMN NUMBER
154 ; (AL) = COLOR VALUE
155 ; IF BIT 7 OF AL = 1, THEN THE COLOR VALUE IS EXCLUSIVE
156 ; ORed WITH THE CURRENT CONTENTS OF THE DOT
157 ; (AH) = 0DH READ DOT
158 ; (DX) = ROW NUMBER
159 ; (CX) = COLUMN NUMBER
160 ; (AL) RETURNS THE DOT READ
161 ;
162 ; ASCII TELETYPE ROUTINE FOR OUTPUT
163 ;
164 ; (AH) = 0EH WRITE TELETYPE TO ACTIVE PAGE
165 ; (AL) = CHAR TO WRITE
166 ; (BL) = FOREGROUND COLOR IN GRAPHICS MODE
167 ; NOTE -- SCREEN WIDTH IS CONTROLLED BY PREVIOUS MODE SET
168 ; (AH) = 0FH CURRENT VIDEO STATE
169 ; RETURNS THE CURRENT VIDEO STATE
170 ; (AL) = MODE CURRENTLY SET ( SEE (AH) = 00H FOR EXPLANATION)
171 ; (AH) = NUMBER OF CHARACTER COLUMNS ON SCREEN
172 ; (BH) = CURRENT ACTIVE DISPLAY PAGE
173 ; (AH) = 10H RESERVED
174 ; (AH) = 11H RESERVED
175 ; (AH) = 12H RESERVED
176 ; (AH) = 13H WRITE STRING
177 ; ES:BP - POINTER TO STRING TO BE WRITTEN
178 ; CX - LENGTH OF CHARACTER STRING TO BE WRITTEN
179 ; DX - CURSOR POSITION FOR STRING TO BE WRITTEN
180 ; BH - PAGE NUMBER
181 ; (AL) = 00H WRITE CHARACTER STRING
182 ; BL - ATTRIBUTE
183 ; STRING IS <CHAR,CHAR, ... ,CHAR>
184 ; CURSOR NOT MOVED
185 ; (AL) = 01H WRITE CHARACTER STRING AND MOVE CURSOR
186 ; BL - ATTRIBUTE
187 ; STRING IS <CHAR,CHAR, ... ,CHAR>
188 ; CURSOR IS MOVED
189 ; (AL) = 02H WRITE CHARACTER AND ATTRIBUTE STRING
190 ; (VALID FOR ALPHA MODES ONLY)
191 ; STRING IS <CHAR,ATTR,CHAR,ATTR .. ,CHAR,ATTR>
192 ; CURSOR IS NOT MOVED
193 ; (AL) = 03H WRITE CHARACTER AND ATTRIBUTE STRING AND MOVE CURSOR
194 ; (VALID FOR ALPHA MODES ONLY)
195 ; STRING IS <CHAR,ATTR,CHAR,ATTR .. ,CHAR,ATTR>
196 ; CURSOR IS MOVED
197 ; NOTE: CARRIAGE RETURN, LINE FEED, BACKSPACE, AND BELL ARE
198 ; TREATED AS COMMANDS RATHER THAN PRINTABLE CHARACTERS.
199 ;
200 ; BX,CX,DX,S1,D1,BP,SP,DS,ES,SS PRESERVED DURING CALLS EXCEPT FOR
201 ; BX,CX,DX RETURN VALUES ON FUNCTIONS 03H,04H,0DH AND 0DH. ON ALL CALLS
202 ; AX IS MODIFIED.
203 ;-----

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ASSUME CS:CODE,DS:DATA,ES:NOTHING
207 0000 005F R M1 DW OFFSET SET_MODE ; TABLE OF ROUTINES WITHIN VIDEO I/O
208 0002 0146 R DW OFFSET SET_CTYPE
209 0004 0167 R DW OFFSET SET_CPOS
210 0006 018F R DW OFFSET READ_CURSOR
211 0008 0785 R DW OFFSET READ_LPEN
212 000A 01A6 R DW OFFSET ACT_DISP_PAGE
213 000C 020F R DW OFFSET SCROLL_UP
214 000E 02AD R DW OFFSET SCROLL_DOWN
215 0010 02FF R DW OFFSET READ_AC_CURRENT
216 0012 038C R DW OFFSET WRITE_AC_CURRENT
217 0014 03BE R DW OFFSET WRITE_C_CURRENT
218 0016 01C8 R DW OFFSET SET_COLOR
219 0018 0450 R DW OFFSET WRITE_DOT
220 001A 043F R DW OFFSET READ_DOT
221 001C 06FE R DW OFFSET WRITE_TTY
222 001E 01EE R DW OFFSET VIDEO_STATE
223 0020 013D R DW OFFSET VIDEO_RETURN ; RESERVED
224 0022 013D R DW OFFSET VIDEO_RETURN ; RESERVED
225 0024 013D R DW OFFSET VIDEO_RETURN ; RESERVED
226 0026 03BB R DW OFFSET WRITE_STRING ; CASE 13H, WRITE STRING
227 = 0028 M1L EQU $-M1
228

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SECTION 5

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229 0028          VIDEO_IO_1  PROC    NEAR          ; ENTRY POINT FOR ORG 0F065H
230 0028 FB          STI              ; INTERRUPTS BACK ON
231 0029 FC          CLO              ; SET DIRECTION FORWARD
232 002A 80 FC 14    CMP             AH,MIL/2      ; TEST FOR WITHIN TABLE RANGE
233 002D 73 2F      JNB             M4              ; BRANCH TO EXIT IF NOT A VALID COMMAND
234
235 002F 06          PUSH     ES              ;
236 0030 1E          PUSH     DS              ; SAVE WORK AND PARAMETER REGISTERS
237 0031 52          PUSH     DX              ;
238 0032 51          PUSH     CX              ;
239 0033 53          PUSH     BX              ;
240 0034 56          PUSH     SI              ;
241 0035 57          PUSH     DI              ;
242 0036 55          PUSH     BP              ;
243 0037 BE ---- R   MOV     SI,DATA          ; POINT DS: TO DATA SEGMENT
244 003A 8E DE       MOV     DS,SI          ;
245 003C 8B F0       MOV     SI,AX          ; SAVE COMMAND/DATA INTO (SI) REGISTER
246 003E A2 0010 R   MOV     AH,BYTE PTR @EQUIP_FLAG ; SET EQUIPMENT FLAG VIDEO BITS
247 0041 24 30      AND     AL,30H         ; ISOLATE CRT SWITCHES
248 0043 3C 30      CMP     AL,30H         ; IS SETTING FOR MONOCHROME CARD?
249 0045 BF B800    MOV     DI,0B800H      ; GET SEGMENT FOR COLOR CARD
250 0048 15 03      JNE     M2             ; SKIP IF NOT MONOCHROME CARD
251 004A BF B000    MOV     DI,0B000H      ; ELSE GET SEGMENT FOR MONOCHROME CARD
252 004D
253 004D 8E C7       MOV     ES,DI          ; SET UP TO POINT AT VIDEO MEMORY AREAS
254 004F 8A C4       MOV     AL,AH          ; PLACE COMMAND IN LOW BYTE OF (AX)
255 0051 98          CBW                     ; AND FORM BYTE OFFSET WITH COMMAND
256 0052 D1 E0       SAL     AX,1           ; TIMES 2 FOR WORD TABLE LOOKUP
257 0054 96          XCHG    SI,AX          ; MOVE OFFSET INTO LOOKUP REGISTER (SI)
258                                     ; AND RESTORE COMMAND/DATA INTO (AX)
259 0055 8A 26 0049 R MOV     AH,@CRT_MODE    ; MOVE CURRENT MODE INTO (AH) REGISTER
260                                     ;
261 0059 2E:1F AA 0000 R JMP     WORD PTR CS:[SI+OFFSET M1] ; GO TO SELECTED FUNCTION
262
263 005E             M4:          ; COMMAND NOT VALID
264 005E CF             ; DO NOTHING IF NOT IN VALID RANGE
265 005F
266
267          VIDEO_IO_1  ENDP
268          ;-----
269          ; SET_MODE
270          ; THIS ROUTINE INITIALIZES THE ATTACHMENT TO
271          ; THE SELECTED MODE. THE SCREEN IS BLANKED.
272          ; INPUT
273          ; @EQUIP_FLAG BITS 0-4 = MODE/WIDTH
274          ; 1 = MONOCHROME (FORCES MODE 7)
275          ; 01 = COLOR ADAPTER 40x25 (MODE 0 DEFAULT)
276          ; 10 = COLOR ADAPTER 80x25 (MODE 2 DEFAULT)
277          ; (AL) = COLOR MODE REQUESTED ( RANGE 0 - 6 )
278          ; OUTPUT
279          ; NONE
280          ;-----
281 005F          SET_MODE  PROC    NEAR
282 005F BA 03D4      MOV     DX,03D4H        ; ADDRESS OF COLOR CARD
283 0062 8B 3E 0010 R MOV     DI,@EQUIP_FLAG  ; GET EQUIPMENT FLAGS SETTING
284 0066 81 ET 0030  AND     DI,30H          ; ISOLATE CRT SWITCHES
285 006A 83 FF 30    CMP     DI,30H          ; IS BW CARD INSTALLED AS PRIMARY
286 006D 75 06      JNE     M8C             ; SKIP AND CHECK IF COLOR
287 006F 8B 07      MOV     AL,7            ; ELSE INDICATE INTERNAL BW CARD MODE
288 0071 82 B4       MOV     DL,0B4H         ; SET ADDRESS OF BW (MONOCHROME) CARD
289 0073 BE 0D      JMP     SHORT M8        ; CONTINUE WITH FORCED MODE 7
290 0075
291 0075 3C 07      CMP     AL,7            ; CHECK FOR VALID COLOR MODES 0-6
292 0077 72 09      JB     M8B              ; CONTINUE IF BELOW MODE 7
293 0079 B0 00      MOV     AL,0            ; FORCE DEFAULT 40x25 BW MODE
294 007B 83 FF 20    CMP     DI,20H          ; CHECK FOR @EQUIP_FLAG AT 80x25 BW
295 007E 74 02      JE     M8               ; CONTINUE WITH MODE 0 IF NOT
296 0080 B0 02      MOV     AL,2            ; ELSE FORCE MODE 2
297 0082
298 0082 A2 0049 R   MOV     @CRT_MODE,AL    ; SAVE MODE IN GLOBAL VARIABLE
299 0085 89 16 0063 R MOV     @ADDR_6845,DX    ; SAVE ADDRESS OF BASE
300 0089 C6 06 0084 R 1B MOV     DS,DS-25-1      ; INITIALIZE DEFAULT ROW COUNT OF 25
301 008E 1E          PUSH     DS              ; SAVE POINTER TO DATA SEGMENT
302 008F 50          PUSH     AX              ; SAVE MODE NUMBER (AL)
303 0091 98          CBW                     ; CLEAR HIGH BYTE OF MODE
304 0093 2E:8A 84 0000 E MOV     SI,AX            ; SET TABLE POINTER, INDEXED BY MODE
305 0098 A2 0065 R   MOV     @CRT_MODE_SET,AL ; GET THE MODE SET VALUE FROM TABLE
306 009D 52          PUSH     DX              ; SAVE THE MODE SET VALUE
307 009E 83 C2 04   ADD     DX,4            ; VIDEO OFF, SAVE HIGH RESOLUTION BIT
308 00A1 EE          OUT     DX,AL           ; SAVE OUTPUT PORT VALUE
309 00A2 5A          POP     DX              ; POINT TO CONTROL REGISTER
310 00A3 2B DB       SUB     BX,BX           ; RESET VIDEO TO OFF TO SUPPRESS ROLLING
311 00A5 8E DB       MOV     DS,BX           ; BACK TO BASE REGISTER
312 00A7 C5 1E 0074 R LDS     BX,@PARAM_PTR   ; SET UP FOR ABS0 SEGMENT
313 00AB 50          POP     AX              ; ESTABLISH VECTOR TABLE ADDRESSING
314 00AB 58          POP     AX              ; GET POINTER TO VIDEO PARAMS
315 00AC B9 0010     MOV     CX,16           ; RECOVER MODE NUMBER IN (AL)
316 00AF 3C 02      CMP     AL,2            ; LENGTH OF EACH ROW OF TABLE
317 00B1 72 0E      JC     M9               ; DETERMINE WHICH ONE TO USE
318 00B3 03 D9     ADD     BX,CX           ; MODE IS 0 OR 1
319 00B5 3C 04      JC     M9               ; NEXT ROW OF INITIALIZATION TABLE
320 00B7 72 08      JC     M9               ; MODE IS 2 OR 3
321 00B9 03 D9     ADD     BX,CX           ; MOVE TO GRAPHICS ROW OF INIT_TABLE
322 00BB 3C 07      JC     M9               ; MODE IS 4,5, OR 6
323 00BD 72 02      JC     M9               ; MOVE TO BW CARD ROW OF INIT_TABLE
324 00BF 03 D9     ADD     BX,CX           ;
325
326          ;-----
327          ; BX POINTS TO CORRECT ROW OF INITIALIZATION TABLE
328
329 00C1          M9:          ; OUT INIT
330 00C1 50          PUSH     AX              ; SAVE MODE IN (AL)
331 00C2 8B 47 0A    MOV     AX,[BX+10]      ; GET THE CURSOR MODE FROM THE TABLE
332 00C5 86 E0      XCHG    AH,AL           ; PUT CURSOR MODE IN CORRECT POSITION
333 00C7 1E          PUSH     DS              ; SAVE TABLE SEGMENT POINTER
334 00C8 5A          ASSUME  DS:DATA         ;
335 00C8 E8 0000 E  CALL    @CURSOR_MODE,AX ; POINT DS TO DATA SEGMENT
336 00CB A3 0060 R   MOV     DS:CODE         ; PLACE INTO BIOS DATA SAVE AREA
337 00CC 5A          ASSUME  DS:CODE         ;
338 00CE 1F          POP     DS              ; RESTORE THE TABLE SEGMENT POINTER
339 00CF 32 E4      XOR     AH,AH           ; AH IS REGISTER NUMBER DURING LOOP
340
341          ;----- LOOP THROUGH TABLE, OUTPUTTING REGISTER ADDRESS, THEN VALUE FROM TABLE
342

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343 00D1          M10:      MOV     AL,AH          ; INITIALIZATION LOOP
344 00D1 BA C4   OUT     DX,AL        ; GET 6845 REGISTER NUMBER
345 00D3 EE      INC     DX           ;
346 00D4 42     INC     DX           ; POINT TO DATA PORT
347 00D5 FE C4   INC     AH          ; NEXT REGISTER VALUE
348 00D7 BA 07   MOV     AL,[BX]      ; GET TABLE VALUE
349 00D9 EE      OUT     DX,AL        ; OUT TO CHIP
350 00DA 43     INC     BX           ; NEXT IN TABLE
351 00DB 4A      DEC     DX           ; BACK TO POINTER REGISTER
352 00DC E2 F3   LOOP    M10         ; DO THE WHOLE TABLE
353 00DE 58      POP     AX          ; GET MODE BACK INTO (AL)
354 00DF 1F      POP     DS          ; RECOVER SEGMENT VALUE
355
356
357             ;----- FILL REGEN AREA WITH BLANK
358
359 00E0 33 FF   XOR     DI,DI        ; SET UP POINTER FOR REGEN
360 00E2 89 3E   MOV     ©CRT_START,DI ; START ADDRESS SAVED IN GLOBAL
361 00E6 C6 06 0062 R 00 MOV     ©ACTIVE_PAGE,0 ; SET PAGE VALUE
362 00EB B9 2000 MOV     CX,8192      ; NUMBER OF WORDS IN COLOR CARD
363 00EE 3C 04   CMP     AL,4        ; TEST FOR GRAPHICS
364 00F0 72 0A   JC      M12         ; NO GRAPHICS INIT
365 00F2 3C 07   CMP     AL,7        ; TEST FOR BW CARD
366 00F4 74 04   JE      M11         ; BW_CARD INIT
367 00F6 33 C0   XOR     AX,AX       ; FILL FOR GRAPHICS MODE
368 00F8 EB 05   JMP     SHORT M13   ; CLEAR BUFFER
369 00FA         ; BW_CARD INIT
370 00FB B5 08   M11:    MOV     CH,08H      ; BUFFER SIZE ON BW CARD (2048)
371 00FC         ; NO GRAPHICS INIT
372 00FC B8 0720 M12:    MOV     AX,' '*7*H  ; FILL CHAR FOR ALPHA + ATTRIBUTE
373 00FF         ; CLEAR BUFFER
374 00FF F3/ AB  M13:    REP     STOSW       ; FILL THE REGEN BUFFER WITH BLANKS
375
376             ;----- ENABLE VIDEO AND CORRECT PORT SETTING
377
378 0101 8B 16 0063 R MOV     DX,©ADDR_6845 ; PREPARE TO OUTPUT TO VIDEO ENABLE PORT
379 0105 83 C2 04   ADD     DX,4        ; POINT TO THE MODE CONTROL REGISTER
380 0108 A0 0065 R  MOV     AL,©CRT_MODE_SET ; GET THE MODE SET VALUE
381 010B EE      OUT     DX,AL        ; SET VIDEO ENABLE PORT
382
383             ;----- DETERMINE NUMBER OF COLUMNS, BOTH FOR ENTIRE DISPLAY
384             ;----- AND THE NUMBER TO BE USED FOR TTY INTERFACE
385
386 010C 2E1 BA 84 0000 E MOV     AL,C5:[SI + OFFSET M6] ; GET NUMBER OF COLUMNS ON THIS SCREEN
387 0111 98      CBW                ; CLEAR HIGH BYTE
388 0112 A3 004A R  MOV     ©CRT_COLS,AX ; INITIALIZE NUMBER OF COLUMNS COUNT
389
390             ;----- SET CURSOR POSITIONS
391
392 0115 81 E6 000E AND     SI,000EH     ; WORD OFFSET INTO CLEAR LENGTH TABLE
393 0119 2E1 8B 84 0000 E MOV     AX,C5:[SI + OFFSET M5] ; LENGTH TO CLEAR
394 011E A3 004C R  MOV     ©CRT_LEN,AX  ; SAVE LENGTH OF CRT --- NOT USED FOR BW
395 0121 B9 0008   MOV     CX,8        ; CLEAR ALL CURSOR POSITIONS
396 0124 BF 0050 R  MOV     DI,OFFSET ©CURSOR_POSN ;
397 0127 1E      PUSH    DS          ; ESTABLISH SEGMENT
398 0128 07      POP     ES          ; ADDRESSING
399 0129 33 C0   XOR     AX,AX       ;
400 012B F3/ AB  REP     STOSW       ; FILL WITH ZEROES
401
402             ;----- SET UP OVERSCAN REGISTER
403
404 012D 42      INC     DX           ; SET OVERSCAN PORT TO A DEFAULT
405 012E 80 30   AND     DX,30H      ; 30H VALUE FOR ALL MODES EXCEPT 640X200
406 0130 80 3E 0049 R 06 MOV     ©CRT_MODE,6  ; SEE IF THE MODE IS 640X200 BW
407 0135 75 02   JNZ    M14         ; IF NOT 640X200, THEN GO TO REGULAR
408 0137 B0 3F   MOV     AL,3FH      ; IF IT IS 640X200, THEN PUT IN 3FH
409
410 0139 EE      OUT     DX,AL        ; OUTPUT THE CORRECT VALUE TO 3D9 PORT
411 013A A2 0066 R  MOV     ©CRT_PALETTE,AL ; SAVE THE VALUE FOR FUTURE USE
412
413             ;----- NORMAL RETURN FROM ALL VIDEO RETURNS
414
415 013D          VIDEO_RETURN:
416 013D 5D      POP     BP
417 013E 5F      POP     DI
418 013F 5E      POP     SI
419 0140 5B      POP     BX
420 0141
421 0141 59      M15:    POP     CX          ; VIDEO_RETURN_C
422 0142 5A      POP     DX
423 0143 1F      POP     DS          ; RECOVER SEGMENTS
424 0144 07      POP     ES          ; ALL DONE
425 0145 CF      IRET
426 0146
427             SET_MODE     ENDP
428             ;-----
429             ; SET_CTYPE
430             ; THIS ROUTINE SETS THE CURSOR VALUE
431             ; INPUT (CX) HAS CURSOR VALUE CH-START LINE, CL-STOP LINE
432             ; OUTPUT
433             ; NONE
434             ;-----
435 0146          SET_CTYPE   PROC   NEAR
436 0146 B4 0A   MOV     AH,10        ; 6845 REGISTER FOR CURSOR SET
437 0148 89 0E 0060 R  MOV     ©CURSOR_MODE,CX ; SAVE IN DATA AREA
438 014C E8 0151 R  CALL    M16         ; OUTPUT CX REGISTER
439 014F EB EC   JMP     VIDEO_RETURN
440
441             ;----- THIS ROUTINE OUTPUTS THE CX REGISTER TO THE 6845 REGISTERS NAMED IN (AH)
442
443
444 0151          M16:
445 0151 8B 16 0063 R MOV     DX,©ADDR_6845 ; ADDRESS REGISTER
446 0155 8A C4   MOV     AL,AH        ; GET VALUE
447 0157 EE      OUT     DX,AL        ; REGISTER SET
448 0158 42     INC     DX           ; DATA REGISTER
449 0159 8A C5   MOV     AL,CH        ; DATA
450 015B EE      OUT     DX,AL        ;
451 015C 4A      DEC     DX           ;
452 015D 8A C4   MOV     AL,AH        ; POINT TO OTHER DATA REGISTER
453 015F FE C0   INC     AL          ; POINT TO OTHER DATA REGISTER
454 0161 EE      OUT     DX,AL        ; SET FOR SECOND REGISTER
455 0162 42     INC     DX           ;
456 0163 8A C1   MOV     AL,CL        ; SECOND DATA VALUE

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SECTION 5

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457 0165 EE          OUT    DX,AL
458 0166 C3          RET
459 0167          SET_CTYPE ENDP          ; ALL DONE
460
461
462          ;-----
463          ; SET_CPOS
464          ; THIS ROUTINE SETS THE CURRENT CURSOR POSITION TO THE
465          ; NEW X-Y VALUES PASSED
466          ; INPUT
467          ; DX - ROW,COLUMN OF NEW CURSOR
468          ; BH - DISPLAY PAGE OF CURSOR
469          ; OUTPUT
470          ; CURSOR IS SET AT 6845 IF DISPLAY PAGE IS CURRENT DISPLAY
471          ;-----
471 0167          SET_CPOS PROC NEAR
472 0167 8A C7          MOV    AL,BH          ; MOVE PAGE NUMBER TO WORK REGISTER
473 0169 98          CBW          ; CONVERT PAGE TO WORD VALUE
474 016A D1 E0        SAL    AX,1          ; WORD OFFSET
475 016C 96          XCHG   AX,S1          ; USE INDEX REGISTER
476 016D 89 94 0050 R MOV    [SI+OFFSET@CURSOR_POSN],DX ; SAVE THE POINTER
477 0171 38 3E 0062 R CMP    *ACTIVE_PAGE,BH
478 0175 75 05        JNZ   M17          ; SET CPOS_RETURN
479 0177 8B C2        MOV    AX,DX          ; GET ROW/COLUMN TO AX
480 0179 E8 017E R   CALL  M18          ; CURSOR_SET
481 017C          M17:          ; SET_CPOS_RETURN
482 017C EB BF        JMP    VIDEO_RETURN
483 017E          SET_CPOS ENDP
484
485          ;----- SET CURSOR POSITION, AX HAS ROW/COLUMN FOR CURSOR
486
487 017E          M18 PROC NEAR
488 017E E8 0200 R   CALL  POSITION          ; DETERMINE LOCATION IN REGEN BUFFER
489 0181 8B C8        MOV    CX,AX
490 0183 03 0E 004E R ADD    CX,@CRT_START ; ADD IN THE START ADDRESS FOR THIS PAGE
491 0187 D1 F9        SAR    SAR          ; DIVIDE BY 2 FOR CHAR ONLY COUNT
492 0189 B4 0E        MOV    AH,14        ; REGISTER NUMBER FOR CURSOR
493 018B E8 0151 R   CALL  M16          ; OUTPUT THE VALUE TO THE 6845
494 018E C3          RET
495 018F          M18 ENDP
496
497          ;-----
498          ; READ_CURSOR
499          ; THIS ROUTINE READS THE CURRENT CURSOR VALUE FROM THE
500          ; 6845, FORMATS IT, AND SENDS IT BACK TO THE CALLER
501          ; INPUT
502          ; BH - PAGE OF CURSOR
503          ; OUTPUT
504          ; DX - ROW, COLUMN OF THE CURRENT CURSOR POSITION
505          ; CX - CURRENT CURSOR MODE
506          ;-----
507 018F          READ_CURSOR PROC NEAR
508 018F 8A DF        MOV    BL,BH
509 0193 D1 E3        XOR    BH,BH
510 0195 8B 97 0050 R MOV    BX,1          ; WORD OFFSET
511 0199 8B 0E 0060 R MOV    DX,[BX+OFFSET@CURSOR_POSN]
512 019D 5D          POP    BP
513 019E 5F          POP    DI
514 019F 5E          POP    SI
515 01A0 5B          POP    BX
516 01A1 58          POP    AX          ; DISCARD SAVED CX AND DX
517 01A2 58          POP    AX
518 01A3 1F          POP    DS
519 01A4 07          POP    ES
520 01A5 CF          IRET
521 01A6          READ_CURSOR ENDP
522
523          ;-----
524          ; ACT_DISP_PAGE
525          ; THIS ROUTINE SETS THE ACTIVE DISPLAY PAGE, ALLOWING
526          ; THE FULL USE OF THE MEMORY SET ASIDE FOR THE VIDEO ATTACHMENT
527          ; INPUT
528          ; AL HAS THE NEW ACTIVE DISPLAY PAGE
529          ; OUTPUT
530          ; THE 6845 IS RESET TO DISPLAY THAT PAGE
531          ;-----
531 01A6          ACT_DISP_PAGE PROC NEAR
532 01A6 A2 0062 R   MOV    *ACTIVE_PAGE,AL ; SAVE ACTIVE PAGE VALUE
533 01A9 98          CBW          ; CONVERT (AL) TO WORD
534 01AA 50          PUSH   AX          ; SAVE PAGE VALUE
535 01AB F7 26 004C R MUL    WORD PTR @CRT_LEN ; DISPLAY PAGE TIMES REGEN LENGTH
536 01AF A3 004E R   MOV    @CRT_START,AX ; SAVE START ADDRESS FOR LATER
537 01B2 8B C8        MOV    CX,AX          ; START ADDRESS TO CX
538 01B4 D1 F9        SAR    CX,1          ; DIVIDE BY 2 FOR 6845 HANDLING
539 01B6 B4 0C        MOV    AH,12        ; 6845 REGISTER FOR START ADDRESS
540 01B8 E8 0151 R   CALL  M16          ; RECOVER PAGE VALUE
541 01BB 5B          POP    BX          ; 2 FOR WORD OFFSET
542 01BC D1 E3        SAL    BX,1          ; GET CURSOR FOR THIS PAGE
543 01BE 8B A0 0066 R MOV    AX,[BX + OFFSET@CURSOR_POSN] ; SET THE CURSOR POSITION
544 01C2 E8 017E R   CALL  M18          ; SET THE CURSOR POSITION
545 01C5 E9 013D R   JMP    VIDEO_RETURN
546 01C8          ACT_DISP_PAGE ENDP
547
548          ;-----
549          ; SET_COLOR
550          ; THIS ROUTINE WILL ESTABLISH THE BACKGROUND COLOR, THE OVERSCAN COLOR,
551          ; AND THE FOREGROUND COLOR SET FOR MEDIUM RESOLUTION GRAPHICS
552          ; INPUT
553          ; (BH) HAS COLOR ID
554          ; IF BH=0, THE BACKGROUND COLOR VALUE IS SET FROM THE LOW BITS OF BL (0-31)
555          ; IF BH=1, THE PALETTE SELECTION IS MADE
556          ; BASED ON THE LOW BIT OF BL:
557          ; 0 = GREEN, RED, YELLOW FOR COLORS 1,2,3
558          ; 1 = BLUE, CYAN, MAGENTA FOR COLORS 1,2,3
559          ; (BL) HAS THE COLOR VALUE TO BE USED
560          ; OUTPUT
561          ; THE COLOR SELECTION IS UPDATED
562          ;-----
563 01C8          SET_COLOR PROC NEAR
564 01C8 8B 16 0063 R MOV    DX,@ADDR_6845 ; I/O PORT FOR PALETTE
565 01CC 83 C2 05      ADD    DX,5          ; OVERSCAN PORT
566 01CF A0 0066 R   MOV    AL,@CRT_PALETTE ; GET THE CURRENT PALETTE VALUE
567 01D2 0A FF        OR    BH,BH          ; IS THIS COLOR 0?
568 01D4 75 0E        JNZ   M20          ; OUTPUT COLOR 1
569
570          ;----- HANDLE COLOR 0 BY SETTING THE BACKGROUND COLOR

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571
572 01D6 24 E0          AND    AL,0E0H          ; TURN OFF LOW 5 BITS OF CURRENT
573 01D8 80 E3 1F      AND    BL,01FH         ; TURN OFF HIGH 3 BITS OF INPUT VALUE
574 01D8 0A C3        OR     AL,BL           ; PUT VALUE INTO REGISTER
575 01DD                M19:   ; OUTPUT THE PALETTE
576 01DD EE           OUT    DX,AL           ; OUTPUT COLOR SELECTION TO 3D9 PORT
577 01DE A2 0066 R    MOV    #CRT_PALETTE,AL ; SAVE THE COLOR VALUE
578 01E1 E9 013D R    JMP    VIDEO_RETURN
579
580
581
582 01E4                ;----- HANDLE COLOR I BY SELECTING THE PALETTE TO BE USED
583 01E4 24 DF          AND    AL,0DFH         ; TURN OFF PALETTE SELECT BIT
584 01E6 DD EB          SHR    BL,1           ; TEST THE LOW ORDER BIT OF BL
585 01E8 73 F3          JNC    M19            ; ALREA DONE
586 01EA 0C 20          OR     AL,20H         ; TURN ON PALETTE SELECT BIT
587 01EC EB EF          JMP    M19            ; GO DO IT
588 01EE
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596 01EE                ;----- SET_COLOR
597 01EE BA 26 004A R  MOV    AH,BYTE PTR #CRT_COLS ; GET NUMBER OF COLUMNS
598 01F2 A0 0049 R    MOV    AL,#CRT_MODE      ; CURRENT MODE
599 01F5 BA 3E 0062 R  MOV    BH,#ACTIVE_PAGE   ; GET CURRENT ACTIVE PAGE
600 01F9 5D            POP    BP              ; RECOVER REGISTERS
601 01FA 5F            POP    DI
602 01FB 5E            POP    SI
603 01FC 59            POP    CX              ; DISCARD SAVED BX
604 01FD E9 0141 R    JMP    M15            ; RETURN TO CALLER
605 0200
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615 0200                ;----- POSITION
616 0200 53            PROC    NEAR
617 0201 93            PUSH   BX              ; SAVE REGISTER
618 0202 A0 004A R    XCHG  BX,AX           ; SAVE ROW/COLUMN POSITION IN (BX)
619 0205 F6 E5          MOV    AL,BYTE PTR #CRT_COLS ; GET COLUMNS PER ROW COUNT
620 0207 32 FF          MUL    BH,BH          ; DETERMINE BYTES TO ROW
621 0209 03 C3          ADD    AX,BX           ; ADD IN COLUMN VALUE
622 020B D1 E0          SAL    AX,1           ; * 2 FOR ATTRIBUTE BYTES
623 020D 5B            POP    BP
624 020E C3            RET
625 020F            ENDP
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642 020F                ;----- SCROLL_UP
643
644 020F E8 02EA R    CALL  TEST_LINE_COUNT ; TEST FOR GRAPHICS MODE
645 0212 80 FC 04      CMP    AH,4           ; HANDLE SEPARATELY
646 0215 72 08          JC     N1              ; TEST FOR BW CARD
647 0217 80 FC 07      CMP    AH,7
648 021A 74 03          JE     N1
649 021C E9 04AC R    JMP    GRAPHICS_UP
650 021F
651 021F 53            PUSH   BX              ; UP_CONTINUE
652 0220 8B C1          MOV    AX,CX           ; SAVE FILL ATTRIBUTE IN BH
653 0222 EB 025C R    CALL  SCROLL_POSITION ; UPPER LEFT POSITION
654 0225 74 31          JZ     N1              ; DO SETUP FOR SCROLL
655 0227 03 F0          ADD    SI,AX           ; BLANK FIELD
656 0229 8A E6          MOV    AH,DH          ; FROM ADDRESS
657 022B 2A E3          SUB    AH,BL           ; # ROWS IN BLOCK
658 022D                ; ROW_LOOP
659 022D E8 029D R    CALL  N10            ; MOVE ONE ROW
660 0230 03 F5          ADD    SI,BP           ; POINT TO NEXT LINE IN BLOCK
661 0232 03 FD          DD    N2              ; COUNT OF LINES TO MOVE
662 0234 FE CC          DEC    AH              ; ROW_LOOP
663 0236 75 F5          JNZ    N2              ; CLEAR_ENTRY
664 0238                ; CLEAR ATTRIBUTE IN AH
665 0238 58            POP    AX              ; FILL WITH BLANKS
666 0239 80 20          MOV    AL,' '         ; CLEAR_LOOP
667 023B EB 0246 R    CALL  CLEAR_THE_ROW  ; CLEAR THE ROW
668 023E 03 FD          DD    N4              ; POINT TO NEXT LINE
669 0240 FE CB          DEC    BL              ; COUNTER OF LINES TO SCROLL
670 0242 75 F7          JNZ    N4              ; CLEAR_LOOP
671 0244                ; SCROLL_END
672 0244
673 0244 E8 0000 E    CALL  DDS             ; IS THIS THE BLACK AND WHITE CARD
674 0247 80 3E 0049 R 0T CMP    #CRT_MODE,7    ; IF SO, SKIP THE MODE RESET
675 024C 74 07          JE     N6              ; GET THE VALUE OF THE MODE SET
676 024E AD 0065 R    MOV    AL,#CRT_MODE_SET ; ALWAYS SET COLOR CARD PORT
677 0251 BA 03D8 R    MOV    DX,03D8H
678 0254 EE           OUT    DX,AL
679 0255                ; VIDEO_RET_HERE
680 0255 E9 013D R    JMP    VIDEO_RETURN
681 0258
682 0258 8A DE          MOV    BL,DH          ; BLANK_FIELD
683 025A 5B DC          JMP    N3              ; GET ROW COUNT
684 025C                ; GO CLEAR THAT AREA
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SECTION 5

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685 ;----- HANDLE COMMON SCROLL SET UP HERE
686
687
688 025C SCROLL_POSITION PROC NEAR
689 025C EB 0200 R CALL POSITION ; CONVERT TO REGEN POINTER
690 025F 03 06 004E R ADD AX,RCRT_START ; OFFSET OF ACTIVE PAGE
691 0263 BB F8 MOV D1,AX ; TO ADDRESS FOR SCROLL
692 0265 BF F0 MOV SI,AX ; FROM ADDRESS FOR SCROLL
693 0267 2B D1 SUB DX,CX ; DX = #ROWS, #COLS IN BLOCK
694 0269 FE C6 INC DH
695 026B FE C2 INC DL ; INCREMENT FOR 0 ORIGIN
696 026D 32 ED XOR CH,CH ; SET HIGH BYTE OF COUNT TO ZERO
697 026F BB 2E 004A R MOV BP,#CRT_COLS ; GET NUMBER OF COLUMNS IN DISPLAY
698 0273 03 02 BP ADD D1,AX ; TIMES 2 FOR ATTRIBUTE BYTE
699 0275 A0 004A R MOV AL,BYTE PTR #CRT_COLS ; GET CHARACTERS PER LINE COUNT
700 0278 F6 E3 MUL BL ; DETERMINE OFFSET TO FROM ADDRESS
701 027A 03 C0 ADD AX,AX ; *2 FOR ATTRIBUTE BYTE
702 027C 50 PUSH AX ; SAVE LINE COUNT
703 027D A0 0049 R MOV AL,#CRT_MODE ; GET CURRENT MODE
704 0280 06 POP ES ; ESTABLISH ADDRESSING TO REGEN BUFFER
705 0281 4F POP DS ; FOR BOTH POINTERS
706 0282 3C 02 CMP AL,2 ; TEST FOR COLOR CARD SPECIAL CASES HERE
707 0284 72 13 JB N9 ; HAVE TO HANDLE 80X25 SEPARATELY
708 0286 3C 03 CMP AL,3
709 0288 77 0F JA N9
710
711 028A 52 PUSH DX ; 80X25 COLOR CARD SCROLL
712 028B BA 03DA MOV DX,3DAH ; GUARANTEED TO BE COLOR CARD HERE
713 028E N8: IN AL,DX ; WAIT DISP_ENABLE
714 028E EC TEST AL,RVRT ; GET PORT
715 028F AB 08 JZ NB ; WAIT FOR VERTICAL RETRACE
716 0291 74 FB JB N8 ; WAIT_DISP_ENABLE
717 0293 80 25 MOV AL,25H
718 0295 B2 D8 MOV DL,0DAH ; ADDRESS CONTROL PORT
719 0297 EE OUT DX,AL ; TURN OFF VIDEO DURING VERTICAL RETRACE
720 0298 5A POP DX
721 0299 N9: POP AX ; RESTORE LINE COUNT
722 0299 58 OR BL,BL ; 0 SCROLL MEANS BLANK FIELD
723 029A 0A DB RET ; RETURN WITH FLAGS SET
724 029C C3 SCROLL_POSITION ENDP
725 029D
726 ;----- MOVE_ROW
727
728 029D PROC NEAR
729 029D BA CA MOV CL,DL ; GET # OF COLS TO MOVE
730 029F 56 PUSH SI
731 02A0 57 PUSH D1 ; SAVE START ADDRESS
732 02A1 F3/ A5 REP MOVSW ; MOVE THAT LINE ON SCREEN
733 02A3 5F POP D1
734 02A4 5E POP SI ; RECOVER ADDRESSES
735 02A5 C3 RET
736 02A6 N10 ENDP
737
738 ;----- CLEAR_ROW
739
740 02A6 PROC NEAR
741 02A6 BA CA MOV CL,DL ; GET # COLUMNS TO CLEAR
742 02A8 57 PUSH D1
743 02A9 F3/ AB REP STOSW ; STORE THE FILL CHARACTER
744 02AC C3 POP D1
745 02AD N11 RET
746
747 ;----- SCROLL_DOWN
748 ; THIS ROUTINE MOVES THE CHARACTERS WITHIN A DEFINED
749 ; BLOCK DOWN ON THE SCREEN, FILLING THE TOP LINES
750 ; WITH A DEFINED CHARACTER
751 ; INPUT
752 ; (AH) = CURRENT CRT MODE
753 ; (AL) = NUMBER OF LINES TO SCROLL
754 ; (CX) = UPPER LEFT CORNER OF REGION
755 ; (DX) = LOWER RIGHT CORNER OF REGION
756 ; (BH) = FILL CHARACTER
757 ; (DS) = DATA SEGMENT
758 ; (ES) = REGEN SEGMENT
759 ; OUTPUT
760 ; NONE -- SCREEN IS SCROLLED
761
762 02AD SCROLL_DOWN PROC NEAR
763 02AD FD STD ; DIRECTION FOR SCROLL DOWN
764 02AE EB 02EA R CALL TEST_LINE_COUNT ; TEST FOR GRAPHICS
765 02B1 80 FC 04 CMP AH,4 ; TEST FOR BW CARD
766 02B4 72 06 JC N12 ; TEST FOR BW CARD
767 02B6 80 FC 07 CMP AH,7
768 02B9 74 03 JE N12
769 02BB E9 0503 R JMP GRAPHICS_DOWN
770 02BE N12: PUSH BX ; CONTINUE DOWN
771 02BE 53 MOV AX,DX ; SAVE ATTRIBUTE IN BH
772 02BF BB C2 CALL SCROLL_POSITION ; LOWER RIGHT CORNER
773 02C1 EB 025C R CALL SCROLL_POSITION ; GET REGEN LOCATION
774 02C4 74 20 JZ N16 ; SI IS FROM ADDRESS
775 02C6 2B F0 SUB SI,AX ; GET TOTAL # ROWS
776 02C8 BA E6 MOV AH,DH ; COUNT TO MOVE IN SCROLL
777 02CA EA E3 SUB AH,BL
778 02CC N13:
779 02CC EB 029D R CALL N10 ; MOVE ONE ROW
780 02CF 2B F5 SUB SI,BP
781 02D1 2B FD SUB D1,BP
782 02D3 FE CC DEC AH
783 02D5 75 F5 JNZ N13
784 02D7 N14: POP AX ; RECOVER ATTRIBUTE IN AH
785 02D7 58 MOV AL,' '
786 02D8 B0 20
787 02DA N15: CALL N11 ; CLEAR ONE ROW
788 02DA EB 02A6 R SUB D1,BP ; GO TO NEXT ROW
789 02DD 2B FD DEC BL
790 02DF FE CB DEC N15
791 02E1 75 F7 JNZ N15
792 02E3 E9 0244 R JMP N5 ; SCROLL_END
793 02E6 N16:
794 02E6 BA DE MOV BL,DH
795 02E8 EB ED JMP N14
796 02EA SCROLL_DOWN ENDP
    
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797 PAGE
798 ;----- IF AMOUNT OF LINES TO BE SCROLLED = AMOUNT OF LINES IN WINDOW
799 ;----- THEN ADJUST AL; ELSE RETURN;
800
801 02EA TEST_LINE_COUNT PROC NEAR
802
803 02EA 8A D8 MOV BL,AL ; SAVE LINE COUNT IN BL
804 02EC 0A C0 OR AL,AL ; TEST IF AL IS ALREADY ZERO
805 02EE 74 0E JZ BL_SET ; IF IT IS THEN RETURN...
806 02F0 50 PUSH AX ; SAVE AX
807 02F1 8A C6 MOV AL,DH ; SUBTRACT LOWER ROW FROM UPPER ROW
808 02F3 2A C5 SUB AL,CH
809 02F5 FE C0 INC AL ; ADJUST DIFFERENCE BY 1
810 02F7 3A C0 CMP AL,BL ; LINE COUNT = AMOUNT OF ROWS IN WINDOW?
811 02F9 56 POP AX ; RESTORE AX
812 02FA 75 02 JNE BL_SET ; IF NOT THEN WE'RE ALL SET
813 02FC 2A DB SUB BL,BL ; OTHERWISE SET BL TO ZERO
814 02FE
815 02FE C3 RET ; RETURN
816 02FF TEST_LINE_COUNT ENDP
817
818
819 ;-----
820 ; READ_AC_CURRENT
821 ; THIS ROUTINE READS THE ATTRIBUTE AND CHARACTER AT THE CURRENT
822 ; CURSOR POSITION AND RETURNS THEM TO THE CALLER
823 ; INPUT
824 ; (AH) = CURRENT CRT MODE
825 ; (BH) = DISPLAY PAGE ( ALPHA MODES ONLY )
826 ; (DS) = DATA SEGMENT
827 ; (ES) = REGEN SEGMENT
828 ; OUTPUT
829 ; (AL) = CHARACTER READ
830 ; (AH) = ATTRIBUTE READ
831 ;-----
832 ASSUME DS:DATA,ES:DATA
833
834 02FF 80 FC 04 READ_AC_CURRENT PROC NEAR ; IS THIS GRAPHICS
835 0302 72 08 CMP AH,4
836 JC P10 P10
837 0304 80 FC 07 CMP AH,7 ; IS THIS BW CARD
838 0307 74 03 JE P10
839
840 0309 E9 063E R JMP GRAPHICS_READ
841 030C CALL FIND_POSITION ; READ AC_CONTINUE
842 030E E8 0328 R ; GET REGEN LOCATION AND PORT ADDRESS
843 030F 8B F7 MOV SI,DI ; ESTABLISH ADDRESSING IN SI
844 0311 06 PUSH ES ; GET REGEN SEGMENT FOR QUICK ACCESS
845 0312 1F POP DS
846
847 ;----- WAIT FOR HORIZONTAL RETRACE OR VERTICAL RETRACE IF COLOR 80
848
849 0313 0A DB OR BL,BL ; CHECK MODE FLAG FOR COLOR CARD IN 80
850 0315 75 0D JNZ P11 ; ELSE SKIP RETRACE WAIT - DO FAST READ
851 0317 FB STI ; WAIT FOR HORIZ RETRACE LOW OR VERTICAL
852 0318 90 NOP ; ENABLE INTERRUPTS FIRST
853 0319 FA CLI ; ALLOW FOR SMALL INTERRUPT WINDOW
854 031A EC IN AL,DX ; BLOCK INTERRUPTS FOR SINGLE LOOP
855 031B A5 01 TEST AL,RHRZ ; GET STATUS FROM THE ADAPTER
856 031D 75 F8 JNZ P11 ; IS HORIZONTAL RETRACE LOW
857 031F EC IN AL,DX ; NOW WAIT FOR EITHER RETRACE HIGH
858 0320 A6 09 TEST AL,RVRT+RHRZ ; GET STATUS
859 0322 74 FB JZ P12 ; IS HORIZONTAL OR VERTICAL RETRACE HIGH
860 0324 AD LODSW ; WAIT UNTIL EITHER IS ACTIVE
861 0325 E9 013D R JMP VIDEO_RETURN ; GET THE CHARACTER AND ATTRIBUTE
862 0328 READ_AC_CURRENT ENDP ; EXIT WITH (AX)
863
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870 0328 XCHG AH,BL ; SETUP FOR BUFFER READ OR WRITE
871 032A 8B E8 MOV BP,AX ; SWAP MODE TYPE WITH ATTRIBUTE
872 032C 80 EB 02 SUB BL,2 ; SAVE CHARACTER/ATTR IN (BP) REGISTER
873 032F D0 EB SHR BL,1 ; CONVERT DISPLAY MODE TYPE TO A
874 0331 8A C7 MOV AL,BH ; ZERO VALUE FOR COLOR IN 80 COLUMN
875 0333 98 CBW ; MOVE DISPLAY PAGE TO LOW BYTE
876 0334 8B F8 MOV DI,AX ; CLEAR HIGH BYTE FOR BYTE OFFSET
877 0336 D1 E7 SAL AX,1 ; MOVE DISPLAY PAGE (COUNT) TO WORK REG
878 0338 8B 95 0050 R MOV DX,[DI+OFFSET *CURSOR_POSN] ; GET ROW/COLUMN OF THAT PAGE
879 033C 74 09 JZ P21 ; SKIP BUFFER ADJUSTMENT IF PAGE ZERO
880
881
882 033E 33 FF XOR DI,D1 ; ELSE SET BUFFER START ADDRESS TO ZERO
883 0340
884 0340 03 3E 004C R ADD DI,*CRT_LEN ; ADD LENGTH OF BUFFER FOR ONE PAGE
885 0344 48 DEC AX ; DECREMENT PAGE COUNT
886 0345 75 F9 JNZ P20 ; LOOP TILL PAGE COUNT EXHAUSTED
887
888
889 0347 MOV AL,BYTE PTR *CRT_COLS ; DETERMINE LOCATION IN REGEN IN PAGE
890 034A F6 E6 MUL DH ; * 2 FOR ATTRIBUTE BYTES
891 034C 32 F6 XOR DH,DH ; DETERMINE BYTES TO ROW
892 034E 03 C2 ADD AX,DX ; ADD IN COLUMN VALUE
893 0350 D1 E0 SAL AX,1 ; * 2 FOR ATTRIBUTE BYTES
894 0352 03 F8 ADD DI,AX ; ADD LOCATION TO START OF REGEN PAGE
895 0354 8B 16 0063 R MOV DX,*ADDR_6845 ; GET BASE ADDRESS OF ACTIVE DISPLAY
896 0356 83 C2 06 ADD DX,6 ; DX= STATUS PORT ADDRESS OF ADAPTER
897 0358 C3 RET ; BP= ATTRIBUTE/CHARACTER (FROM BL/AL)
898 ; DI= POSITION (OFFSET IN REGEN BUFFER)
899 ; BL= MODE FLAG (ZERO FOR 80X25 COLOR)
900 035C FIND_POSITION ENDP

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SECTION 5


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900                                     PAGE
901                                     ;-----;
902                                     ; WRITE AC CURRENT                                     ;
903                                     ; THIS ROUTINE WRITES THE ATTRIBUTE AND CHARACTER   ;
904                                     ; AT THE CURRENT CURSOR POSITION                       ;
905                                     ; INPUT                                             ;
906                                     ; (AH) = CURRENT CRT MODE                             ;
907                                     ; (BH) = DISPLAY PAGE                               ;
908                                     ; (CX) = COUNT OF CHARACTERS TO WRITE         ;
909                                     ; (AL) = CHAR TO WRITE                               ;
910                                     ; (BL) = ATTRIBUTE OF CHAR TO WRITE         ;
911                                     ; (DS) = DATA SEGMENT                               ;
912                                     ; (ES) = REGEN SEGMENT                               ;
913                                     ; OUTPUT                                             ;
914                                     ; DISPLAY REGEN BUFFER UPDATED                       ;
915                                     ;-----;
916
917 035C WRITE_AC_CURRENT PROC NEAR
918 035C      CMP     AH,4      ; IS THIS GRAPHICS
919 035F      JC     P30
920 0361      MOV     DI,0
921 0364      MOV     SI,0
922 0366      MOV     DI,0
923 0369      MOV     SI,0
924 036E      MOV     DI,0
925 036E      MOV     SI,0
926 036C      MOV     DI,0
927 036E      MOV     SI,0
928
929 0370      MOV     DI,0
930 0371      MOV     SI,0
931 0373      MOV     DI,0
932 0373      MOV     SI,0
933
934
935 0375 P31:      MOV     BP,AX      ; LOOP FOR EACH ATTR/CHAR WRITE
936 0375      MOV     BP,AX      ; PLACE ATTR/CHAR BACK IN SAVE REGISTER
937 0376      MOV     BP,AX      ; WAIT FOR HORZ RETRACE LOW OR VERTICAL
938 0376      MOV     BP,AX      ; ENABLE INTERRUPTS FIRST
939 0377      MOV     BP,AX      ; ALLOW FOR INTERRUPT WINDOW
940 0378      MOV     BP,AX      ; BLOCK INTERRUPTS FOR SINGLE LOOP
941 0379      MOV     BP,AX      ; GET STATUS FROM THE ADAPTER
942 037A      MOV     BP,AX      ; CHECK FOR VERTICAL RETRACE FIRST
943 037C      MOV     BP,AX      ; DO FAST WRITE NOW IF VERTICAL RETRACE
944 037E      MOV     BP,AX      ; IS HORIZONTAL RETRACE LOW THEN
945 0380      MOV     BP,AX      ; WAIT UNTIL IT IS
946 0382      MOV     BP,AX      ; WAIT FOR EITHER RETRACE HIGH
947 0382      MOV     BP,AX      ; GET STATUS AGAIN
948 0383      MOV     BP,AX      ; IS HORIZONTAL OR VERTICAL RETRACE HIGH
949 0385      MOV     BP,AX      ; WAIT UNTIL EITHER IS ACTIVE
950 0387
951 0387      MOV     BP,AX      ; GET THE ATTR/CHAR SAVED IN (BP)
952 0388      MOV     BP,AX      ; WRITE THE ATTRIBUTE AND CHARACTER
953 0389      MOV     BP,AX      ; AS MANY TIMES AS REQUESTED - TILL CX=0
954 038B
955 038B      MOV     BP,AX      ; EXIT
956
957 038E WRITE_AC_CURRENT ENDP
958
959                                     ;-----;
960                                     ; WRITE C CURRENT                                     ;
961                                     ; THIS ROUTINE WRITES THE CHARACTER AT           ;
962                                     ; THE CURRENT CURSOR POSITION, ATTRIBUTE UNCHANGED ;
963                                     ; INPUT                                             ;
964                                     ; (AH) = CURRENT CRT MODE                             ;
965                                     ; (BH) = DISPLAY PAGE                               ;
966                                     ; (CX) = COUNT OF CHARACTERS TO WRITE         ;
967                                     ; (AL) = CHAR TO WRITE                               ;
968                                     ; (DS) = DATA SEGMENT                               ;
969                                     ; (ES) = REGEN SEGMENT                               ;
970                                     ; OUTPUT                                             ;
971                                     ; DISPLAY REGEN BUFFER UPDATED                       ;
972                                     ;-----;
973
974 038E WRITE_C_CURRENT PROC NEAR
975 038E      CMP     AH,4      ; IS THIS GRAPHICS
976 0391      JC     P40
977 0393      MOV     DI,0
978 0396      MOV     SI,0
979 0398      MOV     DI,0
980 039B      MOV     SI,0
981 039B      MOV     DI,0
982 039B      MOV     SI,0
983
984
985
986 039E P41:      MOV     BP,AX      ; WAIT FOR HORZ RETRACE LOW OR VERTICAL
987 039E      MOV     BP,AX      ; ENABLE INTERRUPTS FIRST
988 039F      MOV     BP,AX      ; CHECK MODE FLAG FOR COLOR CARD IN 80
989 03A1      MOV     BP,AX      ; ELSE SKIP RETRACE WAIT - DO FAST WRITE
990 03A3      MOV     BP,AX      ; BLOCK INTERRUPTS FOR SINGLE LOOP
991 03A4      MOV     BP,AX      ; GET STATUS FROM THE ADAPTER
992 03A5      MOV     BP,AX      ; CHECK FOR VERTICAL RETRACE FIRST
993 03A7      MOV     BP,AX      ; DO FAST WRITE NOW IF VERTICAL RETRACE
994 03A9      MOV     BP,AX      ; IS HORIZONTAL RETRACE LOW THEN
995 03AB      MOV     BP,AX      ; WAIT UNTIL IT IS
996 03AD      MOV     BP,AX      ; WAIT FOR EITHER RETRACE HIGH
997 03AD      MOV     BP,AX      ; GET STATUS AGAIN
998 03AE      MOV     BP,AX      ; IS HORIZONTAL OR VERTICAL RETRACE HIGH
999 03B0      MOV     BP,AX      ; WAIT UNTIL EITHER RETRACE ACTIVE
1000 03B2
1001 03B2      MOV     BP,AX      ; GET THE CHARACTER SAVED IN (BP)
1002 03B4      MOV     BP,AX      ; PUT THE CHARACTER INTO REGEN BUFFER
1003 03B5      MOV     BP,AX      ; BUMP POINTER PAST ATTRIBUTE
1004 03B6      MOV     BP,AX      ; AS MANY TIMES AS REQUESTED
1005
1006 03B8      MOV     BP,AX      ; EXIT
1007
1008 03BB WRITE_C_CURRENT ENDP
    
```

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1009 PAGE
1010 ;-----
1011 ; WRITE_STRING
1012 ; THIS ROUTINE WRITES A STRING OF CHARACTERS TO THE CRT.
1013 ;
1014 ; INPUT
1015 ; (AL) = WRITE STRING COMMAND 0 - 3
1016 ; (BH) = DISPLAY PAGE (ACTIVE PAGE)
1017 ; (CX) = COUNT OF CHARACTERS TO WRITE, IF (CX) = 0 THEN RETURN
1018 ; (DX) = CURSOR POSITION FOR START OF STRING WRITE
1019 ; (BL) = ATTRIBUTE OF CHARACTER TO WRITE IF (AL) = 0 OR (AL) = 1
1020 ; (BP) = SOURCE STRING OFFSET
1021 ; (OE) = SOURCE STRING SEGMENT (FOR USE IN (ES) IN STACK +14)
1022 ;
1023 ; OUTPUT
1024 ; NONE
1025 ;-----
1026 WRITE_STRING BP PROC NEAR
1027 PUSH BP
1028 MOV BP,SP
1029 MOV ES,[BP]+14*2
1030 POP BP
1031 MOV DI,AX
1032 CMP AL,04
1033 JNB P59
1034 JCXZ E3 71
1035
1036 MOV SI,BX
1037 MOV BL,BH
1038 XOR BH,BH
1039 XCHG SI,BX
1040 SAL SI,1
1041 PUSH [SI+OFFSET @CURSOR_POSN]
1042 MOV AX,0200H
1043 INT 10H
1044
1045 MOV AL,ES:[BP]
1046 INC BP
1047
1048 ;----- TEST FOR SPECIAL CHARACTER'S
1049
1050 CMP AL,08H
1051 JE P51
1052 CMP AL,CR
1053 JE P51
1054 CMP AL,LF
1055 JE P51
1056 CMP AL,07H
1057 JNE P52
1058
1059 MOV AH,0EH
1060 INT 10H
1061 MOV DX,[SI+OFFSET @CURSOR_POSN]
1062 JMP SHORT P54
1063
1064
1065 PUSH CX
1066 PUSH BX
1067 MOV CX,1
1068 CMP DI,2
1069 JB P53
1070 MOV BL,ES:[BP]
1071 INC BP
1072
1073 MOV AH,09H
1074 INT 10H
1075 POP BX
1076 POP CX
1077 INC DL
1078 CMP DL,BYTE PTR @CRT_COLS
1079 JB P54
1080 INC DH
1081 SUB DL,DL
1082 CMP DH,25
1083 JB P54
1084
1085 MOV AX,0E0AH
1086 INT 10H
1087 DEC DH
1088
1089 MOV AX,0200H
1090 INT 10H
1091 LOOP P50
1092
1093 POP DX
1094 XCHG AX,DI
1095 TEST AL,01H
1096 JNZ P59
1097 MOV AX,0200H
1098 INT 10H
1099
1100 JMP VIDEO_RETURN
1101
1102 WRITE_STRING ENDP

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SECTION 5

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1103 PAGE
1104 -----
1105 : READ DOT -- WRITE DOT
1106 : THESE ROUTINES WILL WRITE A DOT, OR READ THE
1107 : DOT AT THE INDICATED LOCATION
1108 : ENTRY --
1109 : DX = ROW (0-199) (THE ACTUAL VALUE DEPENDS ON THE MODE)
1110 : CX = COLUMN ( 0-639) ( THE VALUES ARE NOT RANGE CHECKED )
1111 : AL = DOT VALUE TO WRITE (1,2 OR 4 BITS DEPENDING ON MODE,
1112 : REQUIRED FOR WRITE DOT ONLY, RIGHT JUSTIFIED)
1113 : BIT 1 OF AL = 1 INDICATES XOR THE VALUE INTO THE LOCATION
1114 : DS = DATA SEGMENT
1115 : ES = REGEN SEGMENT
1116 :
1117 : EXIT
1118 : AL = DOT VALUE READ, RIGHT JUSTIFIED, READ ONLY
1119 -----
1120 ASSUME DS:DATA,ES:DATA
1121 READ_DOT PROC NEAR
1122 043F CALL R3 ; DETERMINE BYTE POSITION OF DOT
1123 0442 25: 8A 04 MOV AL,ES:[SI] ; GET THE BYTE
1124 0445 25 C4 AND AL,AH ; MASK OFF THE OTHER BITS IN THE BYTE
1125 0447 D2 E0 SHL AL,CL ; LEFT JUSTIFY THE VALUE
1126 0449 8A CE MOV CL,DH ; GET NUMBER OF BITS IN RESULT
1127 044B D2 C0 ROL AL,CL ; RIGHT JUSTIFY THE RESULT
1128 044D E9 013D R JMP VIDEO_RETURN ; RETURN FROM VIDEO I/O
1129 0450 ENDP
1130
1131 0450 WRITE_DOT PROC NEAR
1132 0450 50 PUSH AX ; SAVE DOT VALUE
1133 0451 50 PUSH AX ; TWICE
1134 0452 E8 0473 R CALL R3 ; DETERMINE BYTE POSITION OF THE DOT
1135 0455 D2 E8 SHR AL,CL ; SHIFT TO SET UP THE BITS FOR OUTPUT
1136 0457 22 C4 AND AL,AH ; STRIP OFF THE OTHER BITS
1137 0459 26: 8A 0C MOV CL,ES:[SI] ; GET THE CURRENT BYTE
1138 045C 5B PDB BX ; RECOVER XOR FLAG
1139 045D F6 C3 80 TEST BL,80H ; IS IT ON
1140 0460 75 0D JNZ R2 ; YES, XOR THE DOT
1141 0462 F6 D4 NOT AH ;
1142 0464 22 CC AND CL,AH ; SET MASK TO REMOVE THE INDICATED BITS
1143 0466 0A C1 OR AL,CL ; OR IN THE NEW VALUE OF THOSE BITS
1144 0468 R1: FINISH_DOT ; FINISH DOT
1145 046B 26: 88 04 MOV AX,ES:[SI],AL ; RESTORE THE BYTE IN MEMORY
1146 046D 5B POP AX ;
1147 046C E9 013D R JMP VIDEO_RETURN ; RETURN FROM VIDEO I/O
1148 046F R2: XOR DOT ; XOR DOT
1149 046F 32 C1 XOR AL,CL ; EXCLUSIVE OR THE DOTS
1150 0471 E8 F5 JMP R1 ; FINISH UP THE WRITING
1151 0473 ENDP
1152 -----
1153 : THIS SUBROUTINE DETERMINES THE REGEN BYTE LOCATION OF THE
1154 : INDICATED ROW COLUMN VALUE IN GRAPHICS MODE.
1155 : ENTRY --
1156 : DX = ROW VALUE (0-199)
1157 : CX = COLUMN VALUE (0-639)
1158 : EXIT --
1159 : SI = OFFSET INTO REGEN BUFFER FOR BYTE OF INTEREST
1160 : AH = MASK TO STRIP OFF THE BITS OF INTEREST
1161 : CL = BITS TO SHIFT TO RIGHT JUSTIFY THE MASK IN AH
1162 : DH = # BITS IN RESULT
1163 : BX = MODIFIED
1164 -----
1165 0473 R3 PROC NEAR
1166 -----
1167 :----- DETERMINE 1ST BYTE IN INDICATED ROW BY MULTIPLYING ROW VALUE BY 40
1168 :----- ( LOW BIT OF ROW DETERMINES EVEN/ODD, 80 BYTES/ROW )
1169
1170 0473 96 XCHG SI,AX ; WILL SAVE AL AND AH DURING OPERATION
1171 0474 B0 28 MOV AL,40 ;
1172 0476 F6 E2 MUL DL ; AX= ADDRESS OF START OF INDICATED ROW
1173 0478 A8 08 TEST AL,008H ; TEST FOR EVEN/ODD ROW CALCULATED
1174 047A 74 03 JZ R4 ; JUMP IF EVEN ROW
1175 047C 05 1FDB ADD AX,2000H-40 ; OFFSET TO LOCATION OF ODD ROWS ADJUST
1176 047F EVEN_ROW ; EVEN_ROW
1177 047F 96 XCHG SI,AX ; MOVE POINTER TO (SI) AND RECOVER (AX)
1178 0480 8B D1 MOV DX,CX ; COLUMN VALUE TO DX
1179
1180 :----- DETERMINE GRAPHICS MODE CURRENTLY IN EFFECT
1181
1182 : SET UP THE REGISTERS ACCORDING TO THE MODE
1183 : CH = MASK FOR LOW OF COLUMN ADDRESS ( 7/3 FOR HIGH/MED RES )
1184 : CL = # OF ADDRESS BITS IN COLUMN VALUE ( 3/2 FOR H/M )
1185 : BL = MASK TO SELECT BITS FROM POINTED BYTE ( 1/02COH FOR H/M )
1186 : BH = NUMBER OF VALID BITS IN POINTED BYTE ( 8/2 FOR H/M )
1187
1188 0482 BB 02C0 MOV BX,2C0H ;
1189 0485 B9 0302 MOV CX,302H ; SET PARMS FOR MED RES
1190 0488 80 3E 0049 R 06 CMP @CRT_MODE,6 ;
1191 048D 72 06 JC R5 ; HANDLE IF MED RES
1192 048F BB 0180 MOV BX,180H ;
1193 0492 B9 0703 MOV CX,703H ; SET PARMS FOR HIGH RES
1194
1195 :----- DETERMINE BIT OFFSET IN BYTE FROM COLUMN MASK
1196 0495 R5: DETERMINE BIT OFFSET IN BYTE FROM COLUMN MASK
1197 0495 22 EA AND CH,DL ; ADDRESS OF PEL WITHIN BYTE TO CH
1198
1199 :----- DETERMINE BYTE OFFSET FOR THIS LOCATION IN COLUMN
1200
1201 0497 D3 EA SHR DX,CL ; SHIFT BY CORRECT AMOUNT
1202 0499 03 F2 ADD SI,DX ; INCREMENT THE POINTER
1203 049B 8A F7 MOV DH,BH ; GET THE # OF BITS IN RESULT TO DH
1204
1205 :----- MULTIPLY BH (VALID BITS IN BYTE) BY CH (BIT OFFSET)
1206
1207 049D 2A C9 SUB CL,CL ; ZERO INTO STORAGE LOCATION
1208 049F R6:
1209 049F D0 C8 ROR AL,1 ; LEFT JUSTIFY VALUE IN AL (FOR WRITE)
1210 04A1 02 CD ADD CL,CH ; ADD IN THE BIT OFFSET VALUE
1211 04A3 FE CF DEC BH ; LOOP CONTROL
1212 04A5 75 F8 JNZ R6 ; ON EXIT, CL HAS COUNT TO RESTORE BITS
1213 04A7 8A E3 MOV AH,BL ; GET MASK TO AH
1214 04A9 D2 EC SHR AH,CL ; MOVE THE MASK TO CORRECT LOCATION
1215 04AB C3 RET ; RETURN WITH EVERYTHING SET UP
1216 04AC R3 ENDP

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1217 ;-----
1218 ; SCROLL UP
1219 ; THIS ROUTINE SCROLLS UP THE INFORMATION ON THE CRT
1220 ; ENTRY --
1221 ; CH,CL = UPPER LEFT CORNER OF REGION TO SCROLL
1222 ; DH,DL = LOWER RIGHT CORNER OF REGION TO SCROLL
1223 ; BOTH OF THE ABOVE ARE IN CHARACTER POSITIONS
1224 ; BH = FILL VALUE FOR BLANKED LINES
1225 ; AL = # LINES TO SCROLL (AL=0 MEANS BLANK THE ENTIRE FIELD)
1226 ; DS = DATA SEGMENT
1227 ; ES = REGEN SEGMENT
1228 ; EXIT --
1229 ; NOTHING, THE SCREEN IS SCROLLED
1230 ;-----
1231 04AC GRAPHICS_UP PROC NEAR
1232 04AC 8A D8 MOV BL,AL ; SAVE LINE COUNT IN BL
1233 04AE 8B C1 MOV AX,CX ; GET UPPER LEFT POSITION INTO AX REG
1234
1235 ;----- USE CHARACTER SUBROUTINE FOR POSITIONING
1236 ;----- ADDRESS RETURNED IS MULTIPLIED BY 2 FROM CORRECT VALUE
1237
1238 04B0 E8 06EC R CALL GRAPH_POSN ; SAVE RESULT AS DESTINATION ADDRESS
1239 04B3 8B F8 MOV DI,AX
1240
1241 ;----- DETERMINE SIZE OF WINDOW
1242
1243 04B5 2B D1 SUB DX,CX
1244 04B7 81 C2 0101 ADD DX,101H ; ADJUST VALUES
1245 04BB D0 E6 SAL DH,1 ; MULTIPLY ROWS BY 4 AT 8 VERT DOTS/CHAR
1246 04BD D0 E6 SAL DH,1 ; AND EVEN/ODD ROWS
1247
1248 ;----- DETERMINE CRT MODE
1249
1250 04BF 80 3E 0049 R 06 CMP CRT_MODE,6 ; TEST FOR MEDIUM RES
1251 04C4 73 04 JNC RT ; FIND_SOURCE
1252
1253 ;----- MEDIUM RES UP
1254 04C6 D0 E2 SAL DL,1 ; # COLUMNS * 2, SINCE 2 BYTES/CHAR
1255 04C8 D1 E7 SAL DI,1 ; OFFSET *2 SINCE 2 BYTES/CHAR
1256
1257 ;----- DETERMINE THE SOURCE ADDRESS IN THE BUFFER
1258 04CA RT: ; FIND_SOURCE
1259 04CA 06 PUSH ES ; GET SEGMENTS BOTH POINTING TO REGEN
1260 04CB 1F POP DS
1261 04CC 2A ED SUB CH,CH ; ZERO TO HIGH OF COUNT REGISTER
1262 04CE D0 E3 SAL BL,1 ; MULTIPLY NUMBER OF LINES BY 4
1263 04D0 D0 E3 SAL BL,1
1264 04D2 74 2B JZ R11 ; IF ZERO, THEN BLANK ENTIRE FIELD
1265 04D4 B0 50 MOV AL,80 ; 80 BYTES/ROW
1266 04D6 F6 E3 MUL B,AL ; DETERMINE OFFSET TO SOURCE
1267 04D8 8B F7 MOV SI,DI ; SET UP SOURCE
1268 04DA 03 F0 ADD SI,AX ; ADD IN OFFSET TO IT
1269 04DC 8A E6 MOV AH,DH ; NUMBER OF ROWS IN FIELD
1270 04DE 2A E3 SUB AH,BL ; DETERMINE NUMBER TO MOVE
1271
1272 ;----- LOOP THROUGH, MOVING ONE ROW AT A TIME, BOTH EVEN AND ODD FIELDS
1273 04E0 R8: ; ROW_LOOP
1274 04E0 E8 0560 R CALL R17 ; MOVE ONE ROW
1275 04E3 81 EE 1FB0 SUB SI,2000H-80 ; MOVE TO NEXT ROW
1276 04E7 81 EF 1FB0 SUB DI,2000H-80 ; MOVE TO NEXT ROW
1277 04EB FE CC DEC AH ; NUMBER OF ROWS TO MOVE
1278 04ED 75 F1 JNZ R8 ; CONTINUE TILL ALL MOVED
1279
1280 ;----- FILL IN THE VACATED LINE(S)
1281 04EF R9: ; CLEAR ENTRY
1282 04EF 8A C7 MOV AL,BH ; ATTRIBUTE TO FILL WITH
1283 04F1 R10:
1284 04F1 E8 0579 R CALL R18 ; CLEAR THAT ROW
1285 04F4 81 EF 1FB0 SUB DI,2000H-80 ; POINT TO NEXT LINE
1286 04F8 FE CB DEC BL ; NUMBER OF LINES TO FILL
1287 04FA 75 F5 JNZ R10 ; CLEAR_LOOP
1288 04FC E9 013D R JMP VIDEO_RETURN ; EVERYTHING DONE
1289
1290 04FF R11: ; BLANK FIELD
1291 04FF 8A DE MOV BL,DH ; SET BLANK COUNT TO EVERYTHING IN FIELD
1292 0501 EB EC JMP R9 ; CLEAR THE FIELD
1293 0503 GRAPHICS_UP ENDP
1294
1295 ;-----
1296 ; SCROLL DOWN
1297 ; THIS ROUTINE SCROLLS DOWN THE INFORMATION ON THE CRT
1298 ; ENTRY --
1299 ; CH,CL = UPPER LEFT CORNER OF REGION TO SCROLL
1300 ; DH,DL = LOWER RIGHT CORNER OF REGION TO SCROLL
1301 ; BOTH OF THE ABOVE ARE IN CHARACTER POSITIONS
1302 ; BH = FILL VALUE FOR BLANKED LINES
1303 ; AL = # LINES TO SCROLL (AL=0 MEANS BLANK THE ENTIRE FIELD)
1304 ; DS = DATA SEGMENT
1305 ; ES = REGEN SEGMENT
1306 ; EXIT --
1307 ; NOTHING, THE SCREEN IS SCROLLED
1308 ;-----
1309 0503 GRAPHICS_DOWN PROC NEAR
1310 0503 FD STD ; SET DIRECTION
1311 0504 8A D8 MOV BL,AL ; SAVE LINE COUNT IN BL
1312 0506 8B C2 MOV AX,DX ; GET LOWER RIGHT POSITION INTO AX REG
1313
1314 ;----- USE CHARACTER SUBROUTINE FOR POSITIONING
1315 ;----- ADDRESS RETURNED IS MULTIPLIED BY 2 FROM CORRECT VALUE
1316
1317 0508 E8 06EC R CALL GRAPH_POSN ; SAVE RESULT AS DESTINATION ADDRESS
1318 050B 8B F8 MOV DI,AX
1319
1320 ;----- DETERMINE SIZE OF WINDOW
1321
1322 050D 2B D1 SUB DX,CX
1323 050F 81 C2 0101 ADD DX,101H ; ADJUST VALUES
1324 0513 D0 E6 SAL DH,1 ; MULTIPLY ROWS BY 4 AT 8 VERT DOTS/CHAR
1325 0515 D0 E6 SAL DH,1 ; AND EVEN/ODD ROWS
1326
1327 ;----- DETERMINE CRT MODE
1328
1329 0517 80 3E 0049 R 06 CMP CRT_MODE,6 ; TEST FOR MEDIUM RES
1330 051C 73 05 JNC R12 ; FIND_SOURCE_DOWN

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1331
1332 ;----- MEDIUM RES DOWN
1333 051E D0 E2 SAL DL,1 ; # COLUMNS * 2, SINCE 2 BYTES/CHAR
1334 0520 D1 E7 SAL D1,1 ; OFFSET *2 SINCE 2 BYTES/CHAR
1335 0522 47 INC D1 ; POINT TO LAST BYTE
1336
1337 ;----- DETERMINE THE SOURCE ADDRESS IN THE BUFFER
1338 0523 R12: PUSH ES ; FIND_SOURCE_DOWN
1339 0523 06 POP DS ; BOTH_SEGMENTS TO REGEN
1340 0524 1F SUB CH,CH ; ZERO TO HIGH OF COUNT REGISTER
1341 0525 2A ED ADD BL,1,240 ; POINT TO LAST ROW OF PIXELS
1342 0527 81 CT 00F0 SAL BL,1 ; MULTIPLY NUMBER OF LINES BY 4
1343 0528 D0 E3 SAL BL,1
1344 052D 00 E3 JZ R16 ; IF ZERO, THEN BLANK ENTIRE FIELD
1345 052F 74 2B MOV AH,80 ; 80 BYTES/ROW
1346 0531 B0 50 MUL BL ; DETERMINE OFFSET TO SOURCE
1347 0533 F6 E3 MOV SI,D1 ; SET UP SOURCE
1348 0535 8B F7 SUB SI,AX ; SUBTRACT THE OFFSET
1349 0537 2B F0 MOV AH,DH ; NUMBER OF ROWS IN FIELD
1350 0539 8A E6 MOV AH,BL ; DETERMINE NUMBER TO MOVE
1351 053B 2A E3
1352
1353 ;----- LOOP THROUGH, MOVING ONE ROW AT A TIME, BOTH EVEN AND ODD FIELDS
1354 053D R13: CALL R17 ; ROW LOOP DOWN
1355 053D E8 0560 R SUB SI,2000H+80 ; MOVE ONE ROW
1356 0540 81 EE 2050 SUB D1,2000H+80 ; MOVE TO NEXT ROW
1357 0544 81 EF 2050 DEC AH ; NUMBER OF ROWS TO MOVE
1358 0548 FE CC JNZ R13 ; CONTINUE TILL ALL MOVED
1359 054A 75 F1
1360
1361 ;----- FILL IN THE VACATED LINE(S)
1362 054C R14: MOV AL,BH ; CLEAR ENTRY DOWN
1363 054C 8A CT ; ATTRIBUTE TO FILL WITH
1364 054E R15: CALL R18 ; CLEAR_LOOP_DOWN
1365 054E E8 0579 R SUB D1,2000H+80 ; CLEAR A ROW
1366 0551 81 EF 2050 ; POINT TO NEXT LINE
1367 0555 FE CB DEC BL ; NUMBER OF LINES TO FILL
1368 0557 75 F5 JNZ R15 ; CLEAR_LOOP_DOWN
1369 0559 E9 013D R JMP VIDEO_RETURN ; EVERYTHING DONE
1370
1371 R16: MOV BL,DH ; BLANK FIELD DOWN
1372 055C JMP R14 ; SET BLANK COUNT TO EVERYTHING IN FIELD
1373 055C 8A DE ; CLEAR THE FIELD
1374 055E EB EC
1375 0560 GRAPHICS_DOWN ENDP
1376
1377 ;----- ROUTINE TO MOVE ONE ROW OF INFORMATION
1378
1379 0560 R17: PROC NEAR
1380 0560 8A CA MOV CL,DL ; NUMBER OF BYTES IN THE ROW
1381 0562 56 PUSH SI
1382 0563 57 PUSH D1 ; SAVE POINTERS
1383 0564 F3/ A4 REP MOVSB ; MOVE THE EVEN FIELD
1384 0566 5F POP DI
1385 0567 5E POP SI
1386 0568 81 C6 2000 ADD SI,2000H
1387 056C 81 CE 2000 ADD D1,2000H ; POINT TO THE ODD FIELD
1388 0570 56 PUSH SI
1389 0571 57 PUSH D1 ; SAVE THE POINTERS
1390 0572 8A CA MOV CL,DL ; COUNT BACK
1391 0574 F3/ A4 REP MOVSB ; MOVE THE ODD FIELD
1392 0576 5F POP DI
1393 0577 5E POP SI ; POINTERS BACK
1394 0578 C3 RET ; RETURN TO CALLER
1395 0579 R17: ENDP
1396
1397 ;----- CLEAR A SINGLE ROW
1398
1399 0579 R18: PROC NEAR
1400 0579 8A CA MOV CL,DL ; NUMBER OF BYTES IN FIELD
1401 057B 57 PUSH D1 ; SAVE POINTER
1402 057C F3/ AA REP STOSB ; STORE THE NEW VALUE
1403 057E 5F POP DI ; POINTER BACK
1404 057F 81 CT 2000 ADD D1,2000H ; POINT TO ODD FIELD
1405 0583 57 PUSH D1
1406 0584 8A CA MOV CL,DL ; FILL THE ODD FIELD
1407 0586 F3/ AA REP STOSB
1408 0588 5F POP DI ; RETURN TO CALLER
1409 0589 C3 RET
1410 058A R18: ENDP
1411
1412 ;-----
1413 ; GRAPHICS WRITE
1414 ; THIS ROUTINE WRITES THE ASCII CHARACTER TO THE CURRENT
1415 ; POSITION ON THE SCREEN.
1416 ; ENTRY --
1417 ; AL = CHARACTER TO WRITE
1418 ; BL = COLOR ATTRIBUTE TO BE USED FOR FOREGROUND COLOR
1419 ; IF BIT 7 IS SET, THE CHAR IS XOR'D INTO THE REGEN BUFFER
1420 ; (0 IS USED FOR THE BACKGROUND COLOR)
1421 ; CX = NUMBER OF CHARS TO WRITE
1422 ; DS = DATA SEGMENT
1423 ; ES = REGEN SEGMENT
1424 ; EXIT --
1425 ; NOTHING IS RETURNED
1426 ;
1427 ; GRAPHICS READ
1428 ; THIS ROUTINE READS THE ASCII CHARACTER AT THE CURRENT CURSOR
1429 ; POSITION ON THE SCREEN BY MATCHING THE DOTS ON THE SCREEN TO THE
1430 ; CHARACTER GENERATOR CODE POINTS
1431 ; ENTRY --
1432 ; NONE (0 IS ASSUMED AS THE BACKGROUND COLOR)
1433 ; AL = CHARACTER READ AT THAT POSITION (0 RETURNED IF NONE FOUND)
1434 ;
1435 ; FOR BOTH ROUTINES, THE IMAGES USED TO FORM CHARS ARE CONTAINED IN ROW
1436 ; 1 FOR THE 1ST 128 CHARS. TO ACCESS CHARS IN THE SECOND HALF, THE USER
1437 ; MUST INITIALIZE THE VECTOR AT INTERRUPT 1FH (LOCATION 007CH) TO
1438 ; POINT TO THE USER SUPPLIED TABLE OF GRAPHIC IMAGES (8X8 BOXES).
1439 ; FAILURE TO DO SO WILL CAUSE IN STRANGE RESULTS
1440 ;-----
1441 ASSUME DS:DATA,ES:DATA
1442 058A GRAPHICS_WRITE PROC NEAR
1443 058A B4 00 MOV AH,0 ; ZERO TO HIGH OF CODE POINT
1444 058C 50 PUSH AX ; SAVE CODE POINT VALUE

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1445
1446
1447
1448 058D E8 06E9 R
1449 0590 8B F8
1450
1451
1452
1453 0592 58
1454 0593 3C 80
1455 0595 73 06
1456
1457
1458
1459 0597 BE 0000 E
1460 059A 0E
1461 059B EB 18
1462
1463
1464
1465 059D
1466 059D 2C 80
1467 059F 1E
1468 05A0 2B F6
1469 05A2 8E DE
1470
1471 05A4 C5 36 007C R
1472 05A8 8C DA
1473
1474 05AA 1F
1475 05AB 52
1476 05AC 0B D6
1477 05AE 75 05
1478
1479 05B0 58
1480 05B1 BE 0000 E
1481 05B4 0E
1482
1483
1484
1485 05B5
1486 05B5 D1 E0
1487 05B7 D1 E0
1488 05B9 D1 E0
1489 05BB 03 F0
1490 05BD 80 3E 0049 R 06
1491 05C2 1F
1492 05C3 72 2C
1493
1494
1495 05C5
1496 05C5 57
1497 05C6 56
1498 05C7 B6 04
1499 05C9
1500 05C9 AC
1501 05CA F6 C3 80
1502 05CD 75 16
1503 05CF AA
1504 05D0 AC
1505 05D1
1506 05D1 26; 88 B5 IFFF
1507 05D6 B3 07 4F
1508 05D9 FE CE
1509 05DB 75 EC
1510 05DD 5E
1511 05DE 8F
1512 05DF 47
1513 05E0 E2 E3
1514 05E2 E9 013D R
1515
1516 05E5
1517 05E5 26; 32 05
1518 05E8 AA
1519 05E9 AC
1520 05EA 26; 32 B5 IFFF
1521 05EF EB E0
1522
1523
1524 05F1
1525 05F1 8A D3
1526 05F3 D1 E7
1527
1528 05F5 80 E3 03
1529 05F8 80 55
1530 05FA F6 E3
1531 05FC 8A D8
1532 05FE 8A F8
1533 0600
1534 0600 57
1535 0601 56
1536 0602 B6 04
1537 0604
1538 0604 AC
1539 0605 E8 06C0 R
1540 0608 23 C3
1541 060A 86 E0
1542 060C F6 C2 80
1543 060F 74 03
1544 0611 26; 33 05
1545 0614
1546 0614 26; 89 05
1547 0617 AC
1548 0618 E8 06C0 R
1549 061B 23 C3
1550 061D 86 E0
1551 061F F6 C2 80
1552 0622 74 05
1553 0624 26; 33 85 2000
1554 0629
1555 0629 26; 89 85 2000
1556 062E B3 C7 50
1557 0631 FE CE
1558 0633 75 CF
;----- DETERMINE POSITION IN REGEN BUFFER TO PUT CODE POINTS
CALL S26 ; FIND LOCATION IN REGEN BUFFER
MOV DI,AX ; REGEN POINTER IN DI
;----- DETERMINE REGION TO GET CODE POINTS FROM
POP AX ; RECOVER CODE POINT
CMP AL,80H ; IS IT IN SECOND HALF
JAE S1 ; YES
;----- IMAGE IS IN FIRST HALF, CONTAINED IN ROM
MOV SI,OFFSET CRT_CHAR_GEN ; OFFSET OF IMAGES
PUSH CS ; SAVE SEGMENT ON STACK
JMP SHORT S2 ; DETERMINE_MODE
;----- IMAGE IS IN SECOND HALF, IN USER MEMORY
S1:
SUB AL,80H ; EXTEND_CHAR
PUSH DS ; ZERO ORIGIN FOR SECOND HALF
SUB SI,SI ; SAVE DATA POINTER
MOV DS,SI ; ESTABLISH VECTOR ADDRESSING
ASSUME DS:ABS0
LDS SI,0EXT_PTR ; GET THE OFFSET OF THE TABLE
MOV DX,DS ; GET THE SEGMENT OF THE TABLE
ASSUME DS:DATA
POP DS ; RECOVER DATA SEGMENT
PUSH DX ; SAVE TABLE SEGMENT ON STACK
OR DX,SI ; CHECK FOR VALID TABLE DEFINED
JNZ S2 ; CONTINUE IF DS:SI NOT 0000:0000
POP AX ; ELSE SET (AX):= 0000 FOR "NULL"
MOV SI,OFFSET CRT_CHAR_GEN ; POINT TO DEFAULT TABLE OFFSET
PUSH CS ; IN THE CODE SEGMENT
;----- DETERMINE GRAPHICS MODE IN OPERATION
S2:
SAL AX,1 ; DETERMINE_MODE
SAL AX,1 ; MULTIPLY CODE POINT VALUE BY 8
SAL AX,1
ADD SI,AX
CMP CRT_MODE,6 ; SI HAS OFFSET OF DESIRED CODES
POP DS ; RECOVER TABLE POINTER SEGMENT
JNC S7 ; TEST FOR MEDIUM RESOLUTION MODE
;----- HIGH RESOLUTION MODE
S3:
PUSH DI ; HIGH_CHAR
PUSH SI ; SAVE REGEN POINTER
MOV DH,4 ; SAVE CODE POINTER
; NUMBER OF TIMES THROUGH LOOP
S4:
LODSB ; GET BYTE FROM CODE POINTS
TEST BL,80H ; SHOULD WE USE THE FUNCTION
JNZ S6 ; TO PUT CHAR IN
STOSB ; STORE IN REGEN BUFFER
LODSB
S5:
MOV ES:[DI+2000H-1],AL ; STORE IN SECOND HALF
ADD DI,79 ; MOVE TO NEXT ROW IN REGEN
DEC DH ; DONE WITH LOOP
JNZ S4
POP SI
POP DI
; RECOVER REGEN POINTER
INC DI ; POINT TO NEXT CHAR POSITION
LOOP S3 ; MORE CHARS TO WRITE
JMP VIDEO_RETURN
S6:
XOR AL,ES:[DI] ; EXCLUSIVE OR WITH CURRENT
STOSB ; STORE THE CODE POINT
LODSB ; AGAIN FOR ODD FIELD
XOR AL,ES:[DI+2000H-1]
JMP S5 ; BACK TO MAINSTREAM
;----- MEDIUM RESOLUTION WRITE
S7:
MOV DL,BL ; MED RES WRITE
SAL DI,1 ; SAVE HIGH COLOR BIT
AND BL,3 ; OFFSET*2 SINCE 2 BYTES/CHAR
MOV BL,055H ; EXPAND BL TO FULL WORD OF COLOR
MOV BL,BL ; ISOLATE THE COLOR BITS ( LOW 2 BITS )
MOV BL,AL ; GET BIT CONVERSION MULTIPLIER
MOV BL,AL ; EXPAND 2 COLOR BITS TO 4 REPLICATIONS
MOV BH,AL ; PLACE BACK IN WORK REGISTER
MOV BH,AL ; EXPAND TO 8 REPLICATIONS OF COLOR BITS
S8:
PUSH DI ; MED_CHAR
PUSH SI ; SAVE REGEN POINTER
MOV DH,4 ; SAVE THE CODE POINTER
; NUMBER OF LOOPS
LODSB ; GET CODE POINT
CALL S21 ; DOUBLE UP ALL THE BITS
AND AX,BX ; CONVERT TO FOREGROUND COLOR ( 0 BACK )
XCHG AH,AL ; SWAP HIGH/LOW BYTES FOR WORD MOVE
TEST DL,80H ; IS THIS XOR FUNCTION
JZ S10 ; NO, STORE IT IN AS IT IS
XOR AX,ES:[DI] ; DO FUNCTION WITH LOW/HIGH
S10:
MOV ES:[DI],AX ; STORE FIRST BYTE HIGH, SECOND LOW
LODSB ; GET CODE POINT
CALL S21
AND AX,BX ; CONVERT TO COLOR
XCHG AH,AL ; SWAP HIGH/LOW BYTES FOR WORD MOVE
TEST DL,80H ; AGAIN, IS THIS XOR FUNCTION
JZ S11 ; NO, JUST STORE THE VALUES
XOR AX,ES:[DI+2000H] ; FUNCTION WITH FIRST HALF LOW
S11:
MOV ES:[DI+2000H],AX ; STORE SECOND PORTION HIGH
ADD DI,80 ; POINT TO NEXT LOCATION
DEC DH
JNZ S9 ; KEEP GOING

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SECTION 5

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1559 0635 5E          POP     SI          ; RECOVER CODE POINTER
1560 0636 5F          POP     DI          ; RECOVER REGEN POINTER
1561 0637 47          INC     DI          ; POINT TO NEXT CHAR POSITION
1562 0638 47          INC     DI
1563 0639 E2 C5      LOOP   S8          ; MORE TO WRITE
1564 063B E9 013D R   JMP     VIDEO_RETURN
1565 063E          GRAPHICS_WRITE  ENDP
1566          ;-----
1567          ; GRAPHICS_READ
1568          ;-----
1569 063E          GRAPHICS_READ  PROC   NEAR
1570 063E EB 06E9 R   CALL   S26        ; CONVERTED TO OFFSET IN REGEN
1571 0641 BB F0      MOV     SI,AX      ; SAVE IN SI
1572 0643 83 EC 08   SUB     SP,BP      ; ALLOCATE SPACE FOR THE READ CODE POINT
1573 0646 BB EC      MOV     BP,SP     ; POINTER TO SAVE AREA
1574
1575          ;-----
1576          ; DETERMINE GRAPHICS MODES
1577 0648 80 3E 0049 R 06 CMP     @CRT_MODE,6
1578 064D 06        PUSH   ES
1579 064E 1F        POP    DS          ; POINT TO REGEN SEGMENT
1580 064F 72 19     JC     S13        ; MEDIUM RESOLUTION
1581
1582          ;-----
1583          ; HIGH RESOLUTION READ
1584          ;-----
1585 0651 B6 04      ; GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
1586 0653          MOV     DH,4      ; NUMBER OF PASSES
1587 0653 8A 04      S12:   MOV     AL,[SI]   ; GET FIRST BYTE
1588 0655 88 46 00  MOV     [BP],AL   ; SAVE IN STORAGE AREA
1589 0658 45        INC     BP        ; NEXT LOCATION
1590 0659 8A 84 2000 MOV     AL,[SI+2000H] ; GET LOWER REGION BYTE
1591 065D 88 46 00  MOV     [BP],AL   ; ADJUST AND STORE
1592 0660 45        INC     BP
1593 0661 83 C6 50  ADD     SI,80     ; POINTER INTO REGEN
1594 0664 FE CE     DEC     DH        ; LOOP CONTROL
1595 0666 75 EB     JNZ    S12       ; DO IT SOME MORE
1596 0668 EB 16     JMP     SHORT S15 ; GO MATCH THE SAVED CODE POINTS
1597
1598          ;-----
1599 066A          ; MEDIUM RESOLUTION READ
1600 066A D1 E6      S13:   SAL     SI,1     ; MED RES READ
1601 066C B6 04      MOV     DH,4     ; OFFSET*2 SINCE 2 BYTES/CHAR
1602 066E          ; NUMBER OF PASSES
1603 066E EB 06CF R   S14:   CALL   S23      ; GET BYTES FROM REGEN INTO SINGLE SAVE
1604 0671 81 C6 1FFE ADD     SI,2000H-2 ; GO TO LOWER REGION
1605 0675 ED 06CF R   CALL   S23      ; GET THIS PAIR INTO SAVE
1606 067B 81 EE 1FB2 DEC     SUB     SI,2000H+80+2 ; ADJUST POINTER BACK INTO UPPER
1607 067C FE CE     DEC     DH
1608 067E 75 EE     JNZ    S14      ; KEEP GOING UNTIL ALL 8 DONE
1609
1610          ;-----
1611 0680          ; SAVE AREA HAS CHARACTER IN IT, MATCH IT
1612 0680 BF 0000 E S15:   MOV     DI,OFFSET CRT_CHAR_GEN ; FIND CHAR
1613 0683 0E        PUSH   CS        ; ESTABLISH ADDRESSING
1614 0684 07        POP    ES
1615 0685 83 ED 08   SUB     BP,8     ; CODE POINTS IN CS
1616 0688 8B F5     MOV     SI,BP   ; ADJUST POINTER TO START OF SAVE AREA
1617 068A 80 00     MOV     AL,0    ; CURRENT CODE POINT BEING MATCHED
1618 068C          S16:
1619 068C 16        PUSH   SS
1620 068D 1F        POP    DS
1621 068E BA 0080   MOV     DX,128  ; ESTABLISH ADDRESSING TO STACK
1622 0691          ; FOR THE STRING COMPARE
1623 0691 56        PUSH   SI
1624 0692 57        PUSH   DI
1625 0693 B9 0004   MOV     CX,4    ; NUMBER OF WORDS TO MATCH
1626 0696 F3/ A7    REPE   CMPSW   ; COMPARE THE 8 BYTES AS WORDS
1627 0698 5F        POP     DI
1628 0699 5E        POP     SI
1629 069A 74 1E     JZ     S18      ; IF ZERO FLAG SET, THEN MATCH OCCURRED
1630 069C FE C0     INC     AL
1631 069E 93 C7 08  INC     DI,8    ; NO MATCH, MOVE ON TO NEXT
1632 06A1 4A        DEC     DX
1633 06A2 75 ED     JNZ    S17     ; NEXT CODE POINT
1634          ; LOOP CONTROL
1635          ; DO ALL OF THEM
1636          ;-----
1637 06A4 3C 00      ; CHAR NOT MATCHED, MIGHT BE IN USER SUPPLIED SECOND HALF
1638 06A6 74 12     CMP     AL,0    ; AL<= 0 IF ONLY 1ST HALF SCANNED
1639 06A8 2B C0     SUB     AX,AX   ; IF = 0, THEN ALL HAS BEEN SCANNED
1640 06AA BE D8     MOV     DS,AX   ; ESTABLISH ADDRESSING TO VECTOR
1641          ASSUME DS:ABS0
1642 06AC C4 3E 007C R LES     DI,@EXT_PTR ; GET POINTER
1643 06B0 BC C0     MOV     AX,ES   ; SEE IF THE POINTER REALLY EXISTS
1644 06B2 0B C7     OR     AX,DI    ; IF ALL 0, THEN DOESN'T EXIST
1645 06B4 74 04     JZ     S18     ; NO SENSE LOOKING
1646 06B6 80 80     MOV     AL,128 ; ORIGIN FOR SECOND HALF
1647 06B8 EB D2     JMP     S16     ; GO BACK AND TRY FOR IT
1648          ASSUME DS:DATA
1649
1650          ;-----
1651 06BA          ; CHARACTER IS FOUND (AL=0 IF NOT FOUND)
1652 06BA B3 C4 08   S18:   ADD     SP,8     ; READJUST THE STACK, THROW AWAY SAVE
1653 06BD E9 013D R   JMP     VIDEO_RETURN ; ALL DONE
1654 06C0          GRAPHICS_READ  ENDP
1655          ;-----
1656          ; EXPAND BYTE
1657          ; THIS ROUTINE TAKES THE BYTE IN AL AND DOUBLES ALL
1658          ; OF THE BITS, TURNING THE 8 BITS INTO 16 BITS.
1659          ; THE RESULT IS LEFT IN AX
1660          ;-----
1661 06C0          S21  PROC   NEAR
1662 06C0 C9        PUSH   CX
1663 06C1 B9 0008     MOV     CX,8    ; SAVE REGISTER
1664 06C4          ; SHIFT COUNT REGISTER FOR ONE BYTE
1665 06C4 D0 C8     ROR     AL,1    ; SHIFT BITS, LOW BIT INTO CARRY FLAG
1666 06C6 D1 DD     RCL     BP,1    ; MOVE CARRY FLAG (LOW BIT) INTO RESULTS
1667 06C8 D1 FD     RCR     BP,1    ; SIGN EXTEND HIGH BIT (DOUBLE IT)
1668 06CA E2 F8     LOOP   S22     ; REPEAT FOR ALL 8 BITS
1669
1670 06CC 95        XCHG   AX,BP   ; MOVE RESULTS TO PARAMETER REGISTER
1671 06CD 59        POP    CX
1672 06CE C3        RET
  
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1673 06CF          S21      ENDP
1674
1675          ; MED READ BYTE
1676          ; THIS ROUTINE WILL TAKE 2 BYTES FROM THE REGEN BUFFER,
1677          ; COMPARE AGAINST THE CURRENT FOREGROUND COLOR, AND PLACE
1678          ; THE CORRESPONDING ON/OFF BIT PATTERN INTO THE CURRENT
1679          ; POSITION IN THE SAVE AREA
1680          ; ENTRY --
1681          ; SI,DS = POINTER TO REGEN AREA OF INTEREST
1682          ; BX = EXPANDED FOREGROUND COLOR
1683          ; BP = POINTER TO SAVE AREA
1684          ; EXIT --
1685          ; SI AND BP ARE INCREMENTED
1686
1687 06CF          S23      PROC    NEAR
1688 06CF AD          LODSW   NEAR          ; GET FIRST BYTE AND SECOND BYTES
1689 06DD B6 C4          XCHG    AL,AX          ; SWAP FOR COMPARE
1690 06D2 B9 C000        MOV     CX,OC000H          ; 2 BIT MASK TO TEST THE ENTRIES
1691 06D5 B2 00          MOV     DI,0              ; RESULT REGISTER
1692 06D7
1693 06D7 85 C1          S24:    TEST    AX,CX              ; IS THIS SECTION BACKGROUND?
1694 06D9 74 01          JZ     JC2                ; IF ZERO, IT IS BACKGROUND (CARRY=0)
1695 06DB F9            STC                     ; WASN'T, SO SET CARRY
1696 06DC
1697 06DC D0 D2          S25:    RCL    DL,1            ; MOVE THAT BIT INTO THE RESULT
1698 06DE D1 E9          SHR    CX,1              ;
1699 06E0 D1 E9          SHR    CX,1              ;
1700 06E2 73 F3          JNC    S24               ; MOVE THE MASK TO THE RIGHT BY 2 BITS
1701 06E4 88 86 00        MOV     [BP],DL          ; DO IT AGAIN IF MASK DIDN'T FALL OUT
1702 06E7 45            INC     [BP],DL          ; STORE RESULT IN SAVE AREA
1703 06E8 C3            RET                     ; ADJUST POINTER
1704 06E9
1705
1706          ; V4_POSITION
1707          ; THIS ROUTINE TAKES THE CURSOR POSITION CONTAINED IN
1708          ; THE MEMORY LOCATION, AND CONVERTS IT INTO AN OFFSET
1709          ; INTO THE REGEN BUFFER, ASSUMING ONE BYTE/CHAR.
1710          ; FOR MEDIUM RESOLUTION GRAPHICS, THE NUMBER MUST
1711          ; BE DOUBLED.
1712          ; ENTRY -- NO REGISTERS, MEMORY LOCATION @CURSOR_POSN IS USED
1713          ; EXIT --
1714          ; AX CONTAINS OFFSET INTO REGEN BUFFER
1715
1716 06E9          S26      PROC    NEAR
1717 06E9 A1 0050 R      MOV     AX,@CURSOR_POSN ; GET CURRENT CURSOR
1718 06EC          GRAPH_POSN LABEL NEAR
1719 06EC 53            PUSH   BX                ; SAVE REGISTER
1720 06ED 8B 08          MOV     BX,AX            ; SAVE A COPY OF CURRENT CURSOR
1721 06EF A0 004A R      MOV     AL,BYTE PTR @CRT_COLS ; GET BYTES PER COLUMN
1722 06F2 F6 E4          MUL    AH                ; MULTIPLY BY ROWS
1723 06F4 D1 E0          SHL    AX,1              ;
1724 06F6 D1 E0          SHL    AX,1              ;
1725 06F8 2A FF          SUB    BH,BH             ; MULTIPLY * 4 SINCE 4 ROWS/BYTE
1726 06FA 03 C3          ADD    AX,BX             ; ISOLATE COLUMN VALUE
1727 06FC 5B            POP     BX                ; DETERMINE OFFSET
1728 06FD C3            RET                     ; RECOVER POINTER
1729 06FE
1730
1731          S26      ENDP
1732          ; WRITE_TTY
1733          ; THIS INTERFACE PROVIDES A TELETYPE LIKE INTERFACE TO THE
1734          ; VIDEO CARDS. THE INPUT CHARACTER IS WRITTEN TO THE CURRENT
1735          ; CURSOR POSITION, AND THE CURSOR IS MOVED TO THE NEXT POSITION.
1736          ; IF THE CURSOR LEAVES THE LAST COLUMN OF THE FIELD, THE COLUMN
1737          ; IS SET TO ZERO, AND THE ROW VALUE IS INCREMENTED. IF THE ROW
1738          ; ROW VALUE LEAVES THE FIELD, THE CURSOR IS PLACED ON THE LAST ROW,
1739          ; FIRST COLUMN, AND THE ENTIRE SCREEN IS SCROLLED UP ONE LINE.
1740          ; WHEN THE SCREEN IS SCROLLED UP, THE ATTRIBUTE FOR FILLING THE
1741          ; NEWLY BLANKED LINE IS READ FROM THE CURSOR POSITION ON THE PREVIOUS
1742          ; LINE BEFORE THE SCROLL, IN CHARACTER MODE. IN GRAPHICS MODE,
1743          ; THE 0 COLOR IS USED.
1744          ; ENTRY --
1745          ; (AH) = CURRENT CRT MODE
1746          ; (AL) = CHARACTER TO BE WRITTEN
1747          ; NOTE THAT BACK SPACE, CARRIAGE RETURN, BELL AND LINE FEED ARE
1748          ; HANDLED AS COMMANDS RATHER THAN AS DISPLAY GRAPHICS CHARACTERS
1749          ; (BL) = FOREGROUND COLOR FOR CHAR WRITE IF CURRENTLY IN A GRAPHICS MODE
1750          ; EXIT --
1751          ; ALL REGISTERS SAVED THROUGH VIDEO_EXIT (INCLUDING (AX))
1752
1753 06FE          ASSUME DS:DATA
1754 06FE 97          S27      PROC    NEAR
1755 06FF B4 03          MOV     AH,03H           ; SAVE (AX) REGISTER IN (DI) FOR EXIT
1756 0701 8A 3E 0062 R  MOV     BH,@ACTIVE_PAGE ; READ CURSOR POSITION
1757 0705 CD 10          INT    10H              ; GET CURRENT PAGE SETTING
1758 0707 8B C7          MOV     AX,DI            ; READ THE CURRENT CURSOR POSITION
1759
1760          ; RECOVER CHARACTER FROM (DI) REGISTER
1761
1762 0709 3C 0D          ;----- DX NOW HAS THE CURRENT CURSOR POSITION
1763 070B 76 46          JBE    AL,CR            ; IS IT CARRIAGE RETURN OR CONTROL
1764
1765          ; GO TO CONTROL CHECKS IF IT IS
1766 070D
1767 070D B4 0A          ;----- WRITE THE CHAR TO THE SCREEN
1768 070F B9 0001        U0:    MOV     AH,0AH           ; WRITE CHARACTER ONLY COMMAND
1769 0712 CD 10          MOV     CX,1             ; ONLY ONE CHARACTER
1770          INT    10H              ; WRITE THE CHARACTER
1771
1772          ;----- POSITION THE CURSOR FOR NEXT CHAR
1773 0714 FE C2          INC     DL                ;
1774 0716 3A 16 004A R  CMP    DL,BYTE PTR @CRT_COLS ; TEST FOR COLUMN OVERFLOW
1775 071A 75 33          JNZ    U1                ; SET CURSOR
1776 071C 92 00          MOV     DL,0             ; COLUMN FOR CURSOR
1777 071E 80 FE 18          CMP    DI,25-1          ; CHECK FOR LAST ROW
1778 0721 75 2A          JNZ    U6                ; SET_CURSOR_INC
1779
1780          ;----- SCROLL REQUIRED
1781 0723
1782 0723 B4 02          U1:    MOV     AH,02H           ;
1783 0725 CD 10          INT    10H              ; SET THE CURSOR
1784
1785          ;----- DETERMINE VALUE TO FILL WITH DURING SCROLL
1786

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SECTION 5


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1787 0727 A0 0049 R      MOV     AL,@CRT_MODE      ; GET THE CURRENT MODE
1788 072A 3C 04         CMP     AL,4              ;
1789 072C 72 06         JC     U2                 ; READ-CURSOR
1790 072E 3C 07         CJP    AL,7              ;
1791 0730 B7 00         MOV     BH,0              ; FILL WITH BACKGROUND
1792 0732 75 06         JNE    U3                 ; SCROLL-UP
1793 0734               U2:  MOV     AH,08H           ; READ-CURSOR
1794 0734 B4 08         INT     10H              ; GET READ CURSOR COMMAND
1795 0736 CD 10         INT     10H              ; READ CHAR/ATTR AT CURRENT CURSOR
1796 0738 BA FC         MOV     BH,AH            ; STORE IN BH
1797 073A               U3:  MOV     AX,0601H         ; SCROLL-UP
1798 073A BB 0601      MOV     CX,CX            ; SCROLL ONE LINE
1799 073D 2B C9         SUB     DH,25-1          ; UPPER LEFT CORNER
1800 073F B6 18         MOV     DL,25-1          ; LOWER RIGHT ROW
1801 0741 BA 16 004A R   MOV     DL,BYTE PTR @CRT_COLS ; LOWER RIGHT COLUMN
1802 0745 FE CA         DEC     DL                ;
1803 0747               U4:  INT     10H              ; VIDEO-CALL-RETURN
1804 0747 CD 10         INT     10H              ; SCROLL UP THE SCREEN
1805 0749               U5:  XCHG   AX,DI            ; TTY-RETURN
1806 0749 97           JMP     VIDEO_RETURN      ; RESTORE THE ENTRY CHARACTER FROM (DI)
1807 074A E9 013D R     JMP     U4                ; RETURN TO CALLER
1808
1809 074D               U6:  INC     DH                ; SET-CURSOR-INC
1810 074D FE C6         MOV     AH,02H          ; NEXT ROW
1811 074F           JMP     U4                ; SET-CURSOR
1812 074F B4 02         MOV     AH,02H          ; ESTABLISH THE NEW CURSOR
1813 0751 EB F4         JMP     U4                ;
1814
1815
1816 0753               U8:  JE      U9                ; CHECK FOR CONTROL CHARACTERS
1817 0753 74 13         JNE    U10               ; WAS IT A CARRIAGE RETURN
1818 0755 3C 0A         CMP     AL,LF            ; IS IT A LINE FEED
1819 0757 74 13         JNE    U10               ; GO TO LINE FEED
1820 0759 3C 07         CMP     AL,07H          ; IS IT A BELL
1821 075B 74 16         JNE    U11               ; GO TO BELL
1822 075D 3C 08         CMP     AL,08H          ; IS IT A BACKSPACE
1823 075F 75 AC         JNE    U0                ; IF NOT A CONTROL, DISPLAY IT
1824
1825
1826
1827 0761 0A D2         OR      DL,DL            ; BACK SPACE FOUND
1828 0763 74 EA         JNE    U7                ; IS IT ALREADY AT START OF LINE
1829 0765 4A           DEC     DX                ; SET CURSOR
1830 0766 EB E7         JMP     U7                ; NO -- JUST MOVE IT BACK
1831
1832
1833
1834 0768               U9:  MOV     DL,0             ; CARRIAGE RETURN FOUND
1835 0768 B2 00         JMP     U7                ; MOVE TO FIRST COLUMN
1836 076A EB E3         JMP     U7                ; SET CURSOR
1837
1838
1839
1840 076C               U10: CMP     DH,25-1          ; LINE FEED FOUND
1841 076C 80 FE 18      JNE    U6                ; BOTTOM OF SCREEN
1842 076F 75 DC         JMP     U11               ; YES, SCROLL THE SCREEN
1843 0771 EB B0         JMP     U1                ; NO, JUST SET THE CURSOR
1844
1845
1846
1847 0773               U11: MOV     CX,1331          ; BELL FOUND
1848 0773 B9 0533      MOV     BL,31            ; DIVISOR FOR 896 HZ TONE
1849 0776 B3 1F         CALL    BEEP             ; SET COUNT FOR 31/64 SECOND FOR BEEP
1850 0778 EB 0000 E     JMP     U5                ; SOUND THE POD BELL
1851 077B EB CC         JMP     U5                ; TTY_RETURN
1852 077D
1853
1854
1855
1856
1857
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1861
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1863
1864
1865
1866
1867
1868 077D 03 03 05 05 03 03 ; WRITE_TTY ENDP
1869 03 04
1870
1871
1872 0785               U1:  ASSUME DS:DATA          ; LIGHT PEN
1873 0785 B4 00         DB     3,3,5,5,3,3,4    ; THIS ROUTINE TESTS THE LIGHT PEN SWITCH AND THE LIGHT
1874 0787 BB 16 0063 R   ; PEN TRIGGER. IF BOTH ARE SET, THE LOCATION OF THE LIGHT
1875 078B B3 C2 06       ; PEN IS DETERMINED. OTHERWISE, A RETURN WITH NO INFORMATION
1876 078E EC           ; IS MADE.
1877 078F A8 04         ; ON EXIT:
1878 0791 74 03         ; (AH) = 0 IF NO LIGHT PEN INFORMATION IS AVAILABLE
1879 0793 E9 0816 R     ; (AH) = 1 IF LIGHT PEN IS AVAILABLE
1880
1881
1882
1883 0796 A8 02         ; (DH,DL) = ROW,COLUMN OF CURRENT LIGHT PEN POSITION
1884 0798 75 03         ; (CH) = RASTER POSITION
1885 079A E9 0820 R     ; (BX) = BEST GUESS AT PIXEL HORIZONTAL POSITION
1886
1887
1888
1889 079D               V1:  ASSUME DS:DATA          ; WAIT FOR LIGHT PEN TO BE DEPRESSED
1890 079D B4 10         DB     3,3,5,5,3,3,4    ;
1891
1892
1893
1894 079F BB 16 0063 R   ; READ_LPEN PROC NEAR
1895 07A3 BA C4         MOV     AH,0             ; SET NO LIGHT PEN RETURN CODE
1896 07A5 EE           MOV     DX,@ADDR_6845    ; GET BASE ADDRESS OF 6845
1897 07A6 90           ADD     DX,6             ; POINT TO STATUS REGISTER
1898 07A7 A2           IN      AL,DX            ; GET STATUS REGISTER
1899 07A8 EC           TEST   AL,004H          ; TEST LIGHT PEN SWITCH
1900 07A9 BA E8         JZ      V6_A             ; GO IF YES
1901 07AB EC           JMP     V6               ; NOT SET, RETURN
1902
1903
1904
1905 07AC A8 02         ; NOW TEST FOR LIGHT PEN TRIGGER
1906 07AC 75 03         TEST   AL,2              ; TEST LIGHT PEN TRIGGER
1907 07AE 7A 03         JNZ    V7A              ; RETURN WITHOUT RESETTING TRIGGER
1908 07B0 7A 03         JMP     V7               ;
1909
1910
1911
1912
1913
1914
1915
1916
1917
1918
1919 07B4 A8 02         ; TRIGGER HAS BEEN SET, READ THE VALUE IN
1920 07B4 7A 03         TEST   AL,2              ;
1921 07B6 7A 03         JNZ    V7A              ;
1922 07B8 7A 03         JMP     V7               ;
1923
1924
1925
1926
1927
1928
1929 07B9 A8 16         ; V7A: MOV     AH,16            ; LIGHT PEN REGISTERS ON 6845
1930 07B9 B4 10         MOV     AH,16            ;
1931
1932
1933
1934 07BD A8 16         ; INPUT REGISTERS POINTED TO BY AH, AND CONVERT TO ROW COLUMN IN (DX)
1935 07BD B4 10         MOV     AH,16            ;
1936 07BF BA C4         MOV     DX,@ADDR_6845    ; ADDRESS REGISTER FOR 6845
1937 07C1 BA C4         MOV     AL,AH            ; REGISTER TO READ
1938 07C3 EE           OUT     DX,AL            ; SET IT UP
1939 07C5 90           NOP                     ; I/O DELAY
1940 07C7 A2           INC     DX                ; DATA REGISTER
1941 07C9 EC           IN      AL,DX            ; GET THE VALUE
1942 07CB EC           MOV     CH,AL            ; SAVE IN CX

```

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1901 07AB 4A          DEC  DX          ; ADDRESS REGISTER
1902 07AC FE C4      INC  AH
1903 07AE 8A C4      MOV  AL,AH      ; SECOND DATA REGISTER
1904 07BD EE         OUT  DX,AL
1905 07B1 42         INC  DX
1906 07B2 90         NOP
1907 07B3 EC         IN   AL,DX     ; POINT TO DATA REGISTER
1908 07B4 8A E5      MOV  AH,CH     ; I/O DELAY
1909
1910
1911
1912 07B6 8A 1E 0049 R  MOV  BL,*CRT_MODE
1913 07BA 2A FF      SUB  BH,BH     ; MODE VALUE TO BX
1914 07BC 2E 3 8A 9F 07DD R  MOV  BL,CS:V1[BX] ; DETERMINE AMOUNT TO SUBTRACT
1915 07C1 2B C3      SUB  AX,BX     ; TAKE IT AWAY
1916 07C3 8B 1E 004E R  MOV  BX,*CRT_START
1917 07C7 D1 EB      SHR  BX,1
1918 07C9 2B C3      SUB  AX,BX     ; CONVERT TO CORRECT PAGE ORIGIN
1919 07CB 79 02      JNS  V2
1920 07CD 2B C0      SUB  AX,AX     ; IF POSITIVE, DETERMINE MODE
1921
1922
1923
1924 07CF             ;----- DETERMINE MODE OF OPERATION
1925 07CF B1 03      V2:          ; DETERMINE MODE
1926 07D1 80 3E 0049 R 04  MOV  CL,3     ; SET *8 SHFTT COUNT
1927 07D5 73 2A      CMP  *CRT_MODE,4 ; DETERMINE IF GRAPHICS OR ALPHA
1928 07DB 80 3E 0049 R 07  JB   V4       ; ALPHA_PEN
1929 07DD 74 23      JE   V4       ; ALPHA_PEN
1930
1931
1932
1933 07DF B2 28             ;----- GRAPHICS MODE
1934 07E1 F6 F2      MOV  DL,40    ; DIVISOR FOR GRAPHICS
1935
1936
1937
1938 07E3 8A E8             ;----- DETERMINE GRAPHIC ROW POSITION
1939 07E5 02 ED      MOV  CH,AL    ; SAVE ROW VALUE IN CH
1940 07E7 8A DC      ADD  CH,CH    ; *2 FOR EVEN/ODD FIELD
1941 07E9 2A FF      MOV  BL,AH    ; COLUMN VALUE TO BX
1942 07EB 80 3E 0049 R 06  SUB  BH,BH    ; MULTIPLY BY 8 FOR MEDIUM RES
1943 07F0 75 04      CMP  *CRT_MODE,6 ; DETERMINE MEDIUM OR HIGH RES
1944 07F2 B1 04      JNE  V3       ; NOT HIGH RES
1945 07F4 D0 E4      MOV  CL,4    ; SHIFT VALUE FOR HIGH RES
1946 07F6             SAL  AH,1     ; COLUMN VALUE TIMES 2 FOR HIGH RES
1947 07F6 03 E3      V3:          SHL  BX,CL    ; MULTIPLY *16 FOR HIGH RES
1948
1949
1950
1951 07F8 8A D4             ;----- DETERMINE ALPHA CHAR POSITION
1952 07FA 8A F0      MOV  DL,AH    ; COLUMN VALUE FOR RETURN
1953 07FC D0 EE      MOV  DH,AL    ; ROW VALUE
1954 07FE D0 EE      SHR  DH,1     ; DIVIDE BY 4
1955 0800 EB 12      JMP  SHORT V5 ; FOR VALUE IN 0-24 RANGE
1956
1957
1958
1959 0802             ;----- ALPHA MODE ON LIGHT PEN
1960 0802 F6 36 004A R  V4:          ; ALPHA_PEN
1961 0806 8A F0      DIV  DH,AL    ; DETERMINE ROW,COLUMN VALUE
1962 0808 8A D4      MOV  DL,AH    ; ROWS TO DH
1963 080A D2 E0      MOV  AL,CL    ; COLS TO DL
1964 080C 8A E8      SAL  AL,CL    ; MULTIPLY ROWS * 8
1965 080E 8A DC      MOV  CH,AL    ; GET RASTER VALUE TO RETURN REGISTER
1966 0810 32 FF      MOV  BL,AH    ; COLUMN VALUE
1967 0812 D3 E3      XOR  BH,BH    ; TO BX
1968 0814      SAL  BX,CL
1969 0814 B4 01      V5:          MOV  AH,1     ; LIGHT PEN RETURN SET
1970 0816             ; INDICATE EVERY THING SET
1971 0816 52             ; LIGHT PEN RETURN
1972 0817 5B 16 0063 R  V6:          PUSH DX       ; SAVE RETURN VALUE (IN CASE)
1973 081B 83 C2 07  ADD  DX,7     ; GET BASE ADDRESS
1974 081E EE         OUT  DX,AL    ; POINT TO RESET PARM
1975 081F 5A         POP  DX       ; ADDRESS, NOT DATA, IS IMPORTANT
1976 0820             ; RECOVER VALUE
1977 0820 5D         V7:          POP  BP       ; RETURN_NO_RESET
1978 0821 5F         POP  DI
1979 0822 5E         POP  SI
1980 0823 1F         POP  DS
1981 0824 1F         POP  DS
1982 0825 1F         POP  DS
1983 0826 1F         POP  DS
1984 0827 07         POP  ES
1985 0828 CF         IRET
1986 0829      READ_LPEN  ENDP
1987 0829      CODE      ENDS
1988
    
```

SECTION 5

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1      PAGE 118,121
2      TITLE BIOS1 ---- 01/10/86 INTERRUPT 15H BIOS ROUTINES
3      .LIST
4      0000      CODE      SEGMENT BYTE PUBLIC
5
6      PUBLIC  CASSETTE_IO_1
7
8      EXTRN  CONF_TBL:NEAR          ; SYSTEM/BIOS CONFIGURATION TABLE
9      EXTRN  DDS:NEAR              ; LOAD (DS) WITH DATA SEGMENT SELECTOR
10
11     -----
12     INT 15 H
13     INPUT - CASSETTE I/O FUNCTIONS
14     ;
15     ; (AH) = 00H
16     ; (AH) = 01H
17     ; (AH) = 02H
18     ; (AH) = 03H
19     ; RETURNS FOR THESE FUNCTIONS ALWAYS (AH) = 86H, CY = 1)
20     ; IF CASSETTE PORT NOT PRESENT
21     -----
22     INPUT - UNUSED FUNCTIONS
23     ; (AH) = 04H THROUGH 7FH
24     ; RETURNS FOR THESE FUNCTIONS ALWAYS (AH) = 86H, CY = 1)
25     ; (UNLESS INTERCEPTED BY SYSTEM HANDLERS)
26     ; NOTE: THE KEYBOARD INTERRUPT HANDLER INTERRUPTS WITH AH=4FH
27     -----
28     EXTENSIONS
29     ; (AH) = 80H  DEVICE OPEN (NULL)
30     ;           (BX) = DEVICE ID
31     ;           (CX) = PROCESS ID
32     ;
33     ; (AH) = 81H  DEVICE CLOSE (NULL)
34     ;           (BX) = DEVICE ID
35     ;           (CX) = PROCESS ID
36     ;
37     ; (AH) = 82H  PROGRAM TERMINATION (NULL)
38     ;           (BX) = DEVICE ID
39     ;
40     ; (AH) = 83H  EVENT WAIT (NULL)
41     ;
42     ; (AH) = 84H  JOYSTICK SUPPORT
43     ;           (DX) = 00H - READ THE CURRENT SWITCH SETTINGS
44     ;           ; RETURNS AL = SWITCH SETTINGS (BITS 7-4)
45     ;           (DX) = 01H - READ THE RESISTIVE INPUTS
46     ;           ; RETURNS AX = A(x) VALUE
47     ;           ;           BX = A(y) VALUE
48     ;           ;           CX = B(x) VALUE
49     ;           ;           DX = B(y) VALUE
50     ;
51     ; (AH) = 88H  EXTENDED MEMORY SIZE DETERMINE
52     ;
53     ; (AH) = 91H  INTERRUPT COMPLETE FLAG SET
54     ;           (AL) = TYPE CODE
55     ;           00H -> 7FH
56     ;           ; SERIALLY REUSABLE DEVICES
57     ;           ; OPERATING SYSTEM MUST SERIALIZE ACCESS
58     ;           80H -> BFH
59     ;           ; REENRANT DEVICES; ES:BX IS USED TO
60     ;           ; DISTINGUISH DIFFERENT CALLS (MULTIPLE I/O
61     ;           ; CALLS ARE ALLOWED SIMULTANEOUSLY)
62     ;           COH -> FFH
63     ;           ; WAIT ONLY CALLS -- THERE IS NO
64     ;           ; COMPLEMENTARY 'POST' FOR THESE WAITS.
65     ;           ; THESE ARE TIMEOUT ONLY. TIMES ARE
66     ;           ; FUNCTION NUMBER DEPENDENT.
67     ;
68     ;           TYPE DESCRIPTION          TIMEOUT
69     ;           00H = DISK                YES
70     ;           01H = DISKETTE            YES
71     ;           02H = KEYBOARD            NO
72     ;           80H = NETWORK             NO
73     ;           ; ES:BX -> NCB
74     ;           FDH = DISKETTE MOTOR START YES
75     ;           FEH = PRINTER             YES
76     ;
77     ; (AH) = C0H  RETURN CONFIGURATION PARAMETERS POINTER
78     ;           RETURNS
79     ;           (AH) = 00H AND CY= 0 (IF PRESENT ELSE 86 AND CY= 1)
80     ;           (ES:BX) = PARAMETER TABLE ADDRESS POINTER
81     ;           WHERE:
82     ;
83     ;           DW 8      LENGTH OF FOLLOWING TABLE
84     ;           DB      MODEL BYTE      SYSTEM MODEL BYTE
85     ;           DB      TYPE_BYTE      SYSTEM MODEL TYPE BYTE
86     ;           DB      BIOS_LEVEL     BIOS REVISION LEVEL
87     ;           DB      ?              10000000 = DMA CHANNEL 3 USE BY BIOS
88     ;           ;           01000000 = CASCADED INTERRUPT LEVEL 2
89     ;           ;           00100000 = REAL TIME CLOCK AVAILABLE
90     ;           ;           00010000 = KEYBOARD SCAN CODE HOOK 1AH
91     ;           DB 0      RESERVED
92     ;           DB 0      RESERVED
93     ;           DB 0      RESERVED
94     ;           DB 0      RESERVED
95     ;
96     ;
97     -----
98     ASSUME  CS:CODE
99
100    0000      CASSETTE_IO_1  PROC  FAR
101    0000 FB      STI
102    0001 80 FC 80  CMP      AH,080H
103    0004 73 06      JAE      C1_G
104
105    0006      C1:
106    0006 B4 86      MOV     AH,86H
107    0008 F9      STC
108
109    0009      C1_F:
110    0009 CA 0002  RET     2
111
112    000C      C1_G:
113    000C 80 FC C0  CMP     AH,0C0H
114    000F 74 2E      JE      CONF_PARMS
115
116    ; ENABLE INTERRUPTS
117    ; CHECK FOR RANGE OF 00-7FH
118    ; SKIP AND HANDLE, ELSE RETURN ERROR
119
120    ; ERROR
121    ; SET BAD COMMAND
122    ; SET CARRY FLAG ON (CY=1)
123
124    ; COMMON EXIT
125    ; FAR RETURN EXIT FROM ROUTINES
126
127    ; CONTINUE CHECKING FOR FUNCTION
128    ; CHECK FOR CONFIGURATION PARAMETERS

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115 0011 80 EC 80      SUB     AH,080H      ; BASE ON 0
116 0014 74 25      JZ     DEV_OPEN    ; DEVICE OPEN      (80H)
117 0016 FE CC      DEC     AH          ;
118 0018 74 21      JZ     DEV_CLOSE   ; DEVICE CLOSE     (81H)
119 001A FE CC      DEC     AH          ;
120 001C 74 1D      JZ     PROG_TERM   ; PROGRAM TERMINATION (82H)
121 001E FE CC      DEC     AH          ; IGNORE EVENT WAIT (83H)
122 0020 FE CC      DEC     AH          ;
123 0022 74 27      JZ     JOY_STICK   ; JOYSTICK BIOS    (84H)
124 0024 FE CC      DEC     AH          ;
125 0026 74 13      JZ     SYS_REQ     ; SYSTEM REQUEST KEY (85H)
126 0028 FE CC      DEC     AH          ; IGNORE WAIT       (86H)
127 002A FE CC      DEC     AH          ; IGNORE BLOCK MOVE (87H)
128 002C FE CC      DEC     AH          ;
129 002E 74 18      JZ     EXT_MEMORY  ; EXTENDED MEMORY SIZE (88H)
130
131 0030 80 EC 08     SUB     AH,8        ; CHECK FOR FUNCTION (90H)
132 0033 74 06      JZ     DEVICE_BUSY ;
133 0035 FE CC      DEC     AH          ; CHECK FOR FUNCTION (91H)
134 0037 74 05      JZ     INT_COMPLETE ; GO TO INTERRUPT COMPLETE RETURN
135 0039 EB CB      JMP     C1         ; EXIT IF NOT A VALID FUNCTION
136
137 003B             DEV_OPEN:         ; NULL HANDLERS
138 003B             DEV_CLOSE:
139 003B             PROG_TERM:
140 003B             SYS_REQ:
141 003B             DEVICE_BUSY:
142 003B F8             CLC
143 003C EB CB      JMP     C1_F      ; TURN CARRY OFF
144                                     ; RETURN WITH (AH= 00) AND CY=0
145 003E             CASSETTE_10_1 ENDP
146
147             ;--- INTERRUPT COMPLETE ---
148             ;
149             ; THIS ROUTINE IS A TEMPORARY HANDLER ;
150             ; FOR INTERRUPT COMPLETE ;
151             ;
152             ; INPUT - SEE PROLOGUE ;
153             ;
154             ;---
155 003E             INT_COMPLETE PROC NEAR
156 003E CF             IRET
157 003F             INT_COMPLETE ENDP ; RETURN
158
159 003F             CONF_PARMS PROC NEAR ; FUNCTION (C0H)
160 003F 0E             PUSH CS ; GET CODE SEGMENT
161 0040 07             POP ES ; PLACE IN SELECTOR POINTER
162 0041 BB 0000 E     MOV     BX,OFFSET CONF_TBL ; GET OFFSET OF PARAMETER TABLE
163 0044 32 E4         XOR     AH,AH ; CLEAR AH AND SET CARRY OFF
164 0046 EB C1         JMP     C1_F ; EXIT THROUGH COMMON RETURN
165 0048             CONF_PARMS ENDP
166
167             ;--- INT 15 H -- ( FUNCTION 88 H - I/O MEMORY SIZE DETERMINE ) ---
168             ; EXT_MEMORY
169             ; THIS ROUTINE RETURNS THE AMOUNT OF MEMORY IN THE SYSTEM THAT IS
170             ; LOCATED STARTING AT THE 1024K ADDRESSING RANGE, AS DETERMINED BY
171             ; THE POST ROUTINES.
172             ;
173             ; INPUT
174             ; AH = 88H
175             ;
176             ; OUTPUT
177             ; (AX) = 0
178             ;
179             ;---
180 0048             EXT_MEMORY PROC
181
182 0048 33 C0         XOR     AX,AX ; SET EXTENDED MEMORY SIZE TO ZERO
183
184 004A CF             IRET ; RETURN TO USER
185
186 004B             EXT_MEMORY ENDP
    
```

SECTION 5

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187
188
189
190
191
192
193
194
195
196
197
198
199
200
201
202
203
204 004B JOY_STICK PROC NEAR
205 004B FB STI ; INTERRUPTS BACK ON
206 004C 8B C2 MOV AX,DX ; GET SUB FUNCTION CODE
207 004E BA 0201 MOV DX,201H ; ADDRESS OF PORT
208 0051 0A C0 OR AL,AL
209 0053 74 09 JZ JOY_2 ; READ SWITCHES
210 0055 F8 C8 DEC AL ; READ RESISTIVE INPUTS
211 0057 74 0A JZ JOY_3 ; GO TO ERROR RETURN
212 0059 EB AB JMP C1
213 005B FB JOY_1: STI ; GO TO COMMON RETURN
214 005B FB JMP C1_F
215 005C EB AB JOY_2: IN AL,DX ; STRIP UNWANTED BITS OFF
216 005E EC AND AL,0F0H ; FINISHED
217 005F 24 F0 JMP JOY_1
218 0061 EB F8
219
220
221
222
223 0063 JOY_3: MOV BL,1
224 0065 E8 0081 R CALL TEST_CORD
225 0068 51 PUSH CX ; SAVE A(X) VALUE
226 0069 B3 02 MOV BL,2
227 006B E8 0081 R CALL TEST_CORD
228 006E 51 PUSH CX ; SAVE A(Y) VALUE
229 006F B3 04 MOV BL,4
230 0071 E8 0081 R CALL TEST_CORD
231 0074 51 PUSH CX ; SAVE B(X) VALUE
232 0075 B3 08 MOV BL,8
233 0077 E8 0081 R CALL TEST_CORD
234 007A 8B D1 MOV DX,CX ; SAVE B(Y) VALUE
235 007C 59 POP CX ; GET B(X) VALUE
236 007D 5B POP BX ; GET A(Y) VALUE
237 007E 58 POP AX ; GET A(X) VALUE
238 007F EB DA JMP JOY_1 ; FINISHED - RETURN
239
240 0081 TEST_CORD PROC NEAR
241 0081 52 PUSH DX ; SAVE
242 0082 FA CLJ ; BLOCK INTERRUPTS WHILE READING
243 0083 B0 00 MOV AL,0 ; SET UP TO LATCH TIMER 0
244 0085 E6 43 OUT TIMER+3,AL
245 0087 EB 00 JMP $+2
246 0089 E4 40 IN AL,TIMER ; READ LOW BYTE OF TIMER 0
247 008B EB 00 JMP $+2
248 008D 8A E0 MOV AH,AL
249 008F E4 40 IN AL,TIMER ; READ HIGH BYTE OF TIMER 0
250 0091 86 E0 XCHG AH,AL ; REARRANGE TO HIGH,LOW
251 0093 50 PUSH AX ; SAVE
252 0094 B9 04FF MOV CX,4FFH ; SET COUNT
253 0097 EE OUT DX,AL ; FIRE TIMER
254 0099 EB 00 JMP $+2
255 009A TEST_CORD_1:
256 009A EC IN AL,DX ; READ VALUES
257 009B 84 C3 TEST AL,BL ; HAS PULSE ENDED?
258 009D E0 FB LOOPNZ TEST_CORD_1
259 009F 83 F9 00 CMP CX,0
260 00A2 59 POP CX ; ORIGINAL COUNT
261 00A3 75 04 JNZ SHORT TEST_CORD_2
262 00A5 2B C9 SUB CX,CX ; SET 0 COUNT FOR RETURN
263 00A7 EB 2D JMP SHORT TEST_CORD_3 ; EXIT WITH COUNT = 0
264 00A9 TEST_CORD_2:
265 00A9 B0 00 MOV AL,0 ; SET UP TO LATCH TIMER 0
266 00AB E6 43 OUT TIMER+3,AL
267 00AD EB 00 JMP $+2
268 00AF E4 40 IN AL,TIMER ; READ LOW BYTE OF TIMER 0
269 00B1 8A E0 MOV AH,AL
270 00B3 EB 00 JMP $+2
271 00B5 E4 40 IN AL,TIMER ; READ HIGH BYTE OF TIMER 0
272 00B7 86 E0 XCHG AH,AL ; REARRANGE TO HIGH,LOW
273
274 00B9 3B C8 CMP CX,AX ; CHECK FOR COUNTER WRAP
275 00BB 73 0B JAE TEST_CORD_4 ; GO IF ND
276 00BD 52 PUSH DX
277 00BE BA FFFF MOV DX,-1
278
279 00C1 2B D0 SUB DX,AX ; ADJUST FOR WRAP
280 00C3 03 CA ADD CX,DX
281 00C5 5A POP DX
282 00C6 EB 02 JMP SHORT TEST_CORD_5
283
284 00C8 TEST_CORD_4: SUB CX,AX
285 00C8 2B C8 TEST_CORD_5: AND CX,1FF0H ; ADJUST
286 00CA MOV CX,1FFF0H
287 00CA 81 E1 IFF0
288 00CE D1 E9 SHR CX,1
289 00D0 D1 E9 SHR CX,1
290 00D2 D1 E9 SHR CX,1
291 00D4 D1 E9 SHR CX,1
292
293 00D6 TEST_CORD_3: STI ; INTERRUPTS BACK ON
294 00D6 FB MOV DX,201H ; FLUSH OTHER INPUTS
295 00D7 BA 0201 PUSH CX
296 00DA 51 PUSH CX
297 00DB 50 PUSH AX
298 00DC B9 04FF MOV CX,4FFH ; COUNT
299 00DF TEST_CORD_6:
300 00DF EC IN AL,DX

```

```
301 00E0 A8 0F          TEST    AL,0FH
302 00E2 E0 FB          LOOPNZ TEST_CORD_6
303
304 00E4 58             POP     AX
305 00E5 59             POP     CX
306 00E6 5A             POP     DX          ; SET COUNT
307
308 00E7 C3             RET              ; RETURN
309
310 00E8             TEST_CORD      ENDP
311 00E8             JOY_STICK     ENDP
312
313 00E8             CODE      ENDS
314                END
```

SECTION 5

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1          PAGE 118,121
2          TITLE POST ----- 01/10/86 SYSTEM POST AND BIOS PROCEDURES
3
4          PUBLIC A1
5          PUBLIC BEEP
6          PUBLIC CONF_TBL
7          PUBLIC CRT_CHAR_GEN
8          PUBLIC DDS
9          PUBLIC DISK_BASE
10         PUBLIC M5
11         PUBLIC M6
12         PUBLIC M7
13         PUBLIC MD_TBL1
14         PUBLIC MD_TBL2
15         PUBLIC MD_TBL3
16         PUBLIC MD_TBL4
17         PUBLIC MD_TBL5
18         PUBLIC MD_TBL6
19         PUBLIC P_D_R
20         PUBLIC RESET
21         PUBLIC VIDEO_PARAMS
22         PUBLIC WAITF
23
24         EXTRN CASSETTE_IO_1:NEAR
25         EXTRN DISKETTE_IO_1:NEAR
26         EXTRN DISK_INT_1:NEAR
27         EXTRN DSKETTE_SETUP:NEAR
28         EXTRN KB_INT_1:NEAR
29         EXTRN KEYBOARD_IO_1:NEAR
30         EXTRN NEC_OUTPUT:NEAR
31         EXTRN PRINTER_IO_1:NEAR
32         EXTRN RESULTS:NEAR
33         EXTRN RS232_IO_1:NEAR
34         EXTRN SEEK:NEAR
35         EXTRN VIDEO_IO_1:NEAR
36
37         EXTRN SET_MODE:NEAR
38         EXTRN SET_CTYPE:NEAR
39         EXTRN SET_CPOS:NEAR
40         EXTRN READ_CURSOR:NEAR
41         EXTRN READ_LPN:NEAR
42         EXTRN ACT_DISP_PAGE:NEAR
43         EXTRN SCROLL_UP:NEAR
44         EXTRN SCROLL_DOWN:NEAR
45         EXTRN READ_AC_CURRENT:NEAR
46         EXTRN WRITE_AC_CURRENT:NEAR
47         EXTRN WRITE_C_CURRENT:NEAR
48         EXTRN SET_COLOR:NEAR
49         EXTRN WRITE_DOT:NEAR
50         EXTRN READ_DOT:NEAR
51         EXTRN WRITE_TTY:NEAR
52         EXTRN VIDEO_STATE:NEAR
53         .LIST
54
55         ;-----
56         ; THE BIOS ROUTINES ARE MEANT TO BE ACCESSED THROUGH INTERRUPT 3
57         ; SOFTWARE INTERRUPTS ONLY. ANY ADDRESSES PRESENT IN THIS LISTING
58         ; ARE INCLUDED ONLY FOR COMPLETENESS, NOT FOR REFERENCE. APPLICATIONS WHICH
59         ; REFERENCE ABSOLUTE ADDRESSES WITHIN THE CODE SEGMENT VIOLATE THE
60         ; STRUCTURE AND DESIGN OF BIOS.
61         ;-----
62         ;
63         ; ROM RESIDENT CODE
64         ;-----
65         CODE SEGMENT BYTE PUBLIC
66
67         0000 1FFF [ CC ] DB 01FFFF DUP (0CCH) ; FILL UNUSED LOCATIONS WITH INTERRUPT 3
68
69
70
71         ;
72         0000 ORG 0E000H
73         0000 36 32 58 30 38 35 ORG 0
74         31 20 43 4F 50 52 DB '62X0851 COPR. IBM 1986' ; COPYRIGHT NOTICE
75         2E 20 49 42 4D 20
76         31 39 38 36
77
78         ;-----
79         ; INITIAL RELIABILITY TESTS -- PHASE I
80         ;-----
81
82         ASSUME CS:CODE,SS:CODE,ES:ABS0,DS:DATA
83
84         0016 00D5 R C1 DW C11 ; RETURN ADDRESS
85         0018 0181 R C2 DW C24 ; RETURN ADDRESS FOR DUMMY STACK
86         001A 20 4B 42 20 4F 4B F3B DB ' KB OK ',CR ; KB FOR MEMORY SIZE
87         0D
88
89         ;-----
90         ; LOAD A BLOCK OF TEST CODE THROUGH THE KEYBOARD PORT
91         ; FOR MANUFACTURING TEST.
92         ; THIS ROUTINE WILL LOAD A TEST (MAX LENGTH=FAFFH) THROUGH
93         ; THE KEYBOARD PORT. CODE WILL BE LOADED AT LOCATION
94         ; 0000:0500. AFTER LOADING, CONTROL WILL BE TRANSFERRED
95         ; TO LOCATION 0000:0500. STACK WILL BE LOCATED JUST BELOW
96         ; THE TEST CODE. THIS ROUTINE ASSUMES THAT THE FIRST 2
97         ; BYTES TRANSFERRED CONTAIN THE COUNT OF BYTES TO BE LOADED
98         ; (BYTE 1=COUNT LOW, BYTE 2=COUNT HI.)
99         ;-----
100        ;----- FIRST, GET THE COUNT
101
102        MFG_BOOT:
103        CALL SP_TEST ; GET COUNT LOW
104        MOV BH,BL ; SAVE IT
105        CALL SP_TEST ; GET COUNT HI
106        MOV CH,BL
107        MOV CL,BH ; CX NOW HAS COUNT
108        CLD ; SET DIR. FLAG TO INCREMENT
109        CLI
110        MOV DI,0500H ; SET TARGET OFFSET (DS=0000)
111        MOV AL,0FDH ; UNMASK K/B INTERRUPT
112        OUT INTA01,AL ; UNMASK K/B INTERRUPT
113        MOV AL,0AH ; SEND READ INT. REQUEST REG. CMD
114        OUT INTA00,AL
115        MOV DX,PORT_B ; SET UP PORT B ADDRESS

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452 021B 90      NOP
453 021C E4 62  IN      AL,PORT_C
454 021E B1 04  MOV     CL,4
455 0220 D2 C0  ROL     AL,CL          ; ROTATE TO HIGH NIBBLE
456 0222 24 F0  AND     AL,11110000B    ; ISOLATE
457 0224 0A C4  OR      AL,AX          ; COMBINE WITH OTHER BANK
458 0226 2A E4  SUB     AH,AH
459 0228 A3 0410 R  MOV     DATA_WORD[0EQUIP_FLAG-DATA40],AX  ; SAVE SWITCH INFO
460 022B B0 99  MOV     AL,99H
461 022D E6 63  OUT     CMD_PORT,AL
462 022F E8 19E3 R  CALL   KBD_RESET      ; SEE IF MFG. JUMPER IN
463 0232 80 FB EA  CMP     BL,0EAH        ; IS THIS THE EXTENDED KEYBOARD?
464 0235 75 08  JNE     KBX1          ; IF NOT THEN LEAVE THE FLAG ALONE
465 0237 C8 06 0496 R 10 MOV     DATA_AREA[0KB_FLAG_3-DATA40],KBX1 ; EXTENDED KEYBOARD
466 023C EB 22 90  JMP     E6             ; DONE WITH KEYBOARD HERE
467 023F
468 023F 80 FB AA  KBX1:  CMP     BL,0AAH        ; KEYBOARD PRESENT?
469 0242 74 1C  JE      E6             ; NO
470 0244 80 FB 65  CMP     BL,065H        ; LOAD MFG. TEST REQUEST?
471 0247 75 03  JNE     D3B           ; GO TO BOOTSTRAP IF SO
472 0249 E9 0021 R  JMP     MFG_BOOT
473 024C
474 024C 0A DB  D3B:  OR      BL,BL          ; MFG PLUG IN?
475 024E 75 10  JNZ     E6             ; NO
476 0250 B0 38  MOV     MOV     AL,38H
477 0252 E6 61  OUT     PORT_B,AL
478 0254 90  NOP
479 0255 90  NOP
480 0256 E4 60  IN      AL,PORT_A
481 0258 24 FF  AND     AL,0FFH        ; WAS DATA LINE GROUNDED
482 025A 75 04  JNZ     E6
483 025C FE 06 0412 R  INC     DATA_AREA[0MFG_TST-DATA40]  ; SET MANUFACTURING TEST FLAG
484
485
486 -----
487 ; INITIALIZE AND START CRT CONTROLLER (6845) ;
488 ; TEST VIDEO READ/WRITE STORAGE. ;
489 ; DESCRIPTION ;
490 ; RESET THE VIDEO ENABLE SIGNAL. ;
491 ; SELECT ALPHANUMERIC MODE, 40 * 25, B & W. ;
492 ; READ/WRITE DATA PATTERNS TO STG. CHECK STG ;
493 ; ADDRESSABILITY. ;
494 ; ERROR = 1 LONG AND 2 SHORT BEEPS ;
495 -----
496 E6:
497 MOV     AX,DATA_WORD[0EQUIP_FLAG-DATA40]  ; GET SENSE SWITCH INFO
498 PUSH   AX
499 AL,30H ; SAVE IT
500 MOV     DATA_WORD[0EQUIP_FLAG-DATA40],AX
501 SUB     AH,AH
502 INT    10H          ; SEND INIT TO B/W CARD
503 MOV     AL,20H
504 DATA_WORD[0EQUIP_FLAG-DATA40],AX
505 SUB     AH,AH        ; AND INIT COLOR CARD
506 INT    10H
507 POP     AX          ; RECOVER REAL SWITCH INFO
508 MOV     DATA_WORD[0EQUIP_FLAG-DATA40],AX ; RESTORE IT
509 AND     AL,30H      ; AND CONTINUE
510 JNZ    E7           ; ISOLATE VIDEO SWS
511 DI,OFFSET 0VIDEO_INT ; SET INT 10H TO DUMMY
512 MOV     WORD_PTR [DI],OFFSET DUMMY_RETURN ; SET IF NO VIDEO CARD
513 JMP     E1B_1
514
515 E7:
516 CMP     AL,30H      ; B/W CARD ATTACHED?
517 JNC     EB          ; YES - SET MODE FOR B/W CARD
518 INC     AH
519 CMP     AL,20H      ; 80X25 MODE SELECTED?
520 JNE     EB          ; NO - SET MODE FOR 40X25
521 MOV     AH,3
522 XCHG   AH,AL        ; SET MODE FOR 80X25
523 PUSH   AX
524 SUB     AH,AH        ; SAVE VIDEO MODE ON STACK
525 INT    10H          ; INITIALIZE TO ALPHANUMERIC MD
526 INT    10H          ; CALL VIDEO IO
527 POP     AX          ; RESTORE VIDEO SENSE SWS IN AH
528 PUSH   AX
529 MOV     BX,0B000H   ; RESAVE VALUE
530 JMP     BX,0B000H   ; BEG VIDEO RAM ADDR B/W CD
531 SHORT E8A
532
533 ;----- UNNATURAL ACT FOR ADDRESS COMPATIBILITY
534
535 ; ORG 0E2C3H
536 ; ORG 002C3H
537 NMI_INT: JMP NMI_INT_1
538
539 E8A:
540 MOV     DX,3B8H     ; MODE REG FOR B/W
541 MOV     CX,2048H    ; RAM WORD CNT FOR B/W CD
542 MOV     AL,1
543 CMP     AH,30H      ; SET MODE FOR B/W CARD
544 JNE     E9          ; B/W VIDEO CARD ATTACHED?
545 MOV     BH,0BBH     ; YES - GO TEST VIDEO STG
546 MOV     DX,3DBH     ; SET VIDEO RAM ADDR COLOR CD
547 MOV     CH,20H      ; MODE REG FOR COLOR CD
548 DEC     AL          ; RAM WORD CNT FOR COLOR CD
549 SET     MODE_1      ; SET MODE TO 0 FOR COLOR CD
550 TEST   VIDEO_STG1  ; TEST VIDEO STG1
551 OUT     DX,AL        ; DISABLE VIDEO FOR COLOR CD
552 CMP     DATA_WORD[0RESET_FLAG-DATA40],1234H ; POD INIT BY KBD RESET?
553 MOV     ES,BX
554 MOV     E10
555 JNE     E10         ; POINT ES TO VIDEO RAM STG
556 MOV     DS,BX
557 ASSUME DS:NOTHING,ES:NOTHING ; POINT DS TO VIDEO RAM STG
558 CALL   STGTEST_CNT ; GO TEST VIDEO R/W STG
559 JNE     E17         ; R/W STG FAILURE - BEEP SPK
560
561 ;-----
562 ; SETUP VIDEO DATA ON SCREEN FOR VIDEO ;
563 ; LINE TEST. ;
564 ; DESCRIPTION ;
565 ; ENABLE VIDEO SIGNAL AND SET MODE. ;
566 ; DISPLAY A HORIZONTAL BAR ON SCREEN. ;
567 -----
568 E10:
569 POP     AX
570 PUSH   AX          ; GET VIDEO SENSE SWS (AH)
571 ; SAVE IT

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566 02F0 B4 00      MOV     AH,0           ; ENABLE VIDEO AND SET MODE
567 02F2 CD 10      INT     10H           ; VIDEO
568 02F4 B8 7020    MOV     AX,7020H      ; WRT BLANKS IN REVERSE VIDEO
569
570 02F7 2B FF      SUB     DI,D1         ; SETUP STARTING LOC
571 02F9 B9 0028    MOV     CX,40         ; NO. OF BLANKS TO DISPLAY
572 02FC F3/ AB     REP     STOSW         ; WRITE VIDEO STORAGE
573
574 ;-----
575 ; CRT INTERFACE LINES TEST
576 ; DESCRIPTION
577 ; SENSE ON/OFF TRANSITION OF THE
578 ; VIDEO ENABLE AND HORIZONTAL
579 ; SYNC LINES.
580 02FE 58          POP     AX             ; GET VIDEO SENSE SW INFO
581 02FF 50          PUSH    AX            ; SAVE IT
582 0300 80 FC 30   CMP     AH,30H        ; B/W CARD ATTACHED?
583 0303 BA 03BA    MOV     DX,03BAH     ; SETUP ADDR OF BW STATUS PORT
584 0306 74 03     JE     E1             ; YES - GO TEST LINES
585 0308 BA 03DA    MOV     DX,03DAH     ; COLOR CARD IS ATTACHED
586 030B           ; LINE_TST:
587 030B B4 08     MOV     AH,8         ; OFLOOP_CNT:
588 030D           ;
589 030D 2B C9     SUB     CX,CX         ; OFLOOP_CNT:
590 030F           ;
591 030F EC     E1:    IN     AL,DX         ; READ CRT STATUS PORT
592 0310 22 C4     AND    AL,AH         ; CHECK VIDEO/HORZ LINE
593 0312 75 04     JNZ   E14           ; ITS ON - CHECK IF IT GOES OFF
594 0314 E2 F9     LOOP  E13           ; LOOP TILL ON OR TIMEOUT
595 0316 EB 09     JMP    SHORT E17     ; GO PRINT ERROR MSG
596 0318           ;
597 0318 2B C9     E14:   SUB     CX,CX         ;
598 031A           ;
599 031A EC     E15:   IN     AL,DX         ; READ CRT STATUS PORT
600 031B 22 C4     AND    AL,AH         ; CHECK VIDEO/HORZ LINE
601 031D 74 11     JZ     E16           ; ITS ON - CHECK NEXT LINE
602 031F E2 F9     LOOP  E15           ; LOOP IF OFF TILL IT GOES ON
603 0321           ; CRT_ERR:
604 0321 IF     POP     DS           ;
605 0322 1E       PUSH    DS           ;
606 0323 C6 06 0015 R 06 MOV     DS:#MFG_ERR_FLAG,06H ; <<-><->CRT ERR CHKPT. 06<-><->
607 0328 BA 0102    MOV     DX,0102H    ;
608 032B E8 19A5 R CALL    ERR_BEEP     ; GO BEEP SPEAKER
609 032E EB 06     JMP    SHORT E18     ;
610 0330           ;
611 0330 B1 03     E16:   MOV     CL,3         ; NXT_LINE:
612 0332 D2 EC     SHR    AH,CL         ; GET NEXT BIT TO CHECK
613 0334 75 D7     JNZ   E12           ;
614 0336           ;
615 0336 58           ;
616 0337 B4 00     E18:   POP     AX           ; DISPLAY CURSOR:
617 0339 CD 10     MOV     AH,0         ; GET VIDEO SENSE SWS (AH)
618 033B           INT     10H         ; SET MODE AND DISPLAY CURSOR
619 033B BA C000    MOV     DX,0C000H   ; CALL VIDEO I/O PROCEDURE
620 033E           ; SEE IF ADVANCED VIDEO CARD
621 033E 8E DA     E18A: MOV     DS,DX        ; IS PRESENT
622 0340 2B DB     SUB     BX,DX        ;
623 0342 8B 07     MOV     AX,[BX]     ; GET FIRST 2 LOCATIONS
624 0344 53       PUSH    BX           ;
625 0345 5B       POP     BX           ; LET BUS SETTLE
626 0346 3D AA55   CMP     AX,0AA55H   ; PRESENT:
627 0349 75 05     JNZ   E18B          ; NOT GO LOOK FOR OTHER MODULES
628 034B E8 1920 R CALL    ROM_CHECK    ; GO SCAN MODULE
629 034E EB 04     JMP    SHORT E18C   ;
630 0350           ;
631 0350 81 C2 0080 ADD     DX,0080H    ; POINT TO NEXT 2K BLOCK
632 0354           ;
633 0354 81 FA C800 E18C: CMP     DX,0C800H   ; TOP OF VIDEO ROM AREA YET?
634 0358 7C E4     JZ     E18A          ; GO SCAN FOR ANOTHER MODULE
635
636 ;-----
637 ; 8259 INTERRUPT CONTROLLER TEST
638 ; DESCRIPTION
639 ; READ/WRITE THE INTERRUPT MASK REGISTER (IMR)
640 ; WITH ALL ONES AND ZEROS. ENABLE SYSTEM
641 ; INTERRUPTS. MASK DEVICE INTERRUPTS OFF. CHECK
642 ; FOR HOT INTERRUPTS (UNEXPECTED).
643 ;-----
644 C2:    POP     DS:ABS0
645
646 ;---- TEST THE IMR REGISTER
647
648 C21A: MOV     DATA_AREA[0*IMR_FLAG-DATA40],05H ; <-><->CHECKPOINT 5<-><->
649 ; <-><->SET IMR TO ZERO
650
651 MOV     AL,0
652 OUT    INTA01,AL ; READ IMR
653 IN     AL,INTA01 ; IMR = 0?
654 OR     AL,AL ; IMR = 0?
655 JNZ   D6 ; GO TO ERR ROUTINE IF NOT 0
656 MOV     AL,0FFH ; DISABLE DEVICE INTERRUPTS
657 OUT    INTA01,AL ; WRITE TO IMR
658 IN     AL,INTA01 ; READ IMR
659 AND    AL,1 ; ALL IMR BIT ON?
660 JNZ   D6 ; NO - GO TO ERR ROUTINE
661
662 ;---- CHECK FOR HOT INTERRUPTS
663
664 ;---- INTERRUPTS ARE MASKED OFF. CHECK THAT NO INTERRUPTS OCCUR.
665
666 0374 A2 046B R MOV     DATA_AREA[0*INTR_FLAG-DATA40],AL ; CLEAR INTERRUPT FLAG
667 0377 FB         STI     ; ENABLE EXTERNAL INTERRUPTS
668 0378 2B C9     SUB     CX,CX        ; WAIT 1 SEC FOR ANY INTRs THAT
669 037A           ; MIGHT OCCUR
670 037A E2 FE     D4:    LOOP  D4
671 037C           ;
672 037C E2 FE     D5:    LOOP  D5
673 037E 80 3E 046B R 00 CMP     DATA_AREA[0*INTR_FLAG-DATA40],00H ; ANY INTERRUPTS OCCUR?
674 0383 74 08     JZ     D7            ; NO - GO TO NEXT TEST
675 0385           ;
676 0385 BE 18CC R MOV     SI,OFFSET E0 ; DISPLAY 101 ERROR
677 0388 E8 1976 R CALL    E_MSG
678 038B FA         CLI     ;
679 038C F4         HLT     ; HALT THE SYSTEM

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SECTION 5


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794 0448 B0 FE          MOV    AL,OFEH          ; ENABLE TIMER INTERRUPT
795 044A E6 21          OUT    INTA01,AL
796
797 -----
798 ; EXPANSION I/O BOX TEST
799 ; CHECK TO SEE IF EXPANSION BOX PRESENT - IF INSTALLED,
800 ; TEST DATA AND ADDRESS BUSES TO I/O BOX
801 ; ERROR='1801'
802 -----
803 ;----- DETERMINE IF BOX IS PRESENT
804
805 044C          EXP_10:          ; (CARD WAS ENABLED EARLIER)
806 044C BA 0210        MOV    DX,0210H        ; CONTROL PORT ADDRESS
807 044F BB 5555        MOV    AX,5555H        ; SET DATA PATTERN
808 0452 EE            OUT    DX,AL           ;
809 0453 B0 01         MOV    AL,01H          ; MAKE AL DIFFERENT
810 0455 EC            IN     AL,DX           ; RECOVER DATA
811 0456 3A C4         CMP    AL,AH           ; REPLY?
812 0458 75 43         JNE   E19             ; NO RESPONSE, GO TO NEXT TEST
813 045A F7 D0         NOT   AX              ; MAKE DATA=AAAA
814 045C EE            OUT    DX,AL           ;
815 045D B0 01         MOV    AL,01H          ;
816 045F EC            IN     AL,DX           ; RECOVER DATA
817 0460 3A C4         CMP    AL,AH           ;
818 0462 75 39         JNE   E19             ;
819
820 ;----- CHECK ADDRESS BUS
821
822 0464          EXP2:          ;
823 0464 BB 0001        MOV    BX,0001H        ; LOAD HI ADDR. REG ADDRESS
824 0467 BA 0215        MOV    CX,0016         ; GO ACROSS 16 BITS
825 046A B9 0010
826 046D          EXP3:          ;
827 046D 2E: 88 07     MOV    CS:[BX],AL      ; WRITE ADDRESS F0000+BX
828 0470 90            NOP
829 0471 EC            IN     AL,DX           ; READ ADDR. HIGH
830 0472 3A C7         CMP    AL,BH           ;
831 0474 75 21         JNE   EXP_ERR         ; GO ERROR IF MISCOMPARE
832 0476 42           INC    DX              ; DX=216H (ADDR. LOW REG)
833 0477 EC            IN     AL,DX           ;
834 0478 3A C3         CMP    AL,BL           ; COMPARE TO LOW ADDRESS
835 047A 75 1B         JNE   EXP_ERR         ;
836 047C 4A           DEC    DX              ; DX BACK TO 215H
837 047D D1 E3         SHL   BX,1            ;
838 047F E2 EC         LOOP  EXP3           ; LOOP TILL '1' WALKS ACROSS BX
839
840 ;----- CHECK DATA BUS
841
842 0481 B9 0008        MOV    CX,0008         ; DO 8 TIMES
843 0484 B0 01         MOV    AL,01           ;
844 0486 4A           DEC    DX              ; MAKE DX=214H (DATA BUS REG)
845 0487          EXP4:          ;
846 0487 BA E0         MOV    AH,AL           ; SEND DATA BUS VALUE
847 0489 EE            OUT    DX,AL           ; SEND VALUE TO REG
848 048A B0 01         MOV    AL,01H          ;
849 048C EC            IN     AL,DX           ; RETRIEVE VALUE FROM REG
850 048D 3A C4         CMP    AL,AH           ; = TO SAVED VALUE
851 048F 75 06         JNE   SHORT EXP_ERR   ;
852 0491 DD E0         SHL   AL,1            ; FORM NEW DATA PATTERN
853 0493 E2 F2         LOOP  EXP4           ; LOOP TILL BIT WALKS ACROSS AL
854 0495 EB 06         JMP   SHORT E19       ; GO ON TO NEXT TEST
855 0497          EXP_ERR:        ;
856 0497 BE 18DC R     MOV    SI,OFFSET F3C  ;
857 049A EB 1976 R     CALL  E_MSG           ;
858
859 ;-----
860 ; ADDITIONAL READ/WRITE STORAGE TEST
861 ; DESCRIPTION
862 ; WRITE/READ DATA PATTERNS TO ANY READ/WRITE
863 ; STORAGE AFTER THE FIRST 64K. STORAGE
864 ; ADDRESSABILITY IS CHECKED.
865 -----
866 049D          E19:          ASSUME DS:DATA
867 049D EB 1A12 R     CALL  DDS             ;
868 04A0 IE           PUSH  DS              ;
869 04A1          E20:          ;
870 04A1 81 3E 0072 R 1234 CMP    0RESET_FLAG,1234H ; WARM START?
871 04A7 75 03         JNE   E20A           ; CONTINUE TEST IF NOT
872 04A9 E9 054A R     JMP   ROM_SCAN       ; GO TO NEXT ROUTINE IF SO
873 04AC          E20A:        ;
874 04AC BB 0040        MOV    AX,64           ; STARTING AMT. OF MEMORY OK
875 04AF EB 28         JMP   SHORT PRT_SIZ  ; POST MESSAGE
876 04B1          E20B:        ;
877 04B1 BB 1E 0013 R  MOV    BX,0MEMORY_SIZE ; GET MEM. SIZE WORD
878 04B5 83 EB 40        SUB   BX,64           ; 15T 64K ALREADY DONE
879 04B8 B1 04         SBB  CL,4            ;
880 04BA 03 EB         SHR   BX,CL           ; DIVIDE BY 16
881 04BC BB CB         MOV    CX,BX          ; SAVE COUNT OF 16K BLOCKS
882 04BE BB 1000        MOV    BX,1000H       ; SET PTR. TO RAM SEGMENT>64K
883 04C1          E21:          ;
884 04C1 BE D0         MOV    DS,BX          ; SET SEG. REG
885 04C3 BE C3         MOV    ES,BX          ;
886 04C5 81 C3 0400    ADD   BX,0400H        ; POINT TO NEXT 16K
887 04C9 52           PUSH  DX              ;
888 04CA 51           PUSH  CX              ;
889 04CB 53           PUSH  BX              ; SAVE WORK REGS
890 04CC 50           PUSH  AX              ;
891 04CD B9 2000        MOV    CX,02000H     ;
892 04DD EB 0CCF R     CALL  STGTST_CNT     ; SET COUNT FOR 8K WORDS
893 04D3 75 4C         JNZ   E21A           ; GO PRINT ERROR
894 04D5 58           POP   AX              ; RECOVER TESTED MEM NUMBER
895 04D6 05 0010        ADD   AX,16           ;
896 04D9          PRT_SIZ:        ;
897 04D9 50           PUSH  AX              ;
898 04DA BB 000A        MOV    BX,10          ; SET UP FOR DECIMAL CONVERT
899 04DD B9 0003        MOV    CX,3           ; OF 3 NIBBLES
900 04E0          DECIMAL_LOOP:      ;
901 04E0 33 D2         XOR   DX,DX           ;
902 04E2 77 F3         OR    BX,BX           ; DIVIDE BY 10
903 04E4 80 CA 30        OR    DL,30H          ; MAKE INTO ASC11
904 04E7 52           PUSH  DX              ;
905 04E8 EB F6         LOOP  DECIMAL_LOOP   ;
906 04EA B9 0003        MOV    CX,3           ;
907 04ED          PRT_DEC_LOOP:      ;

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1022 05A3 E2 FE          LOOP      F11          ; WAIT FOR 1 SECOND
1023 05A5                F12:    LOOP      F12          ; MOTOR_WAIT1:
1024 05A5 E2 FE          XOR       DX,DX        ; SELECT DRIVE 0
1025 05A7 33 D2        MOV      CH,34        ; SELECT TRACK 34
1026 05A9 B5 22        MOV      MOV      @SEEK_STATUS,DL
1027 05AB B8 16 003E R CALL      SEEK
1028 05AF EB 0000 E     JNC      F14          ; RECALIBRATE DISKETTE AND SEEK TO 34
1029 05B2 73 05        F13:    MOV      SI,OFFSET F3 ; OK--> GO TURN OF MOTOR
1030 05B4                JMP      SHORT F14A   ; DISKETTE ERROR
1031 05B4 BE 0990 R    MOV      SI,OFFSET F3 ; GET ADDR OF MSG
1032 05B7 EB 02        JMP      SHORT F14A   ; DISPLAY MESSAGE AFTER DISKETTE SETUP
1033
1034                    ;---- TURN DRIVE 0 MOTOR OFF
1035
1036 05B9                F14:    XOR       SI,S1       ; SEQUENCE END ENTRY IF NO ERROR
1037 05B9 33 F6        F14A:   MOV      AL,S1        ; ZERO S1 IF NO ERROR
1038 05BB                MOV      AL,0CH       ; SEQUENCE END ENTRY IF ERROR
1039 05BB 80 0C        MOV      DX,03F2H    ; TURN DRIVE 0 MOTOR OFF
1040 05BD BA 03F2      OUT      DX,AL        ; FDC CTL ADDRESS
1041 05CD EE
1042                    ;----SETUP DISKETTE STATES
1043
1044                    CALL DSKETTE_SETUP   ; INITIALIZE DISKETTE PARAMS
1045 05C1 E8 0000 E     JC       F14B        ; CY-->DISKETTE SETUP ERROR
1046 05C4 72 04        OR       SI,S1       ; PREVIOUS DISKETTE ERROR
1047 05C6 0B F6        JZ       F15         ; NZ-->DISKETTE ERROR BEFORE SETUP
1048 05C8 74 06        F14B:   MOV      SI,OFFSET F3 ; GET ADDR OF MSG
1049 05CA                CALL    E_MSG        ; GO PRINT ERROR MSG
1050 05CA BE 0990 R
1051 05CD E8 1976 R
1052                    ;---- SETUP PRINTER AND RS232 BASE ADDRESSES IF DEVICE ATTACHED
1053
1054                    F15:    MOV      @INTR_FLAG,00H ; SET STRAY INTERRUPT FLAG = 00
1055 05D0                MOV      SI,OFFSET @KB_BUFFER ; SETUP KEYBOARD PARAMETERS
1056 05D0 C6 06 006B R 00 ; @BUFFER_HEAD,S1
1057 05D5 BE 001E R    MOV      @BUFFER_TAIL,S1
1058 05D8 89 36 001A R ; @BUFFER_START,S1
1059 05DC 89 36 001C R ; @BUFFER_END,S1
1060 05E0 89 36 0080 R ; ADD SI,32 ;DEFAULT BUFFER OF 32 BYTES
1061 05E4 83 C6 20
1062 05E7 89 36 0082 R ; MOV MOV DI,OFFSET @PRINT_TIM_OUT ;SET DEFAULT PRINTER TIMEOUT
1063 05EB BF 0078 R   ; PUSH DS
1064 05EE 1E          POP      ES
1065 05EF 07
1066 05F0 B8 1414      MOV      AX,1414H    ; DEFAULT=20
1067 05F3 AB          STOSW   STOSW
1068 05F4 AB          STOSW   STOSW
1069 05F5 B8 0101      MOV      AX,0101H    ;RS232 DEFAULT=01
1070 05F8 AB          STOSW   STOSW
1071 05F9 AB          IN      AL,INTA01
1072 05FA EA 21      AND     AL,0FCH
1073 05FC 24 FC      OUT    INTA01,AL
1074 05FE E6 21      ; ENABLE TIMER AND KB INTS
1075
1076 0600 83 FD 00    CMP     BP,0000
1077
1078 0603 74 18      JE     F15A_0        ; CHECK FOR BP= NON ZERO
1079 0605 B0 0002      MOV    DX,C2         ; (ERROR HAPPENED)
1080 0608 EB 19A5 R   CALL   ERR_BEEP     ; CONTINUE IF NO ERROR
1081 060B BE 0769 R   MOV    SI,OFFSET F3D ; 2 SHORT BEEPS (ERROR)
1082 060E EB 1997 R   CALL   P_MSG        ; LOAD ERROR MSG
1083 0611
ERR_WAIT1:
1084 0611 B4 00      MOV    AH,00
1085 0613 CD 16      INT   16H           ; WAIT FOR 'F1' KEY
1086 0615 85 FC 3B   CMP   AH,3BH
1087 0618 75 F7     JNE   ERR_WAIT
1088 061A EB 0E 90   JMP   F15A
1089 061D
F15A_0:
1090 061D 80 3E 0012 R 01 ; CMP @MFG_TST,1
1091 0622 74 06     JE    F15A          ; MFG MODE
1092 0624 BA 0001   MOV   DX,I         ; BYPASS BEEP
1093 0627 EB 19A5 R ; CALL ERR_BEEP     ; 1 SHORT BEEP (NO ERRORS)
1094 062A A0 0010 R ; MOV AL,BYTE PTR @EQUIP_FLAG
1095 062D 24 01     AND  AL,00000001B ; GET SWITCHES
1096 062F 75 03     JNZ  F15B          ; 'LOOP POST' SWITCH ON
1097 0631 E9 005B R ; JMP START
1098 0634 2A E4     SUB  AH,AH
1099 0636 A0 0049 R ; MOV AL,#CRT_MODE
1100 0639 CD 10     INT  10H           ; CLEAR SCREEN
1101 063B
F15C:
1102 063B BD 1970 R ; MOV BP,OFFSET F4
1103 063E BE 0000   MOV   SI,0         ; PRT_SRC_TBL
1104 0641
F16:
1105 0641 2E 18 B5 00    MOV   DX,CS:[BP]
1106 0645 B0 AA      MOV   AL,0AAH
1107 0647 EE      OUT  DX,AL         ; PRT_BASE1
1108 0648 1E      PUSH DS            ; GET PRINTER BASE ADDR
1109 0649 EC      IN  AL,DX         ; WRITE DATA TO PORT A
1110 064A 1F      POP  DS
1111 064B 3C AA      CMP  AL,0AAH      ; BUS SETTLEING
1112 064D 75 05     JNE  F17           ; READ PORT A
1113 064F 89 B4 08   MOV  [PRINTER_BASE-DATA40],SI ; DATA PATTERN SAME
1114 0652 46      INC  SI            ; NO - CHECK NEXT PRT CD
1115 0653 46      INC  SI            ; [DX] YES - STORE PRT BASE ADDR
1116 0654                ; INCREMENT TO NEXT WORD
1117 0654 45
F17:
1118 0654 45      INC  BP            ; POINT TO NEXT BASE ADDR
1119 0655 45      INC  BP
1120 0656 81 FD 1976 R ; CMP BP,OFFSET F4E ; ALL POSSIBLE ADDRS CHECKED?
1121 065A 75 E5     JNE  F16           ; PRT BASE
1122 065C BB 0000   MOV  BX,0         ; POINTER TO RS232 TABLE
1123 065F BA 03FA   MOV  DX,3FAH     ; CHECK IF RS232 CD #1 ATTCH?
1124 0662 EC      IN  AL,DX
1125 0663 AB F8     TEST AL,0FBH
1126 0665 75 06     JNZ  F18
1127 0667 C7 07 03F8 ; MOV [RS232_BASE-DATA40][BX],3FBH ; SETUP RS232 CD #1 ADDR
1128 066B 43      INC  BX
1129 066D 43      INC  BX
1130 066D BA 02FA   MOV  DX,2FAH
1131 0670 EC      IN  AL,DX
1132 0671 AB F8     TEST AL,0FBH
1133 0673 75 06     JNZ  F19
1134 0675 C7 07 02F8 ; MOV [RS232_BASE-DATA40][BX],2FBH ; SETUP RS232 CD #2
1135 0679 43      INC  BX
    
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SECTION 5


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1136 067A 43          INC     BX
1137
1138          ;----- SET UP EQUIP FLAG TO INDICATE NUMBER OF PRINTERS AND RS232 CARDS
1139
1140 067B          F19:          MOV     AX,S1          ; BASE END;
1141 067B BB C6          MOV     CL,3          ; S1 HAS 2* NUMBER OF RS232
1142 067D B1 03          ROR     AL,CL         ; SHIFT COUNT
1143 067F D2 C8          OR      AL,CL         ; ROTATE RIGHT 3 POSITIONS
1144 0681 0A C3          MOV     BYTE PTR [DI],AL ; OR IN THE PRINTER COUNT
1145 0683 A2 0011 R    MOV     BYTE PTR [DI],AL ; STORE AS SECOND BYTE
1146 0686 BA 0201      MOV     DX,201H
1147 0689 EC          IN      AL,DX
1148 068A 90          NOP
1149 068B 90          NOP
1150 068C 90          NOP
1151 068D A8 0F          TEST    AL,0FH
1152 068F 75 05          JNZ     F20           ; NO_GAME_CARD
1153 0691 80 0E 0011 R 10 MOV     BYTE PTR [DI],16 ; NO_GAME_CARD;
1154 0696          F20:          OR      AL,0
1155
1156          ;----- ENABLE NMI INTERRUPTS
1157
1158 0696 E4 61          IN      AL,PORT_B     ; RESET CHECK ENABLES
1159 0698 0C 30          OR      AL,30H
1160 069A E6 61          OUT     PORT_B,AL
1161 069C 24 CF          AND     AL,0CFH
1162 069E E6 61          OUT     PORT_B,AL
1163 06A0 B0 80          MOV     AL,80H        ; ENABLE NMI INTERRUPTS
1164 06A2 E6 A0          OUT     0A0H,AL
1165 06A4          F21:          INT     19H          ; LOAD BOOT_STRAP;
1166 06A4 CD 19          ; GO TO THE BOOT LOADER
1167
1168          ;--- INT 19 -----
1169          ; BOOT STRAP LOADER
1170          ; TRACK 0, SECTOR 1 IS READ INTO THE
1171          ; BOOT LOCATION (SEGMENT 0, OFFSET 7C00)
1172          ; AND CONTROL IS TRANSFERRED THERE.
1173          ;
1174          ; IF THERE IS A HARDWARE ERROR CONTROL IS
1175          ; TRANSFERRED TO THE ROM BASIC ENTRY POINT.
1176          ;-----
1177          ASSUME CS:CODE,DS:ABS0
1178          ORG     0E6F2H
1179 06F2          ORG     006F2H
1180
1181 06F2          BOOT_STRAP PROC NEAR
1182 06F2 FB          STI
1183 06F3 2B C0          SUB     AX,AX          ; ENABLE INTERRUPTS
1184 06F5 8E D8          MOV     DS,AX         ; ESTABLISH ADDRESSING
1185
1186          ;----- RESET THE DISK PARAMETER TABLE VECTOR
1187
1188 06F7 C7 06 0078 R 0FC7 R MOV     WORD PTR [DI],OFFSET DISK_BASE
1189 06FD 8C 0E 007A R    MOV     WORD PTR [DI],DISK_POINTER+2,CS
1190
1191          ;----- LOAD SYSTEM FROM DISKETTE -- CX HAS RETRY COUNT
1192
1193 0701 B9 0004      MOV     CX,4          ; SET RETRY COUNT
1194 0704          H1:          ; IPL SYSTEM
1195 0704 51          PUSH    CX            ; SAVE RETRY COUNT
1196 0705 B4 00          MOV     AH,0          ; RESET THE DISKETTE SYSTEM
1197 0707 B4 00          INT     13H          ; DISKETTE 10
1198 0709 72 0F          JC      H2            ; IF ERROR, TRY AGAIN
1199 070B BB 0201      MOV     AX,201H       ; READ IN THE SINGLE SECTOR
1200 070E 2B D2          SUB     DX,DX          ; TO THE BOOT LOCATION
1201 0710 8E C2          MOV     ES,DX
1202 0712 BB 7C00 R    MOV     BX,OFFSET #BOOT_LOCN
1203
1204 0715 B9 0001      MOV     CX,1          ; DRIVE 0, HEAD 0
1205 0718 CD 13          INT     13H          ; SECTOR 1, TRACK 0
1206 071A          H2:          ; DISKETTE_10
1207 071A 59          POP     CX            ; RECOVER RETRY COUNT
1208 071B 73 04          JNC     H4            ; CF SET BY UNSUCCESSFUL READ
1209 071D E2 E5          LOOP   H1             ; DO IT FOR RETRY TIMES
1210
1211          ;----- UNABLE TO IPL FROM THE DISKETTE
1212
1213 071F          H3:          ; GO TO RESIDENT BASIC
1214 071F CD 18          INT     18H
1215
1216          ;----- IPL WAS SUCCESSFUL
1217
1218 0721          H4:          ;
1219 0721 EA 7C00 ---- R JMP     #BOOT_LOCN
1220 0726          BOOT_STRAP ENDP
1221
1222          ;-- ORG 0E729H
1223          ORG     00729H
1224 0729          A1      DW     1047      ; 110 BAUD          ; TABLE OF VALUES
1225 072B 0300          DW     768           ; 150              ; FOR INITIALIZATION
1226 072D 0180          DW     384           ; 300
1227 072F 00C0          DW     192           ; 600
1228 0731 0060          DW     96            ; 1200
1229 0733 0030          DW     48            ; 2400
1230 0735 0018          DW     24            ; 4800
1231 0737 000C          DW     12            ; 9600
1232
1233 0739          RS232_10:
1234 0739 E9 0000 E    JMP     RS232_10_1
1235
1236 073C          CONF_TBL:
1237 073C 0008          DW     0              ; USE INT 15 H AH= 0C0H
1238 073E FB          DB     0              ; CONFIGURATION TABLE FOR THIS SYSTEM
1239 073F 00          DB     0              ; LENGTH OF FOLLOWING TABLE
1240 0740 01          DB     0              ; SYSTEM MODEL BYTE
1241 0741 50          DB     0              ; SYSTEM SUB MODEL TYPE BYTE
1242          DB     0              ; BIOS REVISION LEVEL
1243          DB     0              ; 10000000 = DMA CHANNEL 3 USE BY BIOS
1244          DB     0              ; 01000000 = CASCADED INTERRUPT LEVEL 2
1245 0742 00          DB     0              ; 00100000 = REAL TIME CLOCK AVAILABLE
1246 0743 00          DB     0              ; 00010000 = KEYBOARD SCAN CODE HOOK IAH
1247 0744 00          DB     0              ; RESERVED
1248 0745 00          DB     0              ; RESERVED
1249 = 0746          EQU     $            ; RESERVED FOR EXPANSION
    
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1364 09CE          RTC_00 PROC    NEAR          ; READ TIME COUNT
1365 09CE A0 0070 R      MOV     AL,®TIMER_OFL ; GET THE OVERFLOW FLAG
1366 09D1 C6 06 0070 R 00 MOV     ®TIMER_OFL,0   ; AND THEN RESET THE OVERFLOW FLAG
1367 09D6 B8 0E 006E R 0 MOV     CX,®TIMER_HIGH ; GET COUNT OF TIME HIGH WORD
1368 09DA B8 16 006C R 0 MOV     DX,®TIMER_LOW  ; GET COUNT OF TIME LOW WORD
1369 09DE C3           RET                    ; RETURN WITH NO CARRY
1370
1371 09DF          RTC_10:          ; SET TIME COUNT
1372 09DF 89 16 006C R    MOV     ®TIMER_LOW,DX  ; SET TIME COUNT LOW WORD
1373 09E3 89 0E 006E R    MOV     ®TIMER_HIGH,CX ; SET THE TIME COUNT HIGH WORD
1374 09E7 C6 06 0070 R 00 MOV     ®TIMER_OFL,0   ; RESET OVERFLOW FLAG
1375 09EC C3           RET                    ; RETURN WITH NO CARRY
1376
1377 09ED          RTC_15:          ; INVALID FUNCTION (NOT SUPPORTED)
1378 09ED F9           STC                    ; SET CARRY FLAG FOR ERROR (CY=1)
1379 09EE C3           RET                    ; EXIT THROUGH COMMON RETURN
1380
1381 09EF          RTC_A0:          ; READ SYSTEM DAY COUNT
1382 09EF 8B 0E 00CE R    MOV     CX,®DAY_COUNT  ; GET COUNT OF DAYS
1383 09F3 C3           RET                    ; EXIT THROUGH COMMON RETURN WITH CY=0
1384
1385 09F4          RTC_B0:          ; SET SYSTEM DAY COUNT
1386 09F4 89 0E 00CE R    MOV     ®DAY_COUNT,CX  ; SET COUNT OF DAYS
1387 09F8 C3           RET                    ; EXIT THROUGH COMMON RETURN WITH CY=0
1388
1389 09F9          RTC_00 ENDP
1390
1391          ;
1392 0C59          ; ORG     0EC59H
1393 0C59 E9 0000 E      ORG     00C59H
1394          DISKETTE_10:      JMP     DISKETTE_10_1
1395
-----
1395          ;--- BEEP
1396          ; ROUTINE TO SOUND THE BEEPER USING TIMER 2 FOR TONE
1397          ; ENTRY:
1398          ; (BL) = DURATION COUNTER (1 FOR 1/64 SECOND)
1399          ; (CX) = FREQUENCY DIVISOR ((1193180/FREQUENCY) (1331 FOR 886 HZ))
1400          ; EXIT:
1401          ; (AX),(BL),(CX) MODIFIED.
-----
1402
1403
1404 0C5C          BEEP PROC    NEAR          ; SETUP TIMER 2
1405 0C5C 9C          PUSHF                   ; SAVE INTERRUPT STATUS
1406 0C5D FA          CLI                    ; BLOCK INTERRUPTS DURING UPDATE
1407 0C5E B0 B6       MOV     AL,10110110B   ; SELECT TIMER 2,LSB,MSB,BINARY
1408 0C60 E6 43       OUT     TIMER+3,AL     ; WRITE THE TIMER MODE REGISTER
1409 0C62 90          NOP                    ; I/O DELAY
1410 0C63 8A C1       MOV     AL,CL          ; DIVISOR FOR HZ (LOW)
1411 0C65 E6 42       OUT     TIMER+2,AL     ; WRITE TIMER 2 COUNT - LSB
1412 0C67 90          NOP                    ; I/O DELAY
1413 0C68 8A C5       MOV     AL,CH          ; DIVISOR FOR HZ (HIGH)
1414 0C6A E6 42       OUT     TIMER+2,AL     ; WRITE TIMER 2 COUNT - MSB
1415 0C6C E4 61       IN     AL,PORT_B      ; GET CURRENT SETTING OF PORT
1416 0C6E 8A E0       MOV     AH,®AH        ; SAVE THAT SETTING
1417 0C70 0C 03      OR     AL,GATE2+SPK2   ; GATE TIMER 2 AND TURN SPEAKER ON
1418 0C72 E6 61       OUT     PORT_B,AL     ; AND RESTORE INTERRUPT STATUS
1419 0C74 9D          POPF
1420 0C75          G7:
1421 0C75 B9 040B     MOV     CX,1035        ; 1/64 SECOND PER COUNT (BL)
1422 0C78 E8 0CA0 R  CALL    WAITF          ; DELAY COUNT FOR 1/64 OF A SECOND
1423 0C7B FE C3      DEC     BL             ; GO TO BEEP DELAY 1/64 COUNT
1424 0C7D 75 F6      JNZ    G7             ; (BL) LENGTH COUNT EXPIRED?
1425          ; NO - CONTINUE BEEPING SPEAKER
1426 0C7F 9C          PUSHF                   ; SAVE INTERRUPT STATUS
1427 0C80 FA          CLI                    ; BLOCK INTERRUPTS DURING UPDATE
1428 0C81 E4 61       IN     AL,PORT_B      ; GET CURRENT PORT VALUE
1429 0C83 0C FC      OR     AL,®NOT(GATE2+SPK2) ; ISOLATE CURRENT SPEAKER BITS IN CASE
1430 0C85 22 E0       AND    AH,®AH         ; SOMEONE TURNED THEM OFF DURING BEEP
1431 0C87 8A C4       MOV     AL,®AH        ; RECOVER VALUE OF PORT
1432 0C89 24 FC      AND    AL,®NOT(GATE2+SPK2) ; FORCE SPEAKER DATA OFF
1433 0C8B E6 61       OUT     PORT_B,AL     ; AND STOP SPEAKER TIMER
1434 0C8D 9D          POPF                   ; RESTORE INTERRUPT FLAG STATE
1435 0C8E B9 040B     MOV     CX,1035        ; FORCE 1/64 SECOND DELAY (SHORT)
1436 0C91 E8 0CA0 R  CALL    WAITF          ; MINIMUM DELAY BETWEEN ALL BEEPS
1437 0C94 9C          PUSHF                   ; SAVE INTERRUPT STATUS
1438 0C95 FA          CLI                    ; BLOCK INTERRUPTS DURING UPDATE
1439 0C96 E4 61       IN     AL,PORT_B      ; GET CURRENT PORT VALUE IN CASE
1440 0C98 24 03      AND    AL,GATE2+SPK2  ; SOMEONE TURNED THEM ON
1441 0C9A 0A C3      OR     AL,®AH         ; RECOVER VALUE OF PORT_B
1442 0C9C E6 61       OUT     PORT_B,AL     ; RESTORE SPEAKER STATUS
1443 0C9E 9D          POPF                   ; RESTORE INTERRUPT FLAG STATE
1444 0C9F C3           RET
1445
1446 0CA0          BEEP ENDP
1447
-----
1448          ;--- WAITF
1449          ; FIXED TIME WAIT ROUTINE (HARDWARE CONTROLLED - NOT PROCESSOR)
1450          ;
1451          ; ENTRY:
1452          ; (CX) = COUNT OF 15.085737 MICROSECOND INTERVALS TO WAIT
1453          ; MEMORY REFRESH TIMER OUTPUT AT THE DMA CHANNEL 0
1454          ; ADDRESS REGISTER USED AS REFERENCE.
1455          ; EXIT:
1456          ; AFTER (CX) TIME COUNT (PLUS OR MINUS 31 MICROSECONDS)
1457          ; (CX) = 0
-----
1458
1459
1460 0CA0          WAITF PROC    NEAR          ; DELAY FOR (CX)*15.085737 US
1461 0CA0 50          PUSH    AX             ; SAVE WORK REGISTER (AH)
1462 0CA1 D1 E9       SHR    CX,1           ; DIVIDE 15us COUNT DOWN TO 30us COUNT
1463 0CA3 E3 13       JCXZ   WAITF9         ; EXIT IF COUNT WAS ZERO OR ONE
1464
1465 0CA5 E6 0C       OUT    DMA+12,AL      ; CLEAR THE DMA BYTE POINTER FLIP/FLOP
1466 0CA7          WAITF:          ; SAVE INTERRUPT STATE
1467 0CA7 9C          PUSHF                   ; BLOCK INTERRUPTS TILL NEXT CHANGE
1468 0CA8 FA          CLI                    ; WAIT FOR REFRESH ADDRESS CHANGE
1469 0CA9          WAITF3:          ; READ CURRENT ADDRESS LOW BYTE
1470 0CA9 E4 00       IN     AL,DMA         ; DISCARD LOW BIT (30us)
1471 0CAB 24 FE       AND    AL,11111110B  ; READ VALUE JUST CHANGE
1472 0CAD 3A E0       CMP    AH,AL          ; DID NEW/OLD VALUE INCREASE IT DID
1473 0CAF 8A E0       MOV    AH,AL          ; SAVE NEW/OLD VALUE INCREASE IT DID
1474 0CB1 E4 00       IN     AL,DMA         ; READ HIGH BYTE (AND IGNORE)
1475 0CB3 74 F4       JE     WAITF3         ; WAIT FOR A CHANGE IN ADDRESS BITS
1476
1477 0CB5 9D          POPF                   ; RESTORE INTERRUPTS

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1478 0CB6 E2 EF          LOOP    WAITF1          ; DECREMENT CYCLES COUNT TILL COUNT END
1479 0CB8                WAITF9:
1480 0CB8 58              POP     AX                ; RESTORE (AH)
1481 0CB9 C3              RET                     ; RETURN (CX) = 0
1482
1483 0CBA                WAITF  ENDP
1484
1485
1486
1487                ; PRINT A SEGMENT VALUE TO LOOK LIKE A 20 BIT ADDRESS ;
1488                ; DX MUST CONTAIN SEGMENT VALUE TO BE PRINTED ;
-----
1489 0CBA                PRT_SEG PROC    NEAR
1490 0CBA 8A C6          MOV     AL,DI             ;GET MSB
1491 0CBC E8 1958 R      CALL   XPC_BYTE
1492 0CBF 8A C2          MOV     AL,DL             ;LSB
1493 0CC1 E8 1958 R      CALL   XPC_BYTE
1494 0CC4 B0 30          MOV     AL,'0'           ; PRINT A '0 '
1495 0CC6 E8 1969 R      CALL   PRT_HEX
1496 0CC9 B0 20          MOV     AL,' '           ;SPACE
1497 0CDB E8 1969 R      CALL   PRT_HEX
1498 0CCE C3              RET
1499 0CCF                PRT_SEG ENDP
1500
1501
1502
1503                ; THIS SUBROUTINE PERFORMS A READ/WRITE STORAGE TEST ON A BLOCK ;
1504                ; OF STORAGE. ;
1505                ; ENTRY REQUIREMENTS: ;
1506                ; ES = ADDRESS OF STORAGE SEGMENT BEING TESTED ;
1507                ; DS = ADDRESS OF STORAGE SEGMENT BEING TESTED ;
1508                ; CX = WORD COUNT OF STORAGE BLOCK TO BE TESTED ;
1509                ; EXIT PARAMETERS: ;
1510                ; ZERO FLAG = 0 IF STORAGE ERROR (DATA COMPARE OR PARITY ;
1511                ; CHECK. AL=0 DENOTES A PARITY CHECK. ELSE AL=XOR'ED ;
1512                ; BIT PATTERN OF THE EXPECTED DATA PATTERN VS THE ACTUAL ;
1513                ; DATA READ. ;
1514                ; AX,BX,CX,DX,D1, AND SI ARE ALL DESTROYED. ;
-----
1515
1516 0CCF                STGTST_CNT PROC    NEAR
1517 0CCF 8B D9          MOV     BX,CX             ; SAVE WORD COUNT OF BLOCK TO TEST
1518 0CD1 FC              CLD                     ; SET DIR FLAG TO INCREMENT
1519 0CD2 2B FF          SUB     DI,D1             ; SET DI=OFFSET 0 REL TO ES REG
1520 0CD4 2B C0          SUB     AX,AX             ; SETUP FOR 0->FF PATTERN TEST
1521 0CD6
1522 0CD6 88 05          MOV     [DI],AL           ; ON FIRST BYTE
1523 0CD8 84 05          MOV     AL,[DI]
1524 0CDA 32 C4          XOR     AL,AH             ; O.K. ?
1525 0CDC 75 79          JNZ     C7                ; GO ERROR IF NOT
1526 0CDE FE C4          INC     AH
1527 0CE0 8A C4          MOV     AL,AH
1528 0CE2 75 F2          JNZ     C2_1             ; LOOP TILL WRAP THROUGH FF
1529 0CE4 B8 55AA        MOV     AX,055AAH         ; GET INITIAL DATA PATTERN TO WRITE
1530 0CE7 8B D0          MOV     DX,AX             ; SET INITIAL COMPARE PATTERN.
1531 0CE9 F3 7B          REP     STOSW             ; FILL STORAGE LOCATIONS IN BLOCK
1532 0CEB E4 61          IN     AL,PORT_B
1533 0CED 0C 30          OR     AL,030H           ; TOGGLE PARITY CHECK LATCHES
1534 0CEF E6 61          OUT    PORT_B,AL
1535 0CF1 90              NOP
1536 0CF2 24 CF          AND     AL,0CFH
1537 0CF4 E6 61          OUT    PORT_B,AL
1538
1539 0CF6 4F              DEC     DI                ; POINT TO LAST WORD JUST WRITTEN
1540 0CF7 4F              DEC     DI
1541 0CF8 FD              STD     ; SET DIR FLAG TO GO BACKWARDS
1542 0CF9 8B F7          MOV     SI,DI             ; INITIALIZE DESTINATION POINTER
1543 0CFB 8B CB          MOV     CX,BX             ; SETUP WORD COUNT FOR LOOP
1544 0CFD
1545 0CFD AD              LODSW  ; INNER TEST LOOP
1546 0CFE 33 C2          XOR     AX,DX             ; READ OLD TEST WORD FROM STORAGE
1547 0D00 75 57          JNE     C7X              ; DATA READ AS EXPECTED ?
1548 0D02 8B AA55        MOV     AX,0AA55H         ; NO - GO TO ERROR ROUTINE
1549 0D05 AB              STOSW  ; GET NEXT DATA PATTERN TO WRITE
1550 0D06 E2 F5          LOOP   C3                 ; WRITE INTO LOCATION JUST READ
1551                                ; DECREMENT WORD COUNT AND LOOP
1552 0D08 FC              CLD                     ; SET DIR FLAG TO GO FORWARD
1553 0D09 47              INC     DI                ; SET POINTER TO BEG LOCATION
1554 0D0A 47
1555 0D0B 8B F7          MOV     SI,DI             ; INITIALIZE DESTINATION POINTER
1556 0D0D 8B CB          MOV     CX,BX             ; SETUP WORD COUNT FOR LOOP
1557 0D0F 8B D0          MOV     DX,AX             ; SETUP COMPARE PATTERN ?
1558 0D11
1559 0D11 AD              LODSW  ; INNER TEST LOOP
1560 0D12 33 C2          XOR     AX,DX             ; READ OLD TEST WORD FROM STORAGE
1561 0D14 75 43          JNE     C7X              ; DATA READ AS EXPECTED ?
1562 0D16 8B FFFF        MOV     AX,0FFFFH         ; NO - GO TO ERROR ROUTINE
1563 0D19 AB              STOSW  ; GET NEXT DATA PATTERN TO WRITE
1564 0D1A E2 F5          LOOP   C4                 ; WRITE INTO LOCATION JUST READ
1565                                ; DECREMENT WORD COUNT AND LOOP
1566 0D1C 4F              DEC     DI                ; POINT TO LAST WORD JUST WRITTEN
1567 0D1D 4F              DEC     DI
1568 0D1E FD              STD     ; SET DIR FLAG TO GO BACKWARDS
1569 0D1F 8B F7          MOV     SI,DI             ; INITIALIZE DESTINATION POINTER
1570 0D21 8B CB          MOV     CX,BX             ; SETUP WORD COUNT FOR LOOP
1571 0D23 8B D0          MOV     DX,AX             ; SETUP COMPARE PATTERN "00101H".
1572 0D25
1573 0D25 AD              LODSW  ; INNER TEST LOOP
1574 0D26 33 C2          XOR     AX,DX             ; READ OLD TEST WORD FROM STORAGE
1575 0D28 75 2F          JNE     C7X              ; DATA READ AS EXPECTED ?
1576 0D2A 8B 0101        MOV     AX,00101H         ; NO - GO TO ERROR ROUTINE
1577 0D2D AB              STOSW  ; GET NEXT DATA PATTERN TO WRITE
1578 0D2E E2 F5          LOOP   C5                 ; WRITE INTO LOCATION JUST READ
1579                                ; DECREMENT WORD COUNT AND LOOP
1580 0D30 FC              CLD                     ; SET DIR FLAG TO GO FORWARD
1581 0D31 47              INC     DI                ; SET POINTER TO BEG LOCATION
1582 0D32 47
1583 0D33 8B F7          MOV     SI,DI             ; INITIALIZE DESTINATION POINTER
1584 0D35 8B CB          MOV     CX,BX             ; SETUP WORD COUNT FOR LOOP
1585 0D37 8B D0          MOV     DX,AX             ; SETUP COMPARE PATTERN "00101H".
1586 0D39
1587 0D39 AD              LODSW  ; INNER TEST LOOP
1588 0D3A 33 C2          XOR     AX,DX             ; READ OLD TEST WORD FROM STORAGE
1589 0D3C 75 1B          JNE     C7X              ; DATA READ AS EXPECTED ?
1590 0D3E AB              STOSW  ; NO - GO TO ERROR ROUTINE
1591 0D3F E2 F8          LOOP   C6                 ; WRITE ZERO INTO LOCATION READ
1592                                ; DECREMENT WORD COUNT AND LOOP

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SECTION 5

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1592          ;
1593 0D41 4F          DEC D1          ; POINT TO LAST WORD JUST WRITTEN
1594 0D42 4F          DEC D1
1595 0D43 FD          STD          ; SET DIR FLAG TO GO BACKWARDS
1596 0D44 8B F7       MOV SI,D1      ; INITIALIZE DESTINATION POINTER
1597 0D46 8B CB       MOV CX,BX      ; SETUP WORD COUNT FOR LOOP
1598 0D48 8B D0       MOV DX,AX      ; SETUP COMPARE PATTERN "00000H"
1599 0D4A
1600 0D4A AD          LODSW         ; VERIFY MEMORY IS ZERO.
1601 0D4B 33 C2       XOR AX,DX      ; DATA READ AS EXPECTED ?
1602 0D4D 75 0A       JNE JC00H     ; NO - GO TO ERROR ROUTINE
1603 0D4F E2 F9       LOOP C6X      ; DECREMENT WORD COUNT AND LOOP
1604
1605 0D51 E4 E2       IN AL,PORT_C  ; DID A PARITY ERROR OCCUR ?
1606 0D53 24 C0       AND AL,00H    ; ZERO AL WILL BE OFF, IF PARITY ERROR
1607 0D55 80 00       MOV AL,0      ; AL=0 DATA COMPARE OK
1608 0D57
1609 0D57 FC          CLD          ; SET DIRECTION FLAG TO INC
1610 0D58 C3          RET
1611 0D59
1612 0D59 3C 00       CMP AL,0      ; FIND BYTE THAT FAILED.
1613 0D5B 75 FA       JNZ C7        ;
1614 0D5D 8A C4       MOV AL,AH     ;
1615 0D5F EB F6       JMP SHORT C7  ;
1616 0D61
1617
1618          ;
1619 0F57          ORG 0EF57H
1620 0F57 E9 0000 E   DISK_INT:    ORG 00F57H   DISK_INT_1
1621          ;
1622          ;
1623 0F79          ORG 0EF79H
1624          ;
1625          ;
1626          ;
1627          ;
1628          ;
1629          ;
1630 0F79          ;-----
1631 0F79 DF          ; MEDIA/DRIVE PARAMETER TABLES
1632 0F7A 02 C0       ;-----
1633 0F7B 25         ;
1634 0F7C 02         ;
1635 0F7D 09         ;
1636 0F7E 2A         ;
1637 0F7F FF         ;
1638 0F80 50         ;
1639 0F81 F6         ;
1640 0F82 0F         ;
1641 0F83 08         ;
1642 0F84 27         ;
1643 0F85 80         ;
1644          ;
1645          ;
1646          ;
1647 0F86          ;-----
1648 0F86 DF          ; 40 TRACK LOW DATA RATE MEDIA IN 40 TRACK LOW DATA RATE DRIVE
1649 0F87 02         ;
1650 0F88 25         ;
1651 0F89 02         ;
1652 0F8A 09         ;
1653 0F8B 2A         ;
1654 0F8C FF         ;
1655 0F8D 50         ;
1656 0F8E F6         ;
1657 0F8F 0F         ;
1658 0F90 08         ;
1659 0F91 27         ;
1660 0F92 40         ;
1661          ;
1662          ;
1663          ;
1664 0F93          ;-----
1665 0F93 DF          ; 80 TRACK HI DATA RATE MEDIA IN 80 TRACK HI DATA RATE DRIVE
1666 0F94 02         ;
1667 0F95 25         ;
1668 0F96 02         ;
1669 0F97 0F         ;
1670 0F98 1B         ;
1671 0F99 FF         ;
1672 0F9A 54         ;
1673 0F9B F6         ;
1674 0F9C 0F         ;
1675 0F9D 08         ;
1676 0F9E 4F         ;
1677 0F9F 00         ;
1678          ;
1679          ;
1680          ;
1681 0FA0          ;-----
1682 0FA0 DF          ; 80 TRACK LOW DATA RATE MEDIA IN 80 TRACK LOW DATA RATE DRIVE
1683 0FA1 02         ;
1684 0FA2 25         ;
1685 0FA3 02         ;
1686 0FA4 09         ;
1687 0FA5 2A         ;
1688 0FA6 FF         ;
1689 0FA7 50         ;
1690 0FA8 F6         ;
1691 0FA9 0F         ;
1692 0FAA 08         ;
1693 0FAB 4F         ;
1694 0FAC 80         ;
1695          ;
1696          ;
1697          ;
1698 0FAD          ;-----
1699 0FAD DF          ; 80 TRACK HI DATA RATE MEDIA IN 80 TRACK HI DATA RATE DRIVE
1700 0FAE 02         ;
1701 0FAF 25         ;
1702 0FB0 02         ;
1703 0FB1 09         ;
1704 0FB2 2A         ;
1705 0FB3 FF         ;

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1706 0FB4 50          DB      050H          ; GAP LENGTH FOR FORMAT
1707 0FB5 F6          DB      0F6H          ; FILL BYTE FOR FORMAT
1708 0FB6 0F          DB      15           ; HEAD SETTLE TIME (MILLISECONDS)
1709 0FB7 08          DB      8            ; MOTOR START TIME (1/8 SECONDS)
1710 0FB8 4F          DB      79           ; MAX. TRACK NUMBER
1711 0FB9 80          DB      RATE_250     ; DATA TRANSFER RATE
1712                ; -----
1713                ; 80 TRACK HI DATA RATE MEDIA IN 80 TRACK HI DATA RATE DRIVE ;
1714                ; -----
1715 0FBA                MO_TBL6
1716 0FBA AF          DB      101111B      ; SRT=A, HD UNLOAD=0F - 1ST SPECIFY BYTE
1717 0FB8 02          DB      2            ; HD LOAD=1, MODE=DMA - 2ND SPECIFY BYTE
1718 0FB8 25          DB      MOTOR_WAIT   ; WAIT TIME AFTER OPERATION TILL MOTOR OFF
1719 0FBD 02          DB      2            ; 512 BYTES/SECTOR
1720 0FBE 12          DB      18           ; EDT ( LAST SECTOR ON TRACK)
1721 0FBF 1F          DB      01BH        ; GAP LENGTH
1722 0FC0 FF          DB      0FFH        ; DTL
1723 0FC1 6C          DB      06CH        ; GAP LENGTH FOR FORMAT
1724 0FC2 F6          DB      0F6H        ; FILL BYTE FOR FORMAT
1725 0FC3 0F          DB      15           ; HEAD SETTLE TIME (MILLISECONDS)
1726 0FC4 08          DB      8            ; MOTOR START TIME (1/8 SECONDS)
1727 0FC5 4F          DB      79           ; MAX. TRACK NUMBER
1728 0FC6 00          DB      RATE_500    ; DATA TRANSFER RATE
1729                ; -----
1730                ; DISK_BASE
1731                ; THIS IS THE SET OF PARAMETERS REQUIRED FOR DISKETTE OPERATION. ;
1732                ; THEY ARE POINTED AT BY THE DATA VARIABLE DISK_POINTER. TO ;
1733                ; MODIFY THE PARAMETERS, BUILD ANOTHER PARAMETER BLOCK AND POINT ;
1734                ; DISK_POINTER TO IT. ;
1735                ; -----
1736                ; ORG 0EFC7H
1737 0FC7                ORG 0EFC7H
1738 0FC7                DISK_BASE LABEL BYTE
1739 0FC7 CF          DB      1100111B  ; SRT=C, HD UNLOAD=0F - 1ST SPECIFY BYTE
1740 0FC8 02          DB      2            ; HD LOAD=1, MODE=DMA - 2ND SPECIFY BYTE
1741 0FC9 25          DB      MOTOR_WAIT   ; WAIT AFTER OPN TIL MOTOR OFF
1742 0FCA 02          DB      2            ; 512 BYTES/SECTOR
1743 0FCB 08          DB      8            ; EDT ( LAST SECTOR ON TRACK)
1744 0FCC 2A          DB      02AH        ; GAP LENGTH
1745 0FCD FF          DB      0FFH        ; DTL
1746 0FCE 50          DB      050H        ; GAP LENGTH FOR FORMAT
1747 0CF F6          DB      0F6H        ; FILL BYTE FOR FORMAT
1748 0FDD 19          DB      25           ; HEAD SETTLE TIME (MILLISECONDS)
1749 0FDE 04          DB      4            ; MOTOR START TIME (1/8 SECONDS)
1750                ; -----
1751                ; ORG 0EFD2H
1752 0FD2                ORG 0EFD2H
1753 0FD2                PRINTER_IO: PRINTER_IO_1
1754 0FD2 E9 0000 E    JMP
1755                ; -----
1756                ; ORG 0F045H
1757 1045                MI ORG 0F045H
1758 1045 0000 E      DW 0FF5H SET_MODE ; TABLE OF ROUTINES WITHIN VIDEO I/O
1759 1047 0000 E      DW 0FF5H SET_CTYPE
1760 1049 0000 E      DW 0FF5H SET_CPOS
1761 104B 0000 E      DW 0FF5H READ_CURSOR
1762 104D 0000 E      DW 0FF5H READ_LPEN
1763 104F 0000 E      DW 0FF5H ACT_DISP_PAGE
1764 1051 0000 E      DW 0FF5H SCROLL_UP
1765 1053 0000 E      DW 0FF5H SCROLL_DOWN
1766 1055 0000 E      DW 0FF5H READ_AC_CURRENT
1767 1057 0000 E      DW 0FF5H WRITE_AC_CURRENT
1768 1059 0000 E      DW 0FF5H WRITE_C_CURRENT
1769 105B 0000 E      DW 0FF5H SET_COLOR
1770 105D 0000 E      DW 0FF5H WRITE_DOT
1771 105F 0000 E      DW 0FF5H READ_DOT
1772 1061 0000 E      DW 0FF5H WRITE_TTY
1773 1063 0000 E      DW 0FF5H VIDEO_STATE
1774 = 0020          MIL EQU $-MI
1775                ; -----
1776                ; ORG 0F065H
1777 1065                ORG 0F065H
1778 1065                VIDEO_IO: VIDEO_IO_1
1779 1065 E9 0000 E    JMP
1780                ; -----
1781                ;----- VIDEO PARAMETERS --- INIT_TABLE
1782                ;:-
1783                ;:- ORG 0F0A4H
1784 10A4                ORG 010A4H
1785                ;:-
1786 10A4                VIDEO_PARAMS LABEL BYTE
1787 10A4 38 28 2D 0A 1F 06 DB 38H,28H,2DH,0AH,1FH,6,19H ; SET UP FOR 40X25
1788 19
1789 10AB 1C 02 07 06 07 DB 1CH,2,7,6,7
1790 10B0 00 00 00 00 DB 0,0,0,0
1791 = 0010          M4 EQU $-VIDEO_PARAMS
1792                ;:-
1793 10B4 71 50 5A 0A 1F 06 DB 71H,50H,5AH,0AH,1FH,6,19H ; SET UP FOR 80X25
1794 19
1795 10BB 1C 02 07 06 07 DB 1CH,2,7,6,7
1796 10C0 00 00 00 00 DB 0,0,0,0
1797                ;:-
1798 10C4 38 28 2D 0A 7F 06 DB 38H,28H,2DH,0AH,7FH,6,64H ; SET UP FOR GRAPHICS
1799 64
1800 10CB 70 02 01 06 07 DB 70H,2,1,6,7
1801 10DD 00 00 00 00 DB 0,0,0,0
1802                ;:-
1803 10D4 61 50 52 0F 19 06 DB 61H,50H,52H,0FH,19H,6,19H ; SET UP FOR 80X25 B+W CARD
1804 19
1805 10DB 19 02 00 0B 0C DB 19H,2,0DH,0BH,0CH
1806 10E0 00 00 00 00 DB 0,0,0,0
1807                ;:-
1808 10E4 0800          M5 DW 2048 ; TABLE OF REGEN LENGTHS
1809 10E6 1000          DW 4096 ; 40X25
1810 10E8 4000          DW 16384 ; 80X25
1811 10EA 4000          DW 16384 ; GRAPHICS
1812                ;:-
1813                ;----- COLUMNS
1814 10EC 28 28 50 50 28 28 M6 DB 40,40,80,80,40,40,80,80
1815 50
1816                ;----- C_REG_TAB
1817 10F4 2C 28 2D 29 2A 2E M7 DB 2CH,28H,2DH,29H,2AH,2EH,1EH,29H ; TABLE OF MODE SETS
1818 1E 29

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SECTION 5

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1819 PAGE
1820 :--- INT 12 -----
1821 : MEMORY_SIZE_DET
1822 : THIS ROUTINE DETERMINES THE AMOUNT OF MEMORY IN THE SYSTEM
1823 : AS REPRESENTED BY THE SWITCHES ON THE PLANAR. NOTE THAT THE
1824 : SYSTEM MAY NOT BE ABLE TO USE I/O MEMORY UNLESS THERE IS A FULL
1825 : COMPLEMENT OF 64K BYTES ON THE PLANAR.
1826 : INPUT
1827 : NO REGISTERS
1828 : THE MEMORY_SIZE VARIABLE IS SET DURING POWER ON DIAGNOSTICS
1829 : ACCORDING TO THE FOLLOWING HARDWARE ASSUMPTIONS:
1830 : PORT 60 BITS 3,2 = 00 - 256K BASE RAM
1831 : 01 - 512K BASE RAM
1832 : 10 - 576K BASE RAM
1833 : 11 - 640K BASE RAM
1834 : PORT 62 BITS 3-0 INDICATE AMOUNT OF I/O RAM IN 32K INCREMENTS
1835 : E.G., 0000 - NO RAM IN I/O CHANNEL
1836 : 0010 - 64K RAM IN I/O CHANNEL, ETC.
1837 : OUTPUT
1838 : (AX) = NUMBER OF CONTIGUOUS 1K BLOCKS OF MEMORY
1839 :-----
1840 : ASSUME CS:CODE,DS:DATA
1841 : ORG 0F841H
1842 1841 ORG 01841H
1843 1841 MEMORY_SIZE_DET PROC FAR
1844 1841 FB STI ; INTERRUPTS BACK ON
1845 1842 IE PUSH DS ; SAVE SEGMENT
1846 1843 EB 1A12 R CALL DDS
1847 1846 A1 0013 R MOV AX, MEM_SIZE ; GET VALUE
1848 1849 IF POP DS ; RECOVER SEGMENT
1849 184A CF IRET ; RETURN TO CALLER
1850 184B MEMORY_SIZE_DET ENDP
1851 :-----
1852 :--- INT 11 -----
1853 : EQUIPMENT_DETERMINATION
1854 : THIS ROUTINE ATTEMPTS TO DETERMINE WHAT OPTIONAL
1855 : DEVICES ARE ATTACHED TO THE SYSTEM.
1856 : INPUT
1857 : NO REGISTERS
1858 : THE EQUIP_FLAG VARIABLE IS SET DURING THE POWER ON
1859 : DIAGNOSTICS USING THE FOLLOWING HARDWARE ASSUMPTIONS:
1860 : PORT 60 = LOW ORDER BYTE OF EQUIPMENT
1861 : PORT 3FA = INTERRUPT ID REGISTER OF 8250
1862 : BITS 7-3 ARE ALWAYS 0
1863 : PORT 378 = OUTPUT PORT OF PRINTER -- 8255 PORT THAT
1864 : CAN BE READ AS WELL AS WRITTEN
1865 : OUTPUT
1866 : (AX) IS SET. BIT SIGNIFICANT, TO INDICATE ATTACHED I/O
1867 : BIT 15,14 = NUMBER OF PRINTERS ATTACHED
1868 : BIT 13 NOT USED
1869 : BIT 12 = GAME I/O ATTACHED
1870 : BIT 11,10,9 = NUMBER OF RS232 CARDS ATTACHED
1871 : BIT 8 UNUSED
1872 : BIT 7,6 = NUMBER OF DISKETTE DRIVES
1873 : 00=1, 01=2, 10=3, 11=4 ONLY IF BIT 0 = 1
1874 : BIT 5,4 = INITIAL VIDEO MODE
1875 : 00 - UNUSED
1876 : 01 - 40X25 BW USING COLOR CARD
1877 : 10 - 80X25 BW USING COLOR CARD
1878 : 11 - 80X25 BW USING BW CARD
1879 : BIT 3,2 = PLANAR RAM SIZE (00=256K,01=512K,10=576K,11=640K)
1880 : BIT 1 = MATH COPROCESSOR
1881 : BIT 0 = IPL FROM DISKETTE -- THIS BIT INDICATES THAT
1882 : THERE ARE DISKETTE DRIVES ON THE SYSTEM
1883 : NO OTHER REGISTERS AFFECTED
1884 :-----
1885 : ASSUME CS:CODE,DS:DATA
1886 : ORG 0F84DH
1887 184D ORG 0184DH
1888 184D EQUIPMENT PROC FAR
1889 184D STI ; INTERRUPTS BACK ON
1890 184D FB PUSH DS ; SAVE SEGMENT REGISTER
1891 184E IE CALL DDS
1892 184F EB 1A12 R MOV AX, EQUIP_FLAG ; GET THE CURRENT SETTINGS
1893 1852 A1 0010 R POP DS ; RECOVER SEGMENT
1894 1855 IF IRET ; RETURN TO CALLER
1895 1856 CF EQUIPMENT ENDP
1896 1857 :-----
1897 :--- INT 15 -----
1898 :
1899 : ORG 0F859H
1900 : ORG 01859H
1901 1859 CASSETTE_ID:
1902 1859 JMP CASSETTE_ID_1
1903 1859 E9 0000 E
1904
1905 :-----
1906 : NON-MASKABLE INTERRUPT ROUTINE:
1907 : THIS ROUTINE WILL PRINT A "PARITY CHECK 1 OR 2" MESSAGE
1908 : AND ATTEMPT TO FIND THE STORAGE LOCATION CONTAINING THE
1909 : BAD PARITY. IF FOUND, THE SEGMENT ADDRESS WILL BE
1910 : PRINTED. IF NO PARITY ERROR CAN BE FOUND (INTERMITTANT
1911 : READ PROBLEM) ?????? WILL BE PRINTED WHERE THE ADDRESS
1912 : WOULD NORMALLY GO.
1913 :-----
1914 185C NMI_INT_1 PROC NEAR
1915 : ASSUME DS:DATA
1916 185C 50 PUSH AX ; SAVE ORIG CONTENTS OF AX
1917 185D E4 62 IN AL, PORT_C ; SAVE ORIG CHECK
1918 185F A8 C0 TEST AL, 000H ; PARITY CHECK?
1919 1861 75 03 JNZ NMI_1 ; NO, EXIT FROM ROUTINE
1920 1863 EB 58 90 JMP D14
1921 1866 NMI_1:
1922 1866 BA ---- R MOV DX, DATA
1923 1869 8E DA MOV DS, DX
1924 186B BE 18E2 R MOV SI, OFFSET D1 ; ADDR OF ERROR MSG
1925 186E A8 40 TEST AL, 40H ; I/O PARITY CHECK
1926 1870 75 03 JNZ D13 ; DISPLAY ERROR MSG
1927 1872 BE 18F2 R MOV SI, OFFSET D2 ; MUST BE PLANAR
1928 1875 D13:
1929 1875 B4 00 MOV AH, 00 ; INIT AND SET MODE FOR VIDEO
1930 1877 A0 0449 R MOV AL, %CRT_MODE ; CALL VIDEO_ID PROCEDURE
1931 187A CD 10 INT 10H ; PRINT ERROR MSG
1932 187C E8 1997 R CALL P_MSG
    
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2047 1945 26: C7 06 0067 R 0003      MOV     ES:010_ROM_INIT,0003H ; LOAD OFFSET
2048 194C 26: 8C IE 0069 R           MOV     ES:010_ROM_SEG,DS    ; LOAD SEGMENT
2049 1951 26: FF IE 0067 R           CALL   DWORD PTR ES:010_ROM_INIT ; CALL INIT./TEST ROUTINE
2050 1956 5A                          POP     DX                    ;
2051 1957                          ROM_CHECK:
2052 1957 C3                          RET     ; RETURN TO CALLER
2053 1958                          ROM_CHECK      ENDP
2054
2055
2056      ; CONVERT AND PRINT ASCII CODE
2057      ; AL MUST CONTAIN NUMBER TO BE CONVERTED. ;
2058      ; AX AND BX DESTROYED
2059
-----
2060 1958                          XPC_BYTE      PROC      NEAR
2061 1958 50                          PUSH   AX                    ; SAVE FOR LOW NIBBLE DISPLAY
2062 1959 B1 04                         MOV    CL,4                  ; SHIFT COUNT
2063 195B D2 E8                         SHR    AL,CL                 ; NYBBLE SWAP
2064 195D E8 1963 R                       CALL   XLAT_PR              ; DO THE HIGH NIBBLE DISPLAY
2065 1960 58                             POP    AX                    ; RECOVER THE NIBBLE
2066 1961 24 0F                         AND    AL,0FH               ; ISOLATE TO LOW NIBBLE
2067                                     ; FALL INTO LOW NIBBLE CONVERSION
2068 1963                                XLAT_PR  PROC      NEAR
2069 1963 04 90                         ADD    AL,090H              ; CONVERT 00-0F TO ASCII CHARACTER
2070 1965 27                             DAA                                     ; ADD FIRST CONVERSION FACTOR
2071 1966 14 40                         ADC    AL,040H              ; ADJUST FOR NUMERIC AND ALPHA RANGE
2072 1968 27                             DAA                                     ; ADD CONVERSION AND ADJUST LOW NIBBLE
2073 1969                                PRT_HEX  PROC      NEAR
2074 1969 B4 0E                         MOV    AH,14                ; DISPLAY CHARACTER IN AL
2075 196B B7 00                         MOV    BH,0                 ;
2076 196D CD 10                         INT    10H                  ; CALL VIDEO_IO
2077 196F C3                          RET
2078 1970                                PRT_HEX  ENDP
2079 1970                                XLAT_PR  ENDP
2080 1970                                XPC_BYTE  ENDP
2081
2082 1970                                F4       LABEL   WORD      ; PRINTER SOURCE TABLE
2083 1970 03BC                          DW     39CH
2084 1972 0378                          DW     378H
2085 1974 0278                          DW     278H
2086 1976                                F4E      LABEL   WORD
2087
-----
2088      ; THIS SUBROUTINE WILL PRINT A MESSAGE ON THE DISPLAY ;
2089      ; ENTRY REQUIREMENTS: ;
2090      ; S1 = OFFSET (ADDRESS) OF MESSAGE BUFFER ;
2091      ; CX = MESSAGE BYTE COUNT ;
2092      ; MAXIMUM MESSAGE LENGTH IS 36 CHARACTERS ;
2093
2094
2095
2096 1976                                E_MSG     PROC      NEAR
2097 1976 8B EE                          MOV     BP,S1                ; SET BP NON-ZERO TO FLAG ERR
2098 1978 E8 1997 R                       CALL   P_MSG                 ; PRINT MESSAGE
2099 197B 1E IE                          PUSH   DS                    ;
2100 197C E8 1A12 R                       CALL   DDS                   ;
2101 197F A0 0010 R                       MOV     AL,BYTE PTR 0EQUIP_FLAG ; LOOP/HALT ON ERROR
2102 1982 24 01                          AND    AL,01H               ; SWITCH ON?
2103 1984 75 0F                         JNZ    G12                   ; NO RETURN
2104 1986                                MFG_HALT:
2105 1986 FA                          CLI                             ; YES - HALT SYSTEM
2106 1987 B0 89                          MOV     AL,89H               ;
2107 1989 E6 63                          OUT    CMD_PORT,AL          ;
2108 198B B0 85                          MOV     AL,T0000101B        ; DISABLE KB
2109 198D E6 61                          OUT    PORT_B,AL            ;
2110 198F A0 0015 R                       MOV     AL,0MFG_ERR_FLAG    ; RECOVER ERROR INDICATOR
2111 1992 E6 60                          OUT    PORT_A,AL            ; SET INTO 8255 REG
2112 1994 F4                          HLT                             ; HALT SYS
2113 1995 F4
2114 1995 1F                         G12:  POP     DS                ; WRITE_MSG:
2115 1996 C3                          RET
2116 1997                                E_MSG     ENDP
2117
2118 1997                                P_MSG     PROC      NEAR
2119 1997                                GT2A:
2120 1997 2E1 8A 04                       MOV     AL,CS:[S1]          ; PUT CHAR IN AL
2121 199A 46                             INC    SI                    ; POINT TO NEXT CHAR
2122 199B 50                             PUSH  AX                     ; SAVE PRINT CHAR
2123 199C E8 1969 R                       CALL   PRT_HEX              ; CALL VIDEO IO
2124 199F 58                             POP    AX                     ; RECOVER PRINT CHAR
2125 19A0 3C 0A                          CMP    AL,10                ; WAS IT LINE FEED?
2126 19A2 75 F3                         JNE    G12A                 ; NO,KEEP PRINTING STRING
2127 19A4 C3                          RET
2128 19A5                                P_MSG     ENDP
2129
2130
2131      ; THIS PROCEDURE WILL ISSUE LONG TONES (1-3/4 SECONDS) AND ONE OR ;
2132      ; MORE SHORT TONES (9/32 SECOND) TO INDICATE A FAILURE ON THE ;
2133      ; PLANAR BOARD, A BAD MEMORY MODULE, OR A PROBLEM WITH THE CRT. ;
2134      ; ENTRY PARAMETERS: ;
2135      ; DH = NUMBER OF LONG TONES TO BEEP ;
2136      ; DL = NUMBER OF SHORT TONES TO BEEP. ;
2137
-----
2138
2139 19A5                                ERR_BEEP   PROC      NEAR
2140 19A5 9C                             PUSHF                          ; SAVE FLAGS
2141 19A6 FA                             CLI                             ; DISABLE SYSTEM INTERRUPTS
2142 19A7 0A F6                          OR     DH,DH                 ; ANY LONG ONES TO BEEP
2143 19A9 74 1E                          JZ     G3                     ; NO, DO THE SHORT ONES
2144 19AB                                G1:
2145 19AB B3 70                          MOV     BL,112               ; COUNTER FOR LONG BEEPS (1-3/4 SECONDS)
2146 19AD B9 0500                         MOV     CX,1280              ; DIVISOR FOR 932 HZ
2147 19B0 EB 0505 R                       CALL   BEEP                  ; DO THE BEEP
2148 19B3 B9 C233                         MOV     CX,49715             ; 2/3 SECOND DELAY AFTER LONG BEEP
2149 19B6 EB 0CA0 R                       CALL   WAITF                 ; DELAY BETWEEN BEEPS
2150 19B9 FE CE                          DEC    DH                    ; ANY MORE LONG BEEPS TO DO
2151 19BB 75 EE                          JNZ    G1                     ; LOOP TILL DONE
2152 19BD 1E                             PUSH  DS                     ; SAVE DS REGISTER CONTENTS
2153 19BE EB 1A12 R                       CALL   DDS                   ;
2154 19C0 80 3E 0012 R 01                CMP    0MFG_TST,01H          ; MANUFACTURING TEST MODE?
2155 19C6 1F                             POP    DS                     ; RESTORE ORIGINAL CONTENTS OF (DS)
2156 19C7 74 BD                          JE     YFS                    ; YES - STOP BLINKING LED
2157 19C9                                G3:
2158 19C9                                G3:  MOV     BL,18                ; SHORT BEEPS
2159 19CB B9 04B8                         MOV     CX,208               ; DIVISOR FOR 937 HZ
2160 19CE E8 0C5C R                       CALL   BEEP                  ; DO THE SOUND

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2161 19D1 B9 8178      MOV     CX,33144      ; 1/2 SECOND DELAY AFTER SHORT BEEP
2162 19D4 E8 0CA0 R    CALL    WAITF        ; DELAY BETWEEN BEEPS
2163 19D7 FE CA       DEC     DU           ; DONE WITH SHORT BEEPS COUNT
2164 19D9 75 EE       JNZ     G3          ; LOOP TILL DONE
2165 19DB B9 8178      MOV     CX,33144      ; 1/2 SECOND DELAY AFTER LAST BEEP
2166 19DE E8 0CA0 R    CALL    WAITF        ; MAKE IT ONE SECOND DELAY BEFORE RETURN
2167 19E1 9D          POPF           ; RESTORE_FLAGS TO ORIGINAL SETTINGS
2168 19E2 C3          RET           ; RETURN TO CALLER
2169 19E3              ERR_BEEP      ENDP
2170
2171 -----
2172 ; THIS PROCEDURE WILL SEND A SOFTWARE RESET TO THE KEYBOARD. ;
2173 ; SCAN CODE 'AA' SHOULD BE RETURNED TO THE CPU. ;
2174 -----
2175 KBD_RESET          PROC    NEAR
2176                   ASSUME  DS:ABS0
2177 19E3 B0 08         MOV     AL,08H      ; SET KBD CLK LINE LOW
2178 19E5 E6 61         OUT     PORT_B,AL   ; WRITE 8255 PORT B
2179 19E7 B9 2956      MOV     CX,10582    ; HOLD KBD CLK LOW FOR 20 MS
2180 19EA              G8:
2181 19EA E2 FE         LOOP    G8          ; LOOP FOR 20 MS
2182 19EC B0 C8         MOV     AL,0CBH    ; SET CLK, ENABLE LINES HIGH
2183 19EE E6 61         OUT     PORT_B,AL
2184 19F0              SP_TEST:
2185 19F0 B0 48         MOV     AL,48H     ; ENTRY FOR MANUFACTURING TEST 2
2186 19F2 E6 61         OUT     PORT_B,AL   ; SET KBD CLK HIGH, ENABLE LOW
2187 19F4 B0 FD         MOV     AL,0FDH    ; ENABLE KEYBOARD INTERRUPTS
2188 19F6 E6 21         OUT     INTA01,AL  ; WRITE 8259 IMR
2189 19F8 C5 06 046B R 0 MOV     DATA_AREA[INTR_FLAG-DATA0],0 ; RESET INTERRUPT INDICATOR
2190 19FD FB          STI           ; ENABLE INTERRUPTS
2191 19FE 2B C9        SUB     CX,CX       ; SETUP INTERRUPT TIMEOUT CNT
2192 1A00              G9:
2193 1A00 F6 06 046B R 02 TEST    DATA_AREA[INTR_FLAG-DATA0],02H ; DID A KEYBOARD INTR OCCUR?
2194 1A05 75 02       JNZ     G10        ; YES - READ SCAN CODE RETURNED
2195 1A07 E2 F7       LOOP   G9          ; NO - LOOP TILL TIMEOUT
2196 1A09              G10:
2197 1A09 E4 60       IN     BL,PORT_A   ; READ KEYBOARD SCAN CODE
2198 1A0B 8A D8       MOV     BL,AL       ; SAVE SCAN CODE JUST READ
2199 1A0D B0 C8       MOV     AL,0CBH    ; CLEAR KEYBOARD
2200 1A0F E6 61       OUT     PORT_B,AL
2201 1A11 C3         RET           ; RETURN TO CALLER
2202 1A12              KBD_RESET  ENDP
2203
2204 1A12              DDS
2205 1A12 2E: 8E 1E 1A18 R MOV     DS,CS:DDSDATA ; LOAD (DS) TO DATA AREA
2206 1A17 C3         RET           ; PUT SEGMENT VALUE OF DATA AREA INTO DS
2207                   ; CS,TO USER WITH (DS)= DATA
2208 1A18 ----- R   DDSDATA DW     DATA ; SEGMENT SELECTOR VALUE FOR DATA AREA
2209
2210 1A1A              DDS
2211 1A1A              ENDP
2212
2213 ;--- HARDWARE INT 08 H --- ( IRQ LEVEL 0 ) -----
2214 ;
2215 ; THIS ROUTINE HANDLES THE TIMER INTERRUPT FROM FROM CHANNEL 0 OF
2216 ; THE 8254 TIMER. INPUT FREQUENCY IS 1.19318 MHZ AND THE DIVISOR
2217 ; IS 65536, RESULTING IN APPROXIMATELY 18.2 INTERRUPTS EVERY SECOND.
2218 ;
2219 ; THE INTERRUPT HANDLER MAINTAINS A COUNT (40:16C) OF INTERRUPTS SINCE
2220 ; POWER ON TIME, WHICH MAY BE USED TO ESTABLISH TIME OF DAY.
2221 ; THE INTERRUPT HANDLER ALSO DECREMENTS THE MOTOR CONTROL COUNT (40:40)
2222 ; OF THE DISKETTE, AND WHEN IT EXPIRES, WILL TURN OFF THE
2223 ; DISKETTE MOTOR(S), AND RESET THE MOTOR RUNNING FLAGS.
2224 ; THE INTERRUPT HANDLER WILL ALSO INVOKE A USER ROUTINE THROUGH
2225 ; INTERRUPT ICH AT EVERY TIME TICK. THE USER MUST CODE A
2226 ; ROUTINE AND PLACE THE CORRECT ADDRESS IN THE VECTOR TABLE.
2227 -----
2228 ASSUME  CS:CODE,DS:DATA
2229
2230 1A1A              TIMER_INT_1  PROC    NEAR
2231 1A1A FB          STI           ; INTERRUPTS BACK ON
2232 1A1B IE          PUSH    DS
2233 1A1C 50         PUSH    AX
2234 1A1D 52         PUSH    DX          ; SAVE MACHINE STATE
2235 1A1E 98 ----- R MOV     AX,DATA     ; GET ADDRESS OF DATA SEGMENT
2236 1A23 FF 06 006C R INC     *TIMER_LOW  ; ESTABLISH ADDRESSABILITY
2237 1A27 75 04     JNZ     T4          ; INCREMENT TIME
2238 1A29 FF 06 006E R INC     *TIMER_HIGH ; GO TO TEST DAY
2239 1A2D           JNZ     T4          ; INCREMENT HIGH WORD OF TIME
2240 1A2D 83 3E 006E R 18 CMP     *TIMER_HIGH,018H ; TEST FOR COUNT EQUALING 24 HOURS
2241 1A32 75 19     JNZ     T5          ; GO TO DISKETTE_CTL
2242 1A34 81 3E 006C R 00B0 CMP     *TIMER_LOW,0B0H ;
2243 1A3A 75 11     JNZ     T5          ; GO TO DISKETTE_CTL
2244
2245 ;----- TIMER HAS GONE 24 HOURS
2246
2247 1A3C 2B C0     SUB     AX,AX
2248 1A3E A3 006E R MOV     *TIMER_HIGH,AX ; CLEAR TIMER COUNT HIGH
2249 1A41 A3 006C R MOV     *TIMER_LOW,AX  ; AND LOW
2250 1A44 C6 06 0070 R 01 MOV     *TIMER_OFL,1  ; AND LOW ELAPSED 24 HOURS FLAG
2251 1A49 FF 06 00CE R INC     *DAY_COUNT   ; INCREMENT ELAPSED DAY COUNTER
2252
2253 ;----- TEST FOR DISKETTE TIME OUT
2254
2255 1A4D              T5:
2256 1A4D FE 0E 0040 R DEC     *MOTOR_COUNT ; DECREMENT DISKETTE MOTOR CONTROL
2257 1A51 75 0B     JNZ     T6          ; RETURN IF COUNT NOT 0
2258 1A53 80 26 003F R 0 AND     *MOTOR_STATUS,0F0H ; TURN OFF MOTOR RUNNING BITS
2259 1A58 B0 0C     MOV     AL,0CH      ; FDC CTL PORT
2260 1A5A BA 03F2    MOV     DX,03F2H    ; FDC CTL PORT
2261 1A5D EE         OUT     DX,AL       ; TURN OFF THE MOTOR
2262
2263 1A5E              T6:
2264 1A5E CD 1C     INT     ICH         ; TIMER TICK INTERRUPT
2265                   ; TRANSFER CONTROL TO A USER ROUTINE
2266 1A60 FA        CLI           ; DISABLE INTERRUPTS TILL STACK CLEARED
2267 1A61 B0 20     MOV     AL,E01      ; GET END OF INTERRUPT MASK
2268 1A63 E4 20     OUT     INTA00,AL   ; END OF INTERRUPT TO 8259 - 1
2269 1A65 5A        POP     DX          ; RESTORE (DX)
2270 1A66 58        POP     AX
2271 1A67 1F        POP     DS          ; RESET MACHINE STATE
2272 1A68 CF        IRET          ; RETURN FROM INTERRUPT
2273
2274 1A69              TIMER_INT_1  ENDP

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SECTION 5

2275		PAGE			
2276					
2277					
2278					
2279					
2280	1A6E				
2281	1A6E				
2282	1A6E 00 00 00 00 00 00				
2283	00 00 00 00 00 00				
2284	1A76 7E 81 A5 81 BD 99				
2285	81 7E				
2286	1A7E 7E FF DB FF C3 E7				
2287	FF FE				
2288	1A86 6C FE FE FE 7C 38				
2289	10 00				
2290	1A8E 10 00 7C FE 7C 38				
2291	10 00				
2292	1A96 38 7C 38 FE FE 7C				
2293	38 7C				
2294	1A9E 10 10 3C 7C FE 7C				
2295	38 7C				
2296	1AA6 00 18 3C 3C 18				
2297	00 00				
2298	1AAE FF E7 C3 C3 E7				
2299	FF FF				
2300	1AB6 00 3C 66 42 42 66				
2301	10 00				
2302	1ABE FF C3 99 BD BD 99				
2303	C3 FF				
2304	1AC6 0F 07 0F 7D CC CC				
2305	CC 78				
2306	1ACE 3C 66 66 66 3C 18				
2307	7E 18				
2308	1AD6 3F 33 3F 30 30 70				
2309	FF 00				
2310	1ADE 7F 63 7F 63 63 67				
2311	E6 C0				
2312	1AE6 99 5A 3C E7 ET 3C				
2313	5A 99				
2314					
2315	1AEE 80 E0 F8 FE F8 E0				
2316	80 00				
2317	1AF6 00 3E FE 3E 0E				
2318	02 00				
2319	1AFE 18 3C 7E 18 18 7E				
2320	3C 18				
2321	1B06 66 66 66 66 66 00				
2322	66 00				
2323	1B0E 7F DB DB 7B 1B 1B				
2324	1B 00				
2325	1B16 3E 63 38 6C 6C 38				
2326	CC 78				
2327	1B1E 00 00 00 00 7E 7E				
2328	7E 00				
2329	1B26 18 3C 7E 18 7E 3C				
2330	18 FF				
2331	1B2E 18 3C 7E 18 18 18				
2332	18 00				
2333	1B36 18 18 18 18 7E 3C				
2334	18 00				
2335	1B3E 00 18 0C FE 0C 18				
2336	00 00				
2337	1B46 00 30 60 FE 60 30				
2338	00 00				
2339	1B4E 00 00 C0 C0 C0 FE				
2340	00 00				
2341	1B56 00 24 66 FF 66 24				
2342	00 00				
2343	1B5E 00 18 3C 7E FF FF				
2344	00 00				
2345	1B66 00 FF FE 7C 3C 18				
2346	00 00				
2347					
2348	1B6E 00 00 00 00 00 00				
2349	00 00				
2350	1B76 30 78 78 30 30 00				
2351	30 00				
2352	1B7E 6C 6C 6C 00 00 00				
2353	00 00				
2354	1B86 6C 6C FE 6C FE 6C				
2355	6C 00				
2356	1B8E 30 7C C0 78 0C F8				
2357	00 00				
2358	1B96 00 C6 CC 18 30 66				
2359	C6 00				
2360	1B9E 38 6C 38 78 0C 00				
2361	76 00				
2362	1BA6 60 60 60 60 60 00				
2363	00 00				
2364	1BAE 18 30 60 60 60 30				
2365	18 00				
2366	1BB6 60 30 18 18 18 30				
2367	60 00				
2368	1BBE 00 66 3C FF 3C 66				
2369	00 00				
2370	1BC6 00 30 30 FC 30 30				
2371	00 00				
2372	1BCE 00 00 00 00 00 30				
2373	30 60				
2374	1BD6 00 00 FC 00 00 00				
2375	00 00				
2376	1BDE 00 00 00 00 00 30				
2377	30 00				
2378	1BE6 06 0C 18 30 60 C0				
2379	80 00				
2380					
2381	1BEE 7C C6 CE DE F6 E6				
2382	7C 00				
2383	1BF6 30 70 30 30 30 30				
2384	FC 00				
2385	1BF7 78 CC 0C 38 60 CC				
2386	FC 00				
2387	1C06 78 CC 0C 38 0C CC				
2388	78 00				

2389	1C0E	1C	3C	6C	CC	FE	0C	DB	01CH,03CH,06CH,0CCH,0FEH,00CH,01EH,000H ; D_34	4
2390		IE	00							
2391	1C16	F8	00	F8	0C	0C	CC	DB	0FCH,0C0H,0F8H,00CH,00CH,0CCH,078H,000H ; D_35	5
2392		78	00							
2393	1C1E	38	60	C0	F8	CC	CC	DB	038H,060H,0C0H,0F8H,0CCH,0CCH,078H,000H ; D_36	6
2394		78	00							
2395	1C26	FC	00	0C	18	30	30	DB	0FCH,0CCH,00CH,018H,030H,030H,030H,000H ; D_37	7
2396		30	00							
2397	1C2E	78	CC	CC	78	CC	CC	DB	078H,0CCH,0CCH,078H,0CCH,0CCH,078H,000H ; D_38	8
2398		78	00							
2399	1C36	F8	00	CC	7C	0C	18	DB	078H,0CCH,0CCH,07CH,00CH,018H,070H,000H ; D_39	9
2400		70	00							
2401	1C3E	00	30	30	00	00	30	DB	000H,030H,030H,000H,000H,030H,030H,000H ; D_3A	:
2402		30	00							COLON
2403	1C46	00	30	30	00	00	30	DB	000H,030H,030H,000H,000H,030H,030H,060H ; D_3B	<
2404		30	60							SEMICOLON
2405	1C4E	18	30	C0	60	C0	60	DB	018H,030H,060H,0C0H,060H,030H,018H,000H ; D_3C	<
2406		18	00							LESS THAN
2407	1C56	00	00	FC	00	00	FC	DB	000H,000H,0FCH,000H,000H,0FCH,000H,000H ; D_3D	=
2408		00	00							EQUAL
2409	1C5E	60	30	18	0C	18	30	DB	060H,030H,018H,00CH,018H,030H,060H,000H ; D_3E	>
2410		60	00							GREATER THAN
2411	1C66	78	CC	0C	18	30	00	DB	078H,0CCH,00CH,018H,030H,000H,030H,000H ; D_3F	?
2412		30	00							QUESTION MARK
2413										
2414	1C6E	7C	C6	DE	DE	DE	C0	DB	07CH,0C6H,0DEH,0DEH,0DEH,0C0H,078H,000H ; D_40	*
2415		78	00							AT
2416	1C76	30	78	CC	CC	FC	CC	DB	030H,078H,0CCH,0CCH,0FCH,0CCH,0CCH,000H ; D_41	A
2417		CC	00							
2418	1C7E	FC	66	66	7C	66	66	DB	0FCH,066H,066H,07CH,066H,066H,0FCH,000H ; D_42	B
2419		FC	00							
2420	1C86	3C	66	C0	C0	C0	66	DB	03CH,066H,0C0H,0C0H,0C0H,066H,03CH,000H ; D_43	C
2421		3C	00							
2422	1C8E	F8	6C	66	66	66	6C	DB	0F8H,06CH,066H,066H,066H,06CH,0F8H,000H ; D_44	D
2423		F8	00							
2424	1C96	FE	62	68	78	68	62	DB	0FEH,062H,068H,078H,068H,062H,0FEH,000H ; D_45	E
2425		FE	00							
2426	1C9E	FE	62	68	78	68	60	DB	0FEH,062H,068H,078H,068H,060H,0F0H,000H ; D_46	F
2427		F0	00							
2428	1CA6	3C	66	C0	C0	CE	66	DB	03CH,066H,0C0H,0C0H,0CEH,066H,03CH,000H ; D_47	G
2429		3E	00							
2430	1CAE	CC	CC	CC	FC	CC	CC	DB	0CCH,0CCH,0CCH,0FCH,0CCH,0CCH,0CCH,000H ; D_48	H
2431		CC	00							
2432	1CB6	78	30	30	30	30	30	DB	078H,030H,030H,030H,030H,030H,078H,000H ; D_49	I
2433		78	00							
2434	1CBE	1E	0C	0C	0C	CC	CC	DB	01EH,00CH,00CH,00CH,0CCH,0CCH,078H,000H ; D_4A	J
2435		78	00							
2436	1CC6	E6	66	6C	78	6C	66	DB	0E6H,066H,06CH,078H,06CH,066H,066H,000H ; D_4B	K
2437		E6	00							
2438	1CCE	F0	60	60	60	62	66	DB	0F0H,060H,060H,060H,062H,066H,0FEH,000H ; D_4C	L
2439		FE	00							
2440	1CD6	C6	C6	FE	FE	D6	C6	DB	0C6H,0EEH,0FEH,0FEH,0D6H,0C6H,0C6H,000H ; D_4D	M
2441		C6	00							
2442	1CDE	C6	E6	F6	D6	C6	C6	DB	0C6H,0E6H,0F6H,0DEH,0CEH,0C6H,0C6H,000H ; D_4E	N
2443		C6	00							
2444	1CE6	38	6C	C6	C6	C6	6C	DB	038H,06CH,0C6H,0C6H,0C6H,06CH,038H,000H ; D_4F	O
2445		38	00							
2446										
2447	1CEE	FC	66	66	7C	60	60	DB	0FCH,066H,066H,07CH,060H,060H,0F0H,000H ; D_50	P
2448		F0	00							
2449	1CF6	78	CC	CC	CC	DC	78	DB	078H,0CCH,0CCH,0CCH,0DCCH,078H,01CH,000H ; D_51	Q
2450		1C	00							
2451	1CFE	FC	66	6C	78	6C	66	DB	0FCH,066H,066H,07CH,06CH,066H,066H,000H ; D_52	R
2452		E6	00							
2453	1D06	78	CC	E0	70	1C	CC	DB	078H,0CCH,0E0H,070H,01CH,0CCH,078H,000H ; D_53	S
2454		78	00							
2455	1D0E	FC	84	30	30	30	30	DB	0FCH,0B4H,030H,030H,030H,030H,078H,000H ; D_54	T
2456		78	00							
2457	1D16	CC	CC	CC	CC	CC	CC	DB	0CCH,0CCH,0CCH,0CCH,0CCH,0CCH,0FCH,000H ; D_55	U
2458		FC	00							
2459	1D1E	CC	CC	CC	CC	CC	78	DB	0CCH,0CCH,0CCH,0CCH,0CCH,078H,030H,000H ; D_56	V
2460		30	00							
2461	1D26	C6	C6	C6	D6	FE	EE	DB	0C6H,0C6H,0C6H,0D6H,0FEH,0EEH,0C6H,000H ; D_57	W
2462		C6	00							
2463	1D2E	C6	C6	6C	38	38	6C	DB	0C6H,0C6H,06CH,038H,038H,06CH,0C6H,000H ; D_58	X
2464		C6	00							
2465	1D36	CC	CC	CC	78	30	30	DB	0CCH,0CCH,0CCH,078H,030H,030H,078H,000H ; D_59	Y
2466		78	00							
2467	1D3E	FE	C6	8C	18	32	66	DB	0FEH,0C6H,08CH,018H,032H,066H,0FEH,000H ; D_5A	Z
2468		FE	00							
2469	1D46	78	60	60	60	60	60	DB	078H,060H,060H,060H,060H,060H,078H,000H ; D_5B	[
2470		78	00							LEFT BRACKET
2471	1D4E	C0	60	30	18	0C	06	DB	0C0H,060H,030H,018H,00CH,006H,002H,000H ; D_5C	\
2472		02	00							BACKSLASH
2473	1D56	78	18	18	18	18	18	DB	078H,018H,018H,018H,018H,018H,078H,000H ; D_5D]
2474		78	00							RIGHT BRACKET
2475	1D5E	10	78	38	C6	00	00	DB	010H,038H,06CH,0C6H,000H,000H,000H,000H ; D_5E	^
2476		00	00							CIRCUMFLEX
2477	1D66	00	00	00	00	00	00	DB	000H,000H,000H,000H,000H,000H,000H,0FFH ; D_5F	_
2478		00	FF							UNDERSCORE
2479										
2480	1D6E	30	30	18	00	00	00	DB	030H,030H,018H,000H,000H,000H,000H,000H ; D_60	'
2481		00	00							APOSTROPHE REV
2482	1D76	00	00	78	0C	7C	CC	DB	000H,000H,078H,00CH,07CH,0CCH,076H,000H ; D_61	a
2483		76	00							
2484	1D7E	E0	60	60	7C	66	66	DB	0E0H,060H,060H,07CH,066H,066H,0DCH,000H ; D_62	b
2485		DC	00							
2486	1D86	00	00	78	CC	C0	CC	DB	010H,000H,078H,0CCH,0C0H,0CCH,078H,000H ; D_63	c
2487		78	00							
2488	1D8E	1C	0C	0C	7C	CC	CC	DB	00CH,00CH,00CH,07CH,0CCH,0CCH,076H,000H ; D_64	d
2489		76	00							
2490	1D96	00	00	78	CC	FC	C0	DB	000H,000H,078H,0CCH,0FCH,0C0H,078H,000H ; D_65	e
2491		78	00							
2492	1D9E	38	6C	F0	F0	60	60	DB	038H,06CH,060H,0F0H,060H,060H,0F0H,000H ; D_66	f
2493		F0	00							
2494	1DA6	00	00	76	CC	CC	7C	DB	000H,000H,076H,0CCH,0CCH,07CH,00CH,0F8H ; D_67	g
2495		0C	F8							
2496	1DAE	E0	60	6C	76	66	66	DB	0E0H,060H,06CH,076H,066H,066H,066H,066H ; D_68	h
2497		E6	00							
2498	1DB6	30	00	70	30	30	30	DB	030H,000H,070H,030H,030H,030H,078H,000H ; D_69	i
2499		78	00							
2500	1DBE	0C	00	0C	0C	0C	CC	DB	00CH,000H,00CH,00CH,00CH,0CCH,0CCH,078H ; D_6A	j
2501		C	78							
2502	1DC6	E0	60	66	6C	78	6C	DB	0E0H,060H,066H,06CH,078H,06CH,0E6H,000H ; D_6B	k

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2503      E6 00
2504 1DCE 70 30 30 30 30 30      DB      070H,030H,030H,030H,030H,030H,078H,000H ; D_6C l
2505      78 00
2506 1DD6 00 00 CC FE FE D6      DB      000H,000H,0CCH,0FEH,0FEH,0D6H,0C6H,000H ; D_6D m
2507      C6 00
2508 1DDE 00 00 F8 CC CC CC      DB      000H,000H,0F8H,0CCH,0CCH,0CCH,0CCH,000H ; D_6E n
2509      CC 00
2510 1DE6 00 00 78 CC CC CC      DB      000H,000H,078H,0CCH,0CCH,0CCH,078H,000H ; D_6F o
2511      78 00
2512
2513 1DEE 00 00 DC 66 66 7C      DB      000H,000H,0DCH,066H,066H,07CH,060H,0F0H ; D_70 p
2514      66 F0
2515 1DF6 00 00 76 CC CC 7C      DB      000H,000H,076H,0CCH,0CCH,07CH,00CH,01EH ; D_71 q
2516      0C 1E
2517 1DFE 00 00 DC 76 66 60      DB      000H,000H,0DCH,076H,066H,060H,0F0H,000H ; D_72 r
2518      F0 00
2519 1E06 00 00 7C C0 78 0C      DB      000H,000H,07CH,0C0H,078H,00CH,0F8H,000H ; D_73 s
2520      F8 00
2521 1E0E 10 30 7C 30 30 34      DB      010H,030H,07CH,030H,030H,034H,018H,000H ; D_74 t
2522      18 00
2523 1E16 00 00 CC CC CC CC      DB      000H,000H,0CCH,0CCH,0CCH,0CCH,076H,000H ; D_75 u
2524      76 00
2525 1E1E 00 00 CC CC CC 78      DB      000H,000H,0CCH,0CCH,0CCH,078H,030H,000H ; D_76 v
2526      30 00
2527 1E26 00 00 C6 D6 FE FE      DB      000H,000H,0C6H,0D6H,0FEH,0FEH,06CH,000H ; D_77 w
2528      6C 00
2529 1E2E 00 00 C6 6C 38 6C      DB      000H,000H,0C6H,06CH,038H,06CH,0C6H,000H ; D_78 x
2530      C6 00
2531 1E36 00 00 CC CC CC 7C      DB      000H,000H,0CCH,0CCH,0CCH,07CH,00CH,0F8H ; D_79 y
2532      0C F8
2533 1E3E 00 00 FC 98 30 64      DB      000H,000H,0FCH,098H,030H,064H,0FCH,000H ; D_7A z
2534      FC 00
2535 1E46 1C 30 30 E0 30 30      DB      01CH,030H,030H,0E0H,030H,030H,01CH,000H ; D_7B { LEFT BRACE
2536      1C 00
2537 1E4E 18 18 18 00 18 18      DB      018H,018H,018H,000H,018H,018H,018H,000H ; D_7C | BROKEN STROKE
2538      18 00
2539 1E56 E0 30 30 1C 30 30      DB      0E0H,030H,030H,01CH,030H,030H,0E0H,000H ; D_7D } RIGHT BRACE
2540      E0 00
2541 1E5E 76 DC 00 00 00 00      DB      076H,0DCH,000H,000H,000H,000H,000H ; D_7E ~ TILDE
2542      00 00
2543 1E66 00 10 38 6C C6 C6      DB      000H,010H,038H,06CH,0C6H,0C6H,0FEH,000H ; D_7F DELTA
2544      FE 00
  
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2545 PAGE
2546 ;--- INT 1A -----
2547 ; TIME OF DAY
2548 ; THIS ROUTINE ALLOWS THE CLOCK TO BE SET/READ
2549 ;
2550 ; INPUT
2551 ; (AH) = 0 READ THE CURRENT CLOCK SETTING
2552 ; RETURNS CX = HIGH PORTION OF COUNT
2553 ; DX = LOW PORTION OF COUNT
2554 ; AL = 0 IF TIMER HAS NOT PASSED
2555 ; 24 HOURS SINCE LAST READ
2556 ; <0 IF ON ANOTHER DAY
2557 ; (AH) = 1 SET THE CURRENT CLOCK
2558 ; CX = HIGH PORTION OF COUNT
2559 ; DX = LOW PORTION OF COUNT
2560 ; NOTE: COUNTS OCCUR AT THE RATE OF
2561 ; 11931807/65536 COUNTS/SEC
2562 ; (OR ABOUT 18.2 PER SECOND -- SEE EQUATES BELOW)
2563 -----
2564 ; ASSUME CS:CODE,DS:DATA
2565 ; ORG 0FE6EH
2566 ; ORG 01EE6H
2567 TIME_OF_DAY:
2568 IE6E JMP TIME_OF_DAY_11
2569
2570 ; ORG 0FEA5H
2571 IEA5 ; ORG 01EA5H
2572 IEA5 ;
2573 IEA5 E9 1A1A R JMP TIMER_INT_1
2574
2575 ;-----
2576 ; THESE ARE THE VECTORS WHICH ARE MOVED INTO
2577 ; THE 8086 INTERRUPT ARE DURING POWER ON.
2578 ; ONLY THE OFFSETS ARE DISPLAYED HERE, CODE
2579 ; SEGMENT WILL BE ADDED FOR ALL OF THEM, EXCEPT
2580 ; WHERE NOTED.
2581 -----
2582 ; ASSUME CS:CODE
2583 ; ORG 0FEF3H
2584 IEF3 ; ORG 01EF3H
2585 IEF3 VECTOR_TABLE LABEL WORD ; VECTOR TABLE FOR POST TESTS
2586 IEF3 IEA5 R DW OFFSET TIMER_INT ; INT 08H - HARDWARE TIMER 0 IRQ 0
2587 IEF5 0987 R DW OFFSET KB_INT ; INT 09H - KEYBOARD IRQ 1
2588 IEF7 1F23 R DW OFFSET DT ; INT 0AH - DISKETTE IRQ 2
2589 IEF9 1F23 R DW OFFSET D11 ; INT 0BH - IRQ 3
2590 IEFB 1F23 R DW OFFSET D11 ; INT 0CH - IRQ 4
2591 IEFD 1F23 R DW OFFSET D11 ; INT 0DH - IRQ 5
2592 IEFF 0F57 R DW OFFSET DISK_INT ; INT 0EH - DISKETTE IRQ 6
2593 IF01 1F23 R DW OFFSET D11 ; INT 0FH - IRQ 7
2594
2595 ;----- SOFTWARE INTERRUPTS ( BIOS CALLS AND POINTERS )
2596 -----
2597 IF03 1065 R DW OFFSET VIDEO_IO ; INT 10H -- VIDEO DISPLAY
2598 IF05 184D R DW OFFSET EQUIPMENT ; INT 11H -- GET EQUIPMENT FLAG WORD
2599 IF07 1841 R DW OFFSET MEMORY_SIZE_DET ; INT 12H -- GET REAL MODE MEMORY SIZE
2600 IF09 0059 R DW OFFSET DISKETTE_IO ; INT 13H -- DISKETTE
2601 IF0B 0739 R DW OFFSET RS232_IO ; INT 14H -- COMMUNICATION ADAPTER
2602 IF0D 1859 R DW OFFSET CASSETTE_IO ; INT 15H -- EXPANDED BIOS FUNCTION CALL
2603 IF0F 082E R DW OFFSET KEYBOARD_IO ; INT 16H -- KEYBOARD INPUT
2604 IF11 0FD2 R DW OFFSET PRINTER_IO ; INT 17H -- PRINTER OUTPUT
2605 IF13 0000H DW 00000H ; INT 18H -- 0F600H INSERTED FOR BASIC
2606 IF15 06F2 R DW OFFSET BOOT_STRAP ; INT 19H -- BOOT FROM SYSTEM MEDIA
2607 IF17 1E6E R DW OFFSET TIME_OF_DAY ; INT 1AH -- TIME OF DAY
2608 IF19 1F49 R DW OFFSET DUMMY_RETURN ; INT 1BH -- KEYBOARD BREAK ADDRESS
2609 IF1B 1F49 R DW OFFSET DUMMY_RETURN ; INT 1CH -- TIMER BREAK ADDRESS
2610 IF1D 10A4 R DW OFFSET VIDEO_PARAMS ; INT 1DH -- VIDEO PARAMETERS
2611 IF1F 0FC7 R DW OFFSET DISK_BASE ; INT 1EH -- DISKETTE PARAMETERS
2612 IF21 0000H DW 00000H ; INT 1FH -- POINTER TO VIDEO EXTENSION
2613
2614 ;-----
2615 ; TEMPORARY INTERRUPT SERVICE ROUTINE
2616 ; 1. THIS ROUTINE IS ALSO LEFT IN PLACE AFTER THE
2617 ; POWER ON DIAGNOSTICS TO SERVICE UNUSED
2618 ; INTERRUPT VECTORS. LOCATION 'INTR_FLAG' WILL
2619 ; CONTAIN EITHER: 1. LEVEL OF HARDWARE INT. THAT
2620 ; CAUSED CODE TO BE EXEC.
2621 ; 2. 'FF' FOR NON-HARDWARE INTERRUPTS THAT WAS
2622 ; EXECUTED ACCIDENTLY.
2623 -----
2624 IF23 D11 PROC NEAR
2625 ; ASSUME DS:DATA
2626 IF23 IE PUSH DS
2627 IF24 E8 1A12 R CALL DDS
2628 IF27 50 PUSH AX ; SAVE REG AX CONTENTS
2629 IF28 B0 0B MOV AL,0BH ; READ IN-SERVICE REG
2630 IF2A E6 20 JUT INTA00,AL ; IF IND OUT WHAT LEVEL BEING
2631 IF2C 90 NOP ; SERVICED)
2632 IF2D E4 20 IN AL,INTA00 ; GET LEVEL
2633 IF2F 8A E0 MOV AH,AL ; SAVE IT
2634 IF31 0A C4 OR AL,AH ; 007 (NO HARDWARE ISR ACTIVE)
2635 IF33 75 04 JNZ HW_INT
2636 IF35 B4 FF MOV AH,OFFH
2637 IF37 EB 0A JMP SHORT SET_INTR_FLAG ; SET FLAG TO FF IF NON-HDWARE
2638 IF39
2639 IF39 E4 21 IN AL,INTA01 ; GET MASK VALUE
2640 IF3B 0A C4 OR AL,AH ; MASK OFF LVL BEING SERVICED
2641 IF3D E6 21 JUT INTA01,AL
2642 IF3F B0 20 MOV AL,E01
2643 IF41 E6 20 OUT INTA00,AL
2644 IF43 SET_INTR_FLAG: MOV @INTR_FLAG,AH ; SET FLAG
2645 IF43 88 26 006B R POP AX ; RESTORE REG AX CONTENTS
2646 IF47 58 POP DS
2647 IF48 1F POP DS ; NEED IRET FOR VECTOR TABLE
2648 IF49 DUMMY_RETURN: IRET
2649 IF49 CF D11 ENDP
2650 IF4A
2651
2652 ;-----
2653 ; DUMMY RETURN FOR ADDRESS COMPATIBILITY
2654 ;-----
2655 ; ORG 0FF53H
2656 IEF3 ; ORG 01F53H
2657 IEF3 CF IRET
    
```

SECTION 5

```

2658                                     PAGE
2659 ----- INT 05 H -----
2660 ; PRINT SCREEN
2661 ; THIS LOGIC WILL BE INVOKED BY INTERRUPT 05H TO PRINT THE SCREEN.
2662 ; THE CURSOR POSITION AT THE TIME THIS ROUTINE IS INVOKED WILL BE
2663 ; SAVED AND RESTORED UPON COMPLETION.  THE ROUTINE IS INTENDED TO
2664 ; RUN WITH INTERRUPTS ENABLED.  IF A SUBSEQUENT PRINT SCREEN KEY
2665 ; IS DEPRESSED WHILE THIS ROUTINE IS PRINTING IT WILL BE IGNORED.
2666 ; THE BASE PRINTERS STATUS IS CHECKED FOR NOT BUSY AND NOT OUT OF
2667 ; PAPER.  AN INITIAL STATUS ERROR WILL ABEND THE PRINT REQUEST.
2668 ; ADDRESS 0050:0000 CONTAINS THE STATUS OF THE PRINT SCREEN.
2669
2670                                     50:0 = 0   PRINT SCREEN HAS NOT BEEN CALLED OR UPON RETURN
2671                                     = 1   FROM A CALL THIS INDICATES A SUCCESSFUL OPERATION.
2672                                     = 2   PRINT SCREEN IS IN PROGRESS - IGNORE THIS REQUEST.
2673                                     = 255  ERROR ENCOUNTERED DURING PRINTING.
2674 -----
2675
2676 IF54 ORG OFF54H
2677 ORG 01F54H
2678
2679 PRINT_SCREEN_1 PROC FAR
2680 ; DELAY INTERRUPT ENABLE TILL FLAG SET
2681 ; USE 0040:0100 FOR STATUS AREA STORAGE
2682 ; GET STATUS BYTE DATA SEGMENT
2683 ; SEE IF PRINT ALREADY IN PROGRESS
2684 ; EXIT IF PRINT ALREADY IN PROGRESS
2685 ; INDICATE PRINT NOW IN PROGRESS
2686 ; MUST RUN WITH INTERRUPTS ENABLED
2687 ; SAVE WORK REGISTERS
2688
2689 PUSH DS
2690 CALL DD5
2691 CMP *STATUS_BYTE,1
2692 JE PRI190
2693 MOV *STATUS_BYTE,1
2694 STI
2695 PUSH AX
2696 PUSH BX
2697 PUSH CX
2698 PUSH DX
2699 MOV AH,0FH
2700 INT 10H
2701 ; WILL REQUEST THE CURRENT SCREEN MODE
2702 ; (AL)= MODE
2703 ; (AH)= NUMBER COLUMNS/LINE
2704 ; (BH)= VISUAL PAGE
2705 ; WILL MAKE USE OF (CX) REGISTER TO
2706 ; CONTROL ROWS ON SCREEN & COLUMNS
2707 ; ADJUST ROWS ON DISPLAY COUNT
2708 ; (CL)= NUMBER COLUMNS/LINE
2709 ; (CH)= NUMBER OF ROWS ON DISPLAY
2710
2711 ;-----
2712 ; AT THIS POINT WE KNOW THE COLUMNS/LINE COUNT IS IN (CL) ;
2713 ; AND THE NUMBER OF ROWS ON THE DISPLAY IS IN (CH) ;
2714 ; THE PAGE IF APPLICABLE IS IN (BH).  THE STACK HAS ;
2715 ; (DS), (AX), (BX), (CX), (DX) PUSHED. ;
2716 ;-----
2717
2718 XOR DX,DX ; FIRST PRINTER
2719 MOV AH,02H ; SET PRINTER STATUS REQUEST COMMAND
2720 INT 17H ; REQUEST CURRENT PRINTER STATUS
2721 XOR AH,080H ; CHECK FOR PRINTER BUSY (NOT CONNECTED)
2722 OR AL,AL ; OR OUT OF PAPER
2723 JNZ PRI170 ; ERROR EXIT IF PRINTER STATUS ERROR
2724
2725 CALL CRLF ; CARRIAGE RETURN LINE FEED TO PRINTER
2726 PUSH CX ; SAVE SCREEN BOUNDS
2727 MOV AH,03H ; NOW READ THE CURRENT CURSOR POSITION
2728 INT 10H ; AND RESTORE AT END OF ROUTINE
2729 OR CX ; RECALL SCREEN BOUNDS
2730 PUSH DX ; PRESERVE THE ORIGINAL POSITION
2731 XOR DX,DX ; INITIAL CURSOR (0,0) AND FIRST PRINTER
2732
2733 ;-----
2734 ; THIS LOOP IS TO READ EACH CURSOR POSITION FROM THE ;
2735 ; SCREEN AND PRINT IT. (BH)= VISUAL PAGE (CH)= ROWS ;
2736 ;-----
2737
2738 PRI110: MOV AH,02H ; INDICATE CURSOR SET REQUEST
2739 INT 10H ; NEW CURSOR POSITION ESTABLISHED
2740 MOV AH,08H ; INDICATE READ CHARACTER FROM DISPLAY
2741 INT 10H ; CHARACTER NOW IN (AL)
2742 OR AL,AL ; SEE IF VALID CHAR
2743 JNZ PRI120 ; JUMP IF VALID CHAR
2744 MOV AL,' ' ; ELSE MAKE IT A BLANK
2745
2746 PRI120: PUSH DX ; SAVE CURSOR POSITION
2747 XOR DX,DX ; INDICATE FIRST PRINTER (DX= 0)
2748 OR AH,AH ; INDICATE PRINT CHARACTER IN (AL)
2749 INT 17H ; PRINT THE CHARACTER
2750 OR DL,DL ; RECALL CURSOR POSITION
2751 TEST AH,29H ; TEST FOR PRINTER ERROR
2752 JNZ PRI160 ; EXIT IF ERROR DETECTED
2753 INC DL ; ADVANCE TO NEXT COLUMN
2754 CMP DL,DL ; SEE IF AT END OF LINE
2755 JNZ PRI110 ; IF NOT LOOP FOR NEXT COLUMN
2756 XOR DL,DL ; BACK TO COLUMN 0
2757 ; (AH)=0
2758 ; SAVE NEW CURSOR POSITION
2759 CALL CRLF ; LINE FEED CARRIAGE RETURN
2760 POP DX ; RECALL CURSOR POSITION
2761 INC DL ; ADVANCE TO NEXT LINE
2762 CMP CH,CH ; FINISHED?
2763 JNZ PRI110 ; IF NOT LOOP FOR NEXT LINE
2764
2765 POP DX ; GET CURSOR POSITION
2766 MOV AH,02H ; INDICATE REQUEST CURSOR SET
2767 INT 10H ; CURSOR POSITION RESTORED
2768 CLD ; BLOCK INTERRUPTS TILL STACK CLEARED
2769 MOV *STATUS_BYTE,0 ; MOVE OK RESULTS FLAG TO STATUS_BYTE
2770 JMP SHORT PRI180 ; EXIT PRINTER ROUTINE

```

```

2757
2758 IFCC
2759 IFCC 5A
2760 IFCD B4 02
2761 IFCE CD 10
2762 IFDI
2763 IFDI FA
2764 IFDE C6 06 0100 R FF
2765 IFDI
2766 IFDI 5A
2767 IFDI 59
2768 IFDI 5B
2769 IFDI 5B
2770 IFDI
2771 IFDI 1F
2772 IFDI CF
2773 IFDI
2774
2775
2776
2777 IFDI
2778
2779 IFDI 33 D2
2780 IFDI B8 000D
2781 IFDI CD 17
2782 IFDI B8 000A
2783 IFDI CD 17
2784 IFDI C3
2785 IFDI
2786
2787
2788
2789
2790
2791 IFDI
2792
2793
2794 IFDI
2795 IFDI EA
2796 IFDI E05B
2797 IFDI F000
2798
2799 IFDI 30 31 2F 31 30 2F
2800
2801
2802
2803 IFDI
2804 IFDI
2805 IFDI FB
2806
2807 IFDI
2808

PAGE
PR160:
POP DX
MOV AH,02H
INT 10H
PR170:
CLI
MOV @STATUS_BYTE,0FFH
PR180:
POP DX
POP CX
POP BX
POP AX
PR190:
POP DS
IRET
PRINT_SCREEN_1 ENDP
;----- CARRIAGE RETURN, LINE FEED SUBROUTINE
CRLF PROC NEAR
; SEND CR,LF TO FIRST PRINTER
; ASSUME FIRST PRINTER (DX= 0)
; GET THE PRINT CHARACTER COMMAND AND
; THE CARRIAGE RETURN CHARACTER
; NOW GET THE LINE FEED AND
; SEND IT TO THE BIOS PRINTER ROUTINE
XOR DX,DX
MOV AX,CR
INT 17H
MOV AX,LF
INT 17H
RET
CRLF ENDP
;-----
;----- POWER ON RESET VECTOR ;
;-----
; ORG 0FFFOH
; ORG 01FF0H
;----- POWER ON RESET
P_O_R LABEL FAR
DB 0EAH
DW 0E05BH ; LOW WORD OF RESET
DW 0F000H ; SEGMENT
DB '01/10/86' ; RELEASE MARKER
; ORG 0FFFEH
; ORG 01FFFEH
MODEL: DB MODEL_BYTE
CODE ENDS
ENDS
    
```

SECTION 5

Notes:

System BIOS Listing - 11/8/82

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Notes:

```

1 $TITLE(BIOS FOR THE IBM PERSONAL COMPUTER XT)
2
3
4 ;-----
5 ; THE BIOS ROUTINES ARE MEANT TO BE ACCESSED THROUGH ;
6 ; SOFTWARE INTERRUPTS ONLY. ANY ADDRESSES PRESENT IN ;
7 ; THE LISTINGS ARE INCLUDED ONLY FOR COMPLETENESS, ;
8 ; NOT FOR REFERENCE. APPLICATIONS WHICH REFERENCE ;
9 ; ABSOLUTE ADDRESSES WITHIN THE CODE SEGMENT ;
10 ; VIOLATE THE STRUCTURE AND DESIGN OF BIOS. ;
11 ;-----
12
13 ;-----
14 ; EQUATES ;
15
16 0060 15 PORT_A EQU 60H ; 8255 PORT A ADDR
17 0061 16 PORT_B EQU 61H ; 8255 PORT B ADDR
18 0062 17 PORT_C EQU 62H ; 8255 PORT C ADDR
19 0063 18 CMD_PORT EQU 63H
20 0020 19 INTA00 EQU 20H ; 8259 PORT
21 0021 20 INTA01 EQU 21H ; 8259 PORT
22 0040 21 E01 EQU 20H
23 0043 22 TIMER EQU 40H ; 8253 TIMER/COUNTER PORT ADDR
24 0040 23 TIM_CTL EQU 43H ; 8253 TIMER/CNTR 0 PORT ADDR
25 0001 24 TIMER0 EQU 40H ; TIMER 0 INTR RECVD MASK
26 0008 25 TMINT EQU 01 ; DMA STATUS REG PORT ADDR
27 0000 26 DMA08 EQU 08 ; DMA CH.0 ADDR. REG PORT ADDR
28 0540 27 MAX_PERIOD EQU 540H
29 0410 28 MIN_PERIOD EQU 410H
30 0060 29 KBD_TN EQU 60H ; KEYBOARD DATA IN ADDR PORT
31 0002 30 KBDINT EQU 02 ; KEYBOARD INTR MASK
32 0060 31 KB_DATA EQU 60H ; KEYBOARD SCAN CODE PORT
33 0061 32 KB_CTL EQU 61H ; CONTROL BITS FOR KEYBOARD SENSE DATA
34
35 ;-----
36 ; 8088 INTERRUPT LOCATIONS ;
37 ;-----
38
39 ABS0 SEGMENT AT 0
40 STG_LOCO LABEL BYTE
41 NMI_PTR ORG 2*4 LABEL WORD
42 INT5_PTR ORG 5*4 LABEL WORD
43 INT_ADDR ORG 8*4 LABEL WORD
44 INT_PTR ORG 10H*4 LABEL DWORD
45 VIDEO_INT ORG 10H*4 LABEL WORD
46 PARM_PTR ORG 10H*4 LABEL DWORD ; POINTER TO VIDEO PARMS
47 BASIC_PTR ORG 18H*4 LABEL WORD ; ENTRY POINT FOR CASSETTE BASIC
48 DISK_POINTER ORG 01EH*4 LABEL DWORD ; INTERRUPT IEH
49 EXT_PTR LABEL DWORL ; LOCATION OF POINTER
50 EXT_PTR ORG 400H LABEL DWORL ; POINTER TO EXTENSION
51 DATA_AREA LABEL BYTE ; ABSOLUTE LOCATION OF DATA SEGMENT
52 DATA_WORD LABEL WORD
53 MFG_TEST_RTN ORG 0500H LABEL FAR
54 BOOT_LOCN ORG 7C00H LABEL FAR
55 ABS0 ENDS
56
57 ;-----
58 ; STACK -- USED DURING INITIALIZATION ONLY ;
59 ;-----
60
61 70 STACK SEGMENT AT 30H
62 0000 128 ???? DW 128 DUP(?)
63
64 73 TOS LABEL WORD
65 74 STACK ENDS
66
67 ;-----
68 ; ROM BIOS DATA AREAS ;
69 ;-----
70
71 80 DATA SEGMENT AT 40H
72 RS232_BASE DW 4 DUP(?) ; ADDRESSES OF RS232 ADAPTERS
73
74 82 PRINTER_BASE DW 4 DUP(?) ; ADDRESSES OF PRINTERS
75
76 83 EQUIP_FLAG DW ? ; INSTALLED HARDWARE
77 84 MFG_TST DB ? ; INITIALIZATION FLAG
78 85 MEMORY_SIZE DW ? ; MEMORY SIZE IN K BYTES
79 86 MFG_ERR_FLAG DB ? ; SCRATCHPAD FOR MANUFACTURING
80 87 DB ? ; ERROR CODES
81
82 ;-----
83 ; KEYBOARD DATA AREAS ;
84 ;-----
85
86 92 KB_FLAG DB ?
87
88 ;-----
89 ;----- SHIFT FLAG EQUATES WITHIN KB_FLAG ;
90 ;-----
91
92 96 INS_STATE EQU 80H ; INSERT STATE IS ACTIVE
93 97 CAPS_STATE EQU 40H ; CAPS LOCK STATE HAS BEEN TOGGLED
94 98 NUM_STATE EQU 20H ; NUM LOCK STATE HAS BEEN TOGGLED
95 99 SCROLL_STATE EQU 10H ; SCROLL LOCK STATE HAS BEEN TOGGLED
96 100 ALT_SHIFT EQU 08H ; ALTERNATE SHIFT KEY DEPRESSED
97 101 CTL_SHIFT EQU 04H ; CONTROL SHIFT KEY DEPRESSED
98 102 LEFT_SHIFT EQU 02H ; LEFT SHIFT KEY DEPRESSED
99 103 RIGHT_SHIFT EQU 01H ; RIGHT SHIFT KEY DEPRESSED
100
101

```

SECTION 5

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LOC OBJECT          LINE  SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82
0018 ??            106  KB_FLAG_1      DB      ?           ; SECOND BYTE OF KEYBOARD STATUS
                   107
0080               108  INS_SHIFT      EQU    80H         ; INSERT KEY IS DEPRESSED
0040               109  CAPS_SHIFT    EQU    40H         ; CAPS LOCK KEY IS DEPRESSED
0020               110  NUM_SHIFT     EQU    20H         ; NUM LOCK KEY IS DEPRESSED
0010               111  SCROLL_SHIFT  EQU    10H         ; SCROLL LOCK KEY IS DEPRESSED
0008               112  HOLD_STATE    EQU    08H         ; SUSPEND KEY HAS BEEN TOGGLED
                   113
0019 ??            114  ALT_INPUT     DB      ?           ; STORAGE FOR ALTERNATE KEYPAD ENTRY
001A ?????         115  BUFFER_HEAD   DW      ?           ; POINTER TO HEAD OF KEYBOARD BUFFER
001C ?????         116  BUFFER_TAIL  DW      ?           ; POINTER TO TAIL OF KEYBOARD BUFFER
001E (16           117  KB_BUFFER     DW      16 DUP(?) ; ROOM FOR 15 ENTRIES
)
003E               118  KB_BUFFER_END LABEL  WORD
                   119
                   120  ;----- HEAD = TAIL INDICATES THAT THE BUFFER IS EMPTY
                   121
0045               122  NUM_KEY       EQU    69         ; SCAN CODE FOR NUMBER LOCK
0046               123  SCROLL_KEY   EQU    70         ; SCROLL LOCK KEY
0038               124  ALT_KEY      EQU    56         ; ALTERNATE SHIFT KEY SCAN CODE
001D               125  CTL_KEY      EQU    29         ; SCAN CODE FOR CONTROL KEY
003A               126  CAPS_KEY     EQU    58         ; SCAN CODE FOR SHIFT LOCK
002A               127  LEFT_KEY     EQU    42         ; SCAN CODE FOR LEFT SHIFT
0036               128  RIGHT_KEY    EQU    54         ; SCAN CODE FOR RIGHT SHIFT
0052               129  INS_KEY      EQU    82         ; SCAN CODE FOR INSERT KEY
0053               130  DEL_KEY      EQU    83         ; SCAN CODE FOR DELETE KEY
                   131
                   132  ;-----
                   133  ; DISKETTE DATA AREAS
                   134  ;-----
003E ??            135  SEEK_STATUS  DB      ?           ; DRIVE RECALIBRATION STATUS
                   136  ; BIT 3-0 = DRIVE 3-0 NEEDS RECAL
                   137  ; BEFORE NEXT SEEK IF BIT 15 = 0
                   138
0080               139  INT_FLAG      EQU    080H        ; INTERRUPT OCCURRENCE FLAG
003F ??            140  MOTOR_STATUS DB      ?           ; MOTOR STATUS
                   141  ; BIT 3-0 = DRIVE 3-0 IS CURRENTLY
                   142  ; RUNNING
                   143  ; BIT 7 = CURRENT OPERATION IS A WRITE,
                   144  ; REQUIRES DELAY
                   145
0040 ??            146  MOTOR_COUNT  DB      ?           ; TIME OUT COUNTER FOR DRIVE TURN OFF
0025               147  MOTOR_WAIT   EQU    37         ; 2 SECS OF COUNTS FOR MOTOR TURN OFF
                   148
0041 ??            149  DISKETTE_STATUS DB ?           ; RETURN CODE STATUS BYTE
0080               150  TIME_OUT     EQU    80H         ; ATTACHMENT FAILED TO RESPOND
0040               151  BAD_SEEK     EQU    40H         ; SEEK OPERATION FAILED
0020               152  BAD_NEC     EQU    20H         ; NEC CONTROLLER HAS FAILED
0010               153  BAD_CRC     EQU    10H         ; BAD CRC ON DISKETTE READ
0009               154  DMA_BOUNDARY EQU    09H         ; ATTEMPT TO DMA ACROSS 64K BOUNDARY
0008               155  BAD_DMA     EQU    08H         ; DMA OVERRUN ON OPERATION
0004               156  RECORD_NOT_FND EQU    04H         ; REQUESTED SECTOR NOT FOUND
0003               157  WRITE_PROTECT EQU    03H         ; WRITE ATTEMPTED ON WRITE PROT DISK
0002               158  BAD_ADDR_MARK EQU    02H         ; ADDRESS MARK NOT FOUND
0001               159  BAD_CMD      EQU    01H         ; BAD COMMAND PASSED TO DISKETTE I/O
                   160
0042 (7           161  NEC_STATUS  DB      7 DUP(?) ; STATUS BYTES FROM NEC
??
)
                   162
                   163  ;-----
                   164  ; VIDEO DISPLAY DATA AREA
                   165  ;-----
0049 ??            166  CRT_MODE     DB      ?           ; CURRENT CRT MODE
004A ?????         167  CRT_COLS    DW      ?           ; NUMBER OF COLUMNS ON SCREEN
004C ?????         168  CRT_LEN     DW      ?           ; LENGTH OF REGEN IN BYTES
004E ?????         169  CRT_START   DW      ?           ; STARTING ADDRESS IN REGEN BUFFER
0050 (8           170  CURSOR_POSN  DW      8 DUP(?) ; CURSOR FOR EACH OF UP TO 8 PAGES
???)
)
0060 ?????         171  CURSOR_MODE  DW      ?           ; CURRENT CURSOR MODE SETTING
0062 ?????         172  ACTIVE_PAGE  DB      ?           ; CURRENT PAGE BEING DISPLAYED
0063 ?????         173  ADDR_6845   DW      ?           ; BASE ADDRESS FOR ACTIVE DISPLAY CARD
0065 ??            174  CRT_MODE_SET DB      ?           ; CURRENT SETTING OF THE 3X8 REGISTER
0066 ??            175  CRT_PALETTE  DB      ?           ; CURRENT PALETTE SETTING COLOR CARD
                   176
                   177  ;-----
                   178  ; POST DATA AREA
                   179  ;-----
0067 ?????         180  IO_ROM_INIT  DW      ?           ; PNTR TO OPTIONAL I/O ROM INIT ROUTINE
0069 ?????         181  IO_ROM_SEG   DW      ?           ; POINTER TO IO ROM SEGMENT
006B ??            182  INTR_FLAG    DB      ?           ; FLAG TO INDICATE AN INTERRUPT HAPPEND
                   183
                   184  ;-----
                   185  ; TIMER DATA AREA
                   186  ;-----
006C ?????         187  TIMER_LOW   DW      ?           ; LOW WORD OF TIMER COUNT
006E ?????         188  TIMER_HIGH  DW      ?           ; HIGH WORD OF TIMER COUNT
0070 ??            189  TIMER_OF    DB      ?           ; TIMER HAS ROLLED OVER SINCE LAST READ
                   190  ; COUNTS_SEC   EQU    18
                   191  ; COUNTS_MIN  EQU    1092
                   192  ; COUNTS_HOUR EQU    65543
                   193  ; COUNTS_DAY  EQU    1573040 = 1800BH
                   194
                   195  ;-----
                   196  ; SYSTEM DATA AREA
                   197  ;-----
0071 ??            198  BIOS_BREAK   DB      ?           ; BIT 7=1 IF BREAK KEY HAS BEEN HIT
0072 ?????         199  RESET_FLAG   DW      ?           ; WORD=1234H IF KEYBOARD RESET UNDERWAY
                   200
                   201  ;-----
                   202  ; FIXED DISK DATA AREAS
                   203  ;-----
0074 ?????         203  ;
0076 ?????         204  ;
                   205  ;-----
                   206  ; PRINTER AND RS232 TIME-OUT VARIABLES
                   207  ;-----
0078 (4           208  PRINT_TIM_OUT DB      4 DUP(?)
??
)
007C (4           209  RS232_TIM_OUT DB      4 DUP(?)
??
)

```

```

LOC OBJECT          LINE SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82
-----
210 ;-----
211 ;      ADDITIONAL KEYBOARD DATA AREA  ;
212 ;-----
0080 ????          213 BUFFER_START   DW   ?
0082 ????          214 BUFFER_END   DW   ?
-----
215 DATA _ ENDS
216 ;-----
217 ;      EXTRA DATA AREA  ;
218 ;-----
219 XXDATA SEGMENT AT 50H
0000 ??           220 STATUS_BYTE  DB   ?
-----
221 XXDATA ENDS
222 ;-----
223 ;      VIDEO DISPLAY BUFFER  ;
224 ;-----
225 VIDEO_RAM SEGMENT AT 0B800H
0000             226 REGEN LABEL   BYTE
0000             227 REGENW LABEL  WORD
0000 (16384      228 DB          16384 DUP(?)
    ?
)
-----
229 VIDEO_RAM ENDS
230 ;-----
231 ;      ROM RESIDENT CODE  ;
232 ;-----
233 CODE SEGMENT AT 0F000H
0000 (57344      234 DB          57344 DUP(?) ; FILL LOWEST 56K
    ?
)
-----
E000 31353031353132 235
      20434F60522E20 236 DB          '1501512 CPR. IBM 1982' ; COPYRIGHT NOTICE
      49424D20313938
      32
-----
237
238
239 ;-----
240 ;      INITIAL RELIABILITY TESTS -- PHASE 1  ;
241 ;-----
242
243 ASSUME CS:CODE,SS:CODE,ES:AB50,DS:DATA
244
245 ;-----
246 ;      DATA DEFINITIONS  ;
247 ;-----
248
E016 D7E0          249 C1 DW C11 ; RETURN ADDRESS
E018 7EE1          250 C2 DW C24 ; RETURN ADDRESS FOR DUMMY STACK
-----
E01A 204B42204F4B 251
E020 0D            252 F3B DB ' KB OK',13 ; KB FOR MEMORY SIZE
-----
253
254 ;-----
255 ;      LOAD A BLOCK OF TEST CODE THROUGH THE KEYBOARD PORT  ;
256 ;      FOR MANUFACTURING TEST.  ;
257 ;      THIS ROUTINE WILL LOAD A TEST (MAX LENGTH=FAFFH) THROUGH  ;
258 ;      THE KEYBOARD PORT. CODE WILL BE LOADED AT LOCATION  ;
259 ;      0000:0500. AFTER LOADING, CONTROL WILL BE TRANSFERRED  ;
260 ;      TO LOCATION 0000:0500. STACK WILL BE LOCATED JUST BELOW  ;
261 ;      THE TEST CODE. THIS ROUTINE ASSUMES THAT THE FRIST 2  ;
262 ;      BYTES TRANSFERED CONTAIN THE COUNT OF BYTES TO BE LOADED  ;
263 ;      (BYTE 1=COUNT LOW, BYTE 2=COUNT HI.)  ;
264 ;-----
265
266 ;-----
267 ;      FIRST, GET THE COUNT
268
E021 MFG_BOOT:
E021 E8131A        269 CALL SP_TEST ; GET COUNT LOW
E024 8AFB          270 MOV BH,BL ; SAVE IT
E026 E80E1A        271 CALL SP_TEST ; GET COUNT HI
E029 8AEB          272 MOV CH,BL
E02B 8ACF          273 MOV CL,BH ; CX NOW HAS COUNT
E02D FC           274 CLD ; SET DIR. FLAG TO INCREMENT
E02E FA           275 CLI
E02F BF0005        276 MOV DI,0500H ; SET TARGET OFFSET (DS=0000)
E032 B0FD          277 MOV AL,0FDH ; UNMASK K/B INTERRUPT
E034 E621          278 OUT INTA01,AL
E036 B00A          279 MOV AL,0AH ; SEND READ INT. REQUEST REG. CMD
E038 E620          280 OUT INTA00,AL
E03A B6100         281 MOV DX,61H ; SET UP PORT B ADDRESS
E03D BBCC4C        282 MOV BX,4CCCH ; CONTROL BITS FOR PORT B
E040 B402          283 MOV AH,02H ; K/B REQUEST PENDING MASK
E042 TST:          284
E042 8AC3          285 MOV AL,BL
E044 EE           286 OUT DX,AL ; TOGGLE K/B CLOCK
E045 8ACT          287 MOV AL,BH
E047 EE           288 OUT DX,AL
E048 4A           289 DEC DX ; POINT DX AT ADDR. 60 (KB DATA)
E049 TST1:        290
E049 E420          291 IN AL,INTA00 ; GET IRR REG
E04B 22CA          292 AND AL,AH ; KB REQUEST PENDING?
E04D 74FA          293 JZ TST1 ; LOOP TILL DATA PRESENT
E04F EC           294 IN AL,DX ; GET DATA
E050 AA           295 STOSB ; STORE IT
E051 42           296 INC DX ; POINT DX BACK AT PORT B (61)
E052 E2EE          297 LOOP TST ; LOOP TILL ALL BYTES READ
298
E054 EA00050000    299 JMP MFG_TEST_RTN ; FAR JUMP TO CODE THAT WAS JUST
300 ; LOADED
301

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LOC OBJECT                LINE SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82
E10E                    531 CLR_STG:
E10E 2BC0                532     SUB AX,AX ; MAKE AX=0000
E190 F3                  533     REP STOSW ; STORE 8K WORDS OF 0000
E191 AB                  534
E192                    535 HOW_BIG:
E192 891E7204            536     MOV DATA_WORD[OFFSET RESET_FLAG],BX ; RESTORE RESET FLAG
E196 8A0004              537     MOV DX,0400H ; SET POINTER TO JUST>16KB
E199 BB1000              537     MOV BX,16 ; BASIC COUNT OF 16K
E19C                    538 FILL_LOOP:
E19C BEC2                539     MOV ES,DX ; SET SEG. REG.
E19E 2BFF                540     SUB DI,DI
E1A0 B855AA              541     MOV AX,0AA55H ; TEST PATTERN
E1A3 8BC8                542     MOV CX,AX ; SAVE PATTERN
E1A5 289005              543     MOV ES:[DI],AX ; SEND PATTERN TO MEM.
E1A8 800F                544     MOV AL,0FH ; PUT SOMETHING IN AL
E1AA 268B05              545     MOV AX,ES:[DI] ; GET PATTERN
E1AD 33C1                546     XOR AX,CX ; COMPARE PATTERNS
E1AF 7511                547     JNE HIGH_BIG_END ; GO END IF NO COMPARE
E1B1 890020              548     MOV CX,2000H ; SET COUNT FOR 8K WORDS
E1B4 F3                  549     REP STOSW ; FILL 8K WORDS
E1B5 AB                  550
E1B6 81C20004            550     ADD DX,400H ; POINT TO NEXT 16KB BLOCK
E1BA 83C310              551     ADD BX,16 ; BUMP COUNT BY 16KB
E1BD 80FEA0              552     CMP DH,0A0H ; BUMP COUNT BY 16KB
E1C0 750A                553     JNZ FILL_LOOP ; TOP OF RAM AREA YET? (A0000)
E1C2                    554 HOW_BIG_END:
E1C2 891E1304            555     MOV DATA_WORD[OFFSET MEMORY_SIZE],BX ; SAVE MEMORY SIZE
E1C5                    556
E1C5                    557 ;----- SETUP STACK SEG AND SP
E1C5                    558
E1C6 B83000              559     MOV AX,STACK ; GET STACK VALUE
E1C9 8E00                560     MOV SS,AX ; SET THE STACK UP
E1CB 8C0001              561     MOV SP,OFFSET TOS ; STACK IS READY TO GO
E1C5                    562
E1C5                    563 ;----- INITIALIZE THE 8259 INTERRUPT CONTROLLER CHIP ;
E1C5                    564
E1CE B013                565     MOV AL,13H ; ICW1 - EDGE, SNGL, ICW4
E1D0 E620                566     OUT INTA00,AL
E1D2 B008                567     MOV AL,8 ; SETUP ICW2 - INT TYPE 8 (8-F)
E1D4 E621                568     OUT INTA01,AL
E1D6 B009                569     MOV AL,9 ; SETUP ICW4 - BUFFRD,8086 MODE
E1D8 E621                570     OUT INTA01,AL
E1DA 80FF                571     MOV AL,0FFH ; MASK ALL INTS. OFF
E1DC E621                572     OUT INTA01,AL ; VIDEO ROUTINE ENABLES INTS.)
E1D5                    573
E1D5                    574 ;----- SET UP THE INTERRUPT VECTORS TO TEMP INTERRUPT
E1D5                    575
E1DE 1E                  576     PUSH DS
E1DF 892000              577     MOV CX,32 ; FILL ALL 32 INTERRUPTS
E1E2 2BFF                578     SUB DI,DI ; FIRST INTERRUPT LOCATION
E1E4 BEC7                579     MOV ES,DI ; SET ES=0000 ALSO
E1E6 8B23FF              580     MOV AX,OFFSET D11 ; MOVE ADDR OF INTR PROC TO TBL
E1E9 AB                  581     STOSW
E1EA BCC8                582     MOV AX,CS ; GET ADDR OF INTR PROC SEG
E1EC AB                  583     STOSW
E1ED E2F7                584     LOOP D3 ; VECTBLO
E1E5                    585
E1E5                    586 ;----- ESTABLISH BIOS SUBROUTINE CALL INTERRUPT VECTORS
E1E5                    587
E1EF BF4000              588     MOV DI,OFFSET VIDEO_INT ; SETUP ADDR TO INTR AREA
E1F2 0E                  589     CS PUSH DS
E1F3 1F                  590     POP DS ; SETUP ADDR OF VECTOR TABLE
E1F4 8CD8                591     MOV AX,DS ; SET AX=SEGMENT
E1F6 8E03FF90            592     MOV SI,OFFSET VECTOR_TABLE+16 ; START WITH VIDEO ENTRY
E1FA B91000              593     MOV CX,16
E1FD A5                  594     D3A: MOVSW ; MOVE VECTOR TABLE TO RAM
E1FE 47                  595     INC DI ; SKIP SEGMENT POINTER
E1FF 47                  596     INC DI
E200 E2FB                597     LOOP D3A
E201                    598
E201                    599 ;----- DETERMINE CONFIGURATION AND MFG. MODE ;
E201                    600
E201                    601
E202 1F                  602     POP DS
E203 1E                  603     PUSH DS ; RECOVER DATA SEG
E204 E462                604     IN AL,PORT_C ; GET SWITCH INFO
E206 240F                605     AND AL,0000T111B ; ISOLATE SWITCHES
E208 8AE0                606     MOV AH,AL ; SAVE
E20A B0AD                607     MOV AL,10101101B ; ENABLE OTHER BANK OF SWS.
E20C E661                608     OUT PORT_B,AL
E20E 90                  609     NOP
E20F E462                610     IN AL,PORT_C
E211 8104                611     MOV CL,4
E213 D2C0                612     ROL AL,CL ; ROTATE TO HIGH NIBBLE
E215 24F0                613     AND AL,11110000B ; ISOLATE
E217 0AC4                614     OR AL,AH ; COMBINE WITH OTHER BANK
E219 2AE4                615     SUB AH,AH
E21B A31004              616     MOV DATA_WORD[OFFSET EQUIP_FLAG],AX ; SAVE SWITCH INFO
E21E B099                617     MOV AL,99H
E220 E663                618     OUT CMD_PORT,AL
E222 E30518              619     CUI KBC_RESET ; SEE IF MFG. JUMPER IN
E225 80FBAA              620     CMP BL,0AAH ; KEYBOARD PRESENT?
E228 7418                621     JE E6
E22A 80FB65              622     CMP BL,065H ; LOAD MFG. TEST REQUEST?
E22D 7503                623     JNE D3B
E22F E9E9FD              624     JMP MFG_BOOT ; GO TO BOOTSTRAP IF SO
E232 B038                625     D3B: MOV AL,38H
E234 E661                626     OUT PORT_B,AL
E236 90                  627     NOP
E237 90                  628     NOP
E238 E460                629     IN AL,PORT_A
E23A 24FF                630     AND AL,0FFH ; WAS DATA LINE GROUNDED
E23C 7604                631     JNS E5
E23E FE061204            632     INC DATA_AREA[OFFSET MFG_TST] ; SET MANUFACTURING TEST FLAG
E235                    633

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634 ;-----
635 ; INITIALIZE AND START CRT CONTROLLER (6845) ;
636 ; TEST VIDEO READ/WRITE STORAGE. ;
637 ; DESCRIPTION ;
638 ; RESET THE VIDEO ENABLE SIGNAL. ;
639 ; SELECT ALPHANUMERIC MODE, 40 * 25, B & W. ;
640 ; READ/WRITE DATA PATTERNS TO STG. CHECK STG ;
641 ; ADDRESSABILITY ;
642 ; ERROR = 1 LONG AND 2 SHORT BEEPS ;
643 ;-----
E242 E6:
E242 A11004 644 MOV AX,DATA_WORD[OFFSET EQUIP_FLAG] ; GET SENSE SWITCH INFO
E245 50 645 PUSH AX ; SAVE IT
E246 B030 647 MOV AL,30H
E248 A31004 648 MOV DATA_WORD[OFFSET EQUIP_FLAG],AX
E24B 2AE4 649 SUB AH,AH
E24D CD10 650 INT 10H ; SEND INIT TO B/W CARD
E24F B020 651 MOV AL,20H
E251 A31004 652 MOV DATA_WORD[OFFSET EQUIP_FLAG],AX
E254 2AE4 653 SUB AH,AH ; AND INIT COLOR CARD
E256 CD10 654 INT 10H
E258 58 655 POP AX ; RECOVER REAL SWITCH INFO
E259 A31004 656 MOV DATA_WORD[OFFSET EQUIP_FLAG],AX ; RESTORE IT
E25C 2430 657 AND AL,30H ; AND CONTINUE
E25E 750A 658 JNZ E7 ; ISOLATE VIDEO SWS
E260 BF4000 659 MOV DI,OFFSET VIDEO_INT ; VIDEO SWS SET TO 0?
E263 C7054BFF 660 MOV [DI],OFFSET DUMMY_RETURN ; SET INT 10H TO DUMMY
E267 E9A000 661 JMP E18 ; RETURN IF NO VIDEO CARD
E26A E7: ; BYPASS VIDEO TEST
E26A 3C30 662 CMP AL,30H ; TEST VIDEO:
E26C 7408 664 JE E65 ; B/W CARD ATTACHED?
E26E FEC4 666 INC AH ; YES - SET MODE FOR B/W CARD
E270 3C20 667 CMP AL,20H ; SET COLOR MODE FOR COLOR CD
E272 7502 668 JNE E8 ; 80X25 MODE SELECTED?
E274 B403 669 MOV AH,3 ; NO - SET MODE FOR 40X25
E276 86E0 670 XCHG AH,AL ; SET MODE:
E278 50 671 PUSH AX ; SAVE VIDEO MODE ON STACK
E279 2AE4 672 SUB AH,AH ; INITIALIZE TO ALPHANUMERIC MD
E27B CD10 673 INT 10H ; CALL VIDEO IO
E27D 58 674 POP AX ; RESTORE VIDEO SENSE SWS IN AH
E27E 50 675 PUSH AX ; RESAVE VALUE
E27F B0B0B0 676 MOV BX,0B000H ; BEG VIDEO RAM ADDR B/W CD
E282 BAB803 677 MOV DX,3B8H ; MODE REG FOR B/W
E285 B90008 678 MOV CX,2048 ; RAM WORD CNT FOR B/W CD
E288 B001 679 MOV AL,1 ; SET MODE FOR BW CARD
E28A 80FC30 680 AND AH,30H ; B/W VIDEO CARD ATTACHED?
E28D 7409 681 JE E9 ; YES - GO TEST VIDEO STG
E28F B7B8 682 MOV BH,0B8H ; BEG VIDEO RAM ADDR COLOR CD
E291 BAD803 683 MOV DX,3D8H ; MODE REG FOR COLOR CD
E294 B520 684 MOV CH,20H ; RAM WORD CNT FOR COLOR CD
E296 FEC8 685 DEC AL ; SET MODE TO 0 FOR COLOR CD
E298 E7: ; TEST VIDEO STG:
E298 EE 686 ; DISABLE VIDEO FOR COLOR CD
E299 813E72043412 687 CMP DATA_WORD[OFFSET RESET_FLAG],234H ; 1234H: POD INIT BY KBD RESET?
E29F 8EC3 688 MOV ES,BX ; POINT ES TO VIDEO RAM STG
E2A1 7407 689 JE E10 ; YES - SKIP VIDEO RAM TEST
E2A3 8EDB 690 MOV DS,BX ; POINT DS TO VIDEO RAM STG
E2A5 E8C703 691 ASSUME DS:NOTHING,ES:NOTHING
E2A8 7546 692 CALL STGTST_CNT ; GO TEST VIDEO R/W STG
E2A8 7546 693 JNE E17 ; R/W STG FAILURE - BEEP SPK
E2A8 7546 694
E2A8 7546 695
E2A8 7546 696 ; SETUP VIDEO DATA ON SCREEN FOR VIDEO ;
E2A8 7546 697 ; LINE TEST. ;
E2A8 7546 698 ; DESCRIPTION ;
E2A8 7546 699 ; ENABLE VIDEO SIGNAL AND SET MODE. ;
E2A8 7546 700 ; DISPLAY A HORIZONTAL BAR ON SCREEN. ;
E2A8 7546 701 ;-----
E2AA E10:
E2AA 58 702 POP AX ; GET VIDEO SENSE SWS (AH)
E2AB 50 704 PUSH AX ; SAVE IT
E2AC B400 705 MOV AH,0 ; ENABLE VIDEO AND SET MODE
E2AE CD10 706 INT 10H ; VIDEO
E2B0 B82010 707 MOV AX,7020H ; WRT BLANKS IN REVERSE VIDEO
E2B0 708
E2B0 709 ;---- UNNATURAL ACT FOR ADDRESS COMPATIBILITY
E2B3 EB11 710
E2C3 711
E2C3 E99915 712 ORG 0E2C3H
E2C3 713 JMP NMI_INT
E2C3 714
E2C6 E10A:
E2C6 2BFF 715 SUB DI,D1 ; SETUP STARTING LOC
E2C8 B92800 716 MOV CX,40 ; NO. OF BLANKS TO DISPLAY
E2CB F3 717 REP STOSW ; WRITE VIDEO STORAGE
E2CC AB 718
E2C6 719
E2C6 720 ;-----
E2C6 721 ; CRT INTERFACE LINES TEST ;
E2C6 722 ; DESCRIPTION ;
E2C6 723 ; SENSE ON/OFF TRANSITION OF THE ;
E2C6 724 ; VIDEO ENABLE AND HORIZONTAL ;
E2C6 725 ; SYNC LINES. ;
E2C6 726 ;-----
E2CD 58 726 POP AX ; GET VIDEO SENSE SW INFO
E2CE 50 727 PUSH AX ; SAVE IT
E2CF 80FC30 728 MOV AH,30H ; B/W CARD ATTACHED?
E2D2 BABA03 729 MOV DX,03BAH ; SETUP ADDR OF BW STATUS PORT
E2D5 7403 730 JE E11 ; YES - GO TEST LINES
E2D7 BADA03 731 MOV DX,03DAH ; COLOR CARD IS ATTACHED
E2DA EDA 732 ; LINE_TST:
E2DA B408 733 MOV AH,8
E2DC E2C 734
E2DE 2BC9 735 MOV CX,CX ; OFLOOP_CNT:
E2DE 736
E2DE EC 737
E2DF 22C4 738 IN AL,DX ; READ CRT STATUS PORT
E2E1 7504 739 AND AL,AH ; CHECK VIDEO/HORZ LINE
E2E3 E2F9 740 JNZ E13 ; ITS ON - CHECK IF IT GOES OFF
E2E5 EB09 741 LOOP E13 ; LOOP TILL ON OR TIMEOUT
E2E7 E14: 742 JMP SHORT E17 ; GO PRINT ERROR MSG
E2E7 2BC9 743 SUB CX,CX
E2E9 744
E2E9 EC 745 IN AL,DX ; READ CRT STATUS PORT
E2EA 22C4 746 AND AL,AH ; CHECK VIDEO/HORZ LINE
E2EC 747 JZ E16 ; ITS ON - CHECK NEXT LINE
E2EE E2F9 748 LOOP E15 ; LOOP IF OFF TILL IT GOES ON

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SECTION 5


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861 ;----- SETUP TIMER 0 TO MODE 3
862
863 MOV AL,0FFH ; DISABLE ALL DEVICE INTERRUPTS
864 OUT INTA01,AL
865 MOV AL,36H ; SEL TIM 0,LSB,MSB,MODE 3
866 OUT TIMER+3,AL ; WRITE TIMER MODE REG
867 MOV AL,0
868 OUT TIMER,AL ; WRITE LSB TO TIMER 0 REG
869 OUT TIMER,AL ; WRITE MSB TO TIMER 0 REG
870
871 ;-----
872 ; KEYBOARD TEST ;
873 ; DESCRIPTION ;
874 ; RESET THE KEYBOARD AND CHECK THAT SCAN ;
875 ; CODE 'AA' IS RETURNED TO THE CPU. ;
876 ; CHECK FOR STUCK KEYS. ;
877 ;-----
878 TS12:
879 MOV AL,99H ; SET 8255 MODE A,C=IN B=OUT
880 OUT CMD_PORT,AL
881 MOV AL,DATA_AREA[OFFSET EQUIP_FLAG]
882 AND AL,01 ; TEST CHAMBER?
883 JZ F7 ; BYPASS IF SO
884 CMP DATA_AREA[OFFSET MFG_TST],F7 ; YES - SKIP KEYBOARD TEST MODE?
885 JE F7 ; YES - SKIP KEYBOARD TEST
886 CALL KBD_RESET ; ISSUE RESET TO KEYBRD
887 JCXZ F6 ; PRINT ERR MSG IF NO INTERRUPT
888 MOV AL,49H ; ENABLE KEYBOARD
889 OUT PORT_B,AL
890 CMP BL,0AAH ; SCAN CODE AS EXPECTED?
891 JNE F6 ; NO - DISPLAY ERROR MSG
892
893 ;----- CHECK FOR STUCK KEYS
894
895 MOV AL,0CBH ; CLR KBD, SET CLK LINE HIGH
896 OUT PORT_B,AL
897 MOV AL,4BH ; ENABLE KBD,CLK IN NEXT BYTE
898 OUT PORT_B,AL
899 SUB CX,CX
900 F5: ; KBD WAIT:
901 LOOP F5 ; DELAY FOR A WHILE
902 IN AL,KBD_IN ; CHECK FOR STUCK KEYS
903 CMP AL,0 ; SCAN CODE = 0?
904 JE F7 ; YES - CONTINUE TESTING
905 CALL XPC_BYTE ; CONVERT AND PRINT
906 F6:
907 MOV SI,OFFSET F1 ; GET MSG ADDR
908 CALL E_MSG ; PRINT MSG ON SCREEN
909 ;-----
910 ; SETUP HARDWARE INT. VECTOR TABLE ;
911 F7:
912 PUSH DS ; SETUP_INT_TABLE:
913 SUB AX,AX
914 MOV ES,AX
915 MOV CX,08 ; GET VECTOR CNT
916 PUSH CS ; SETUP_DS_SEG_REG
917 POP DS
918 MOV SI,OFFSET VECTOR_TABLE
919 MOV DI,OFFSET INT_PTR
920 F7A:
921 MOVSW
922 INC DI ; SKIP OVER SEGMENT
923 INC DI
924 LOOP F7A
925 POP DS
926
927 ;----- SET UP OTHER INTERRUPTS AS NECESSARY
928
929 MOV NMI_PTR,OFFSET NMI_INT ; NMI INTERRUPT
930 MOV INT5_PTR,OFFSET PRINT_SCREEN ; PRINT SCREEN
931 MOV BASIC_PTR+2,0F600H ; SEGMENT FOR CASSETTE BASIC
932
933 ;----- SETUP TIMER 0 TO BLINK LED IF MANUFACTURING TEST MODE
934
935 CMP DATA_AREA[OFFSET MFG_TST],01H ; MFG. TEST MODE?
936 JNZ EXP_TO
937 MOV WORD PRT(1CH*4),OFFSET BLINK_INT; SETUP TIMER INTR TO BLINK LED
938 MOV AL,0FEH ; ENABLE TIMER INTERRUPT
939 OUT INTA01,AL

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940 ;-----
941 ; EXPANSION I/O BOX TEST ;
942 ; CHECK TO SEE IF EXPANSION BOX PRESENT - IF INSTALLED, ;
943 ; TEST DATA AND ADDRESS BUSES TO I/O BOX ;
944 ; ERROR='180!' ;
945 ;-----
946
947 ;----- DETERMINE IF BOX IS PRESENT
948
E418 EXP_10: ; (CARD WAS ENABLED EARLIER)
E418 BA1002 950 MOV DX,0210H ; CONTROL PORT ADDRESS
E41B B85555 951 IN AX,555H ; SET DATA PATTERN
E41E EE 952 OUT DX,AL
E41F B001 953 MOV AL,01H ; MAKE AL DIFFERENT
E421 EC 954 IN AL,DX ; RECOVER DATA
E422 3AC4 955 CMP AL,AH ; REPLY?
E424 7544 956 JNE E19 ; NO RESPONSE, GO TO NEXT TEST
E426 F7D0 957 NOT AX ; MAKE DATA=AAAA
E428 EE 958 OUT DX,AL
E429 B001 959 MOV AL,01H
E42B EC 960 IN AL,DX ; RECOVER DATA
E42C 3AC4 961 CMP AL,AH
E42E 753A 962 JNE E19
963
964 ;----- CHECK ADDRESS BUS
965
E430 EXP2:
E430 BB0100 966 MOV BX,0001H
E433 BA1502 968 MOV DX,0215H ; LOAD HI ADDR. REG ADDRESS
E436 B91000 969 MOV CX,0016 ; GO ACROSS 16 BITS
E439 970
E439 2E8807 EXP3:
E43C 90 971 MOV CS:[BX],AL ; WRITE ADDRESS F0000-BX
E43D EC 972 NOP ;
E43E 3AC7 973 IN AL,DX ; READ ADDR. HIGH
E440 7521 974 CMP AL,BH ;
E442 42 975 JNE EXP_ERR ; GO ERROR IF MISCOMPARE
E443 EC 976 INC DX ; DX=216H (ADDR. LOW REG)
E444 3AC3 977 IN AL,DX ;
E446 751B 978 CMP AL,BL ; COMPARE TO LOW ADDRESS
E448 4A 979 JNE EXP_ERR ;
E449 D1E3 980 DEC DX ; DX BACK TO 215H
E44B E2EC 981 SHL BX,1
982 LOOP EXP3 ; LOOP TILL '1' WALKS ACROSS BX
983
984 ;----- CHECK DATA BUS
985
E44D B90800 986 MOV CX,0008 ; DO 8 TIMES
E450 B001 987 MOV AL,01
E452 4A 988 DEC DX ; MAKE DX=214H (DATA BUS REG)
E453 989
E453 8AE0 EXP4:
E455 EE 991 MOV AH,AL ; SAVE DATA BUS VALUE
E456 B001 992 OUT DX,AL ; SEND VALUE TO REG
E458 EC 993 IN AL,DX ; RETRIVE VALUE FROM REG
E459 3AC4 994 CMP AL,AH ; = TO SAVED VALUE
E45B 7506 995 JNE SHORT_EXP_ERR ;
E45D D0E0 996 SHL AL,1 ; FORM NEW DATA PATTERN
E45F E2F2 997 LOOP EXP4 ; LOOP TILL BIT WALKS ACROSS AL
E461 EB07 998 JMP SHORT_E19 ; GO ON TO NEXT TEST
E463 999
E463 BE0FF990 EXP_ERR:
E467 E83F15 1000 MOV SI,OFFSET F3C
1001 CALL E_MSG

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LOC OBJECT                LINE  SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82
1102 ;-----
1103 ; CHECK FOR OPTIONAL ROM FROM C8000->F4000 IN 2K BLOCKS
1104 ; (A VALID MODULE HAS '55AA' IN THE FIRST 2 LOCATIONS,
1105 ; INDICATOR (LENGTH/512) IN THE 3D LOCATION AND
1106 ; TEST/INIT. CODE STARTING IN THE 4TH LOCATION.)
1107 ;-----
E518 ROM_SCAN:
E518 BA00C8      1108      MOV     DX,0C800H          ; SET BEGINNING ADDRESS
E51B            1109      ROM_SCAN_1:
E51B            1110      MOV     DS,DX
E51B BEDA       1111      SUB     BX,BX            ; SET BX=0000
E51D 2BDB      1112      MOV     AX,[BX]         ; GET 15T WORD FROM MODULE
E51F 8B07      1113      PUSH   BX
E521 53        1114      POP    BX
E522 5B        1115      JNC   BX                ; BUS SETTLING
E523 3D55AA    1116      JMP    AX,0AA55H        ; = TO ID WORD?
E526 7506      1117      JNZ   NEXT_ROM        ; PROCEED TO NEXT ROM IF NOT
E528 E82B14    1118      CALL  ROM_CHECK       ; GO CHECK OUT MODULE
E52B AB0590    1119      JMP    ARE_WE_DONE     ; CHECK FOR END OF ROM SPACE
E52E            1120      NEXT_ROM:
E52E 81C28000  1121      ADD     DX,00800H      ; POINT TO NEXT 2K ADDRESS
E532            1122      ARE_WE_DONE:
E532 81FA00F6  1123      CMP     DX,0F600H      ; AT F6000 YET?
E536 7CE3      1124      JLE   ROM_SCAN_1      ; GO CHECK ANOTHER ADD. IF NOT
E538 E80190    1125      JMP    BASE_ROM_CHK    ; GO CHECK BASIC ROM
1126 ;-----
1127 ; A CHECKSUM IS DONE FOR THE 4 ROS MODULES CONTAINING BASIC CODE
1128 ;-----
E53B            1129      BASE_ROM_CHK:
E53B B404       1130      MOV     AH,4            ; NO. OF ROS MODULES TO CHECK
E53D            1131      E4:
E53D 2BDB      1132      SUB     BX,BX            ; SETUP STARTING ROS ADDR
E53F BEDA      1133      MOV     DS,DX
1134 ;-----
E541 E8AE13    1134      CALL   ROS_CHECKSUM    ; CHECK ROS
E544 7403      1135      JE     E5               ; CONTINUE IF OK
E546 E88201    1137      CALL   ROM_ERR         ; POINT ERROR
E549            1138      E5:
E549 81C20002  1139      ADD     DX,0200H       ; ADD TO NEXT 8K MODULE
E54D FECC      1140      DEC     AH              ; ANY MORE TO DO?
E54F 75EC      1141      JNZ    E4              ; YES - CONTINUE
1142 ;-----
1143 ; DISKETTE ATTACHMENT TEST
1144 ; DESCRIPTION
1145 ; CHECK IF IPL DISKETTE DRIVE IS ATTACHED TO SYSTEM. IF
1146 ; ATTACHED, VERIFY STATUS OF NEC FDC AFTER A RESET. ISSUE
1147 ; A RECAL AND SEEK CMD TO FDC AND CHECK STATUS. COMPLETE
1148 ; SYSTEM INITIALIZATION THEN PASS CONTROL TO THE BOOT
1149 ; LOADER PROGRAM.
1150 ;-----
E551            1151      F9:
E551 1F        1152      POP    DS
E552 A01000    1153      MOV     AL,BYTE PTR EQUIP_FLAG ; DISKETTE PRESENT?
E555 2401      1154      AND     AL,01H         ; NO - BYPASS DISKETTE TEST
E557 743E      1155      JZ     F15             ; NO - BYPASS DISKETTE TEST
E559            1156      F10:
E559 E421      1157      IN     AL,INTA01       ; DISK_TEST:
E55B 24BF      1158      AND     AL,0BFH        ; ENABLE DISKETTE INTERRUPTS
E55D E621      1159      OUT    INTA01,AL
E55F B400      1160      MOV     AH,0           ; RESET NEC FDC
E561 8AD4      1161      MOV     DL,AH          ; SET FOR DRIVE 0
E563 CD13      1162      INT    13H            ; VERIFY STATUS AFTER RESET
E565 F6C4FF    1163      TEST   AH,0FFH        ; STATUS OK?
E568 7520      1164      JNZ    F13            ; NO - FDC FAILED
1165 ;-----
1166 ;----- TURN DRIVE 0 MOTOR ON
1167 ;-----
E56A BAF203    1168      MOV     DX,03F2H       ; GET ADDR OF FDC CARD
E56D B01C      1169      MOV     AL,1CH         ; TURN MOTOR ON, EN DMA/INT
E56F CE        1170      SUB     DX,AL          ; WRITE FDC CONTROL REG
E570 2BC9      1171      SUB     CX,CX
E572            1172      F11:
E572 E2FE      1173      LOOP   F11            ; MOTOR WAIT:
E574            1174      F12:
E574 E2FE      1175      LOOP   F12            ; MOTOR WAIT1:
E576 33D2      1176      XOR     DX,DX          ; SELECT DRIVE 0
E578 B501      1177      MOV     CH,1           ; SELECT TRACK 1
E57A 8B163E00  1178      MOV     MOV     SEEK_STATUS,DL
E57E E8FC08    1179      CALL   SEEK           ; RECALIBRATE DISKETTE
E581 7207      1180      JC     F13            ; GO TO ERR SUBROUTINE IF ERR
E583 B522      1181      MOV     CH,34          ; SELECT TRACK 34
E585 E8F508    1182      CALL   SEEK           ; SEEK TO TRACK 34
E588 7307      1183      JNC    F14            ; OK, TURN MOTOR OFF
E58A            1184      F13:
E58A            1185      MOV     SI,OFFSET F3   ; DSK ERR:
E58B            1186      CALL   E_MSG          ; GET ADDR OF MSG
E58E E81814    1187      CALL   E_MSG          ; GO PRINT ERROR MSG
1188 ;-----
1189 ;----- TURN DRIVE 0 MOTOR OFF
1190 ;-----
E591            1190      F14:
E591 B00C      1191      MOV     AL,0CH         ; DR0 OFF:
E593 BAF203    1192      MOV     DX,03F2H       ; TURN DRIVE 0 MOTOR OFF
E596 EE        1193      OUT    DX,AL          ; FDC CTL ADDRESS
1194 ;-----
1195 ;----- SETUP PRINTER AND RS232 BASE ADDRESSES IF DEVICE ATTACHED
1196 ;-----
E597            1197      F15:
E597 C6066B0000 1198      MOV     INTR_FLAG,00H   ; SET STRAY INTERRUPT FLAG = 00
E59C BE1E00    1199      MOV     SI,OFFSET KB_BUFFER ; SETUP KEYBOARD PARAMETERS
E59F 89361A00  1200      MOV     BUFFER_HEAD,SI
E5A3 89361C00  1201      MOV     BUFFER_TAIL,SI
E5A7 89368000  1202      MOV     BUFFER_START,SI
E5AB 83C620    1203      ADD     SI,32           ; DEFAULT BUFFER OF 32 BYTES
E5AE 89368200  1204      MOV     BUFFER_END,SI
E5B2 BF7800    1205      MOV     DI,OFFSET PRINT_TIM_OUT ; SET DEFAULT PRINTER TIMEOUT
E5B5 IE        1206      PUSH   DS
E5B6 07        1207      POP    ES
E5B7 B81414    1208      MOV     AX,1414H       ; DEFAULT=20
E5BA AB        1209      STOSW
E5BB AB        1210      STOSW
E5BC B80101    1211      MOV     AX,0101H       ; RS232 DEFAULT=01
E5BF AB        1212      STOSW
E5C0 AB        1213      STOSW
E5C1 E421      1214      IN     AL,INTA01
E5C3 24FC      1215      AND     AL,0FCH        ; ENABLE TIMER AND KB INTS
E5C5 E621      1216      OUT    INTA01,AL

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LOC OBJECT          LINE SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82
E5C7 83FD00        1217      CMP      BP,0000H          ; CHECK FOR BP= NON-ZERO
1218                                     ; (ERROR HAPPENED)
E5CA 7419          1219      JE       F15A_0         ; CONTINUE IF NO ERROR
E5CC BA0200        1220      MOV     DX,2           ; 2 SHORT BEEPS (ERROR)
E5CF E0614         1221      CALL    ERR_BEEP
E5D2 BE09E890      1222      MOV     SI,OFFSET F3D  ; LOAD ERROR MSG
E5D6 E8F113        1223      CALL    P_MSG
E5D9              1224      ERR_WAIT
E5D9 B400          1225      MOV     AH,00
E5DB CD16          1226      INT     16H           ; WAIT FOR 'F1' KEY
E5DD 80FC3B        1227      CMP     AH,3BH
E5E0 75F7          1228      JNE     ERR_WAIT
E5E2 EB0E90         1229      JMP     F15A           ; BYPASS ERROR
E5E5              1230      F15A_0:
E5E5 803E120001     1231      CMP     MFG_TST,1     ; MFG MODE
E5EA 7406          1232      JE      F15A         ; BYPASS BEEP
E5EC BA0100        1233      MOV     DX,1         ; 1 SHORT BEEP (NO ERRORS)
E5EF E8E613        1234      CALL    ERR_BEEP
E5F2 A01000        1235      AND     AL,BYTE PTR EQUIP_FLAG ; GET SWITCHES
E5F5 2401          1236      MOV     AL,0000001B   ; 'LOOP POST' SWITCH ON
E5F7 7503          1237      JNZ    F15B         ; CONTINUE WITH BRING-UP
E5F9 E95FFA        1238      JMP     START
E5FC 2AE4          1239      SUB     AH,AH
E5FE A04900        1240      MOV     AL,CRT_MODE
E601 CD10          1241      INT     10H         ; CLEAR SCREEN
E603              1242      F15C:
E603 BDA3F990      1243      MOV     BP,OFFSET F4 ; PRT_SRC_TBL
E607 BE0000        1244      MOV     SI,0
E60A              1245      F16:
E60A 2E8B5600       1246      MOV     DX,CS:[BP]   ; GET PRINTER BASE ADDR
E60E B0AA         1247      MOV     AL,0AAH     ; WRITE DATA TO PORT A
E610 EE           1248      OUT     DX,AL
E611 1E           1249      PUSH   DS           ; BUS SETTLEING
E612 EC           1250      IN     AL,DX        ; READ PORT A
E613 1F           1251      POP    DS
E614 3CAA         1252      CMP     AL,0AAH     ; DATA PATTERN SAME
E616 7505         1253      JNE     F17         ; NO - CHECK NEXT PRT CD
E618 895408        1254      MOV     PRINTER_BASE[SI],DX ; YES - STORE PRT BASE ADDR
E61B 46           1255      INC    SI           ; INCREMENT TO NEXT WORD
E61C 46           1256      INC    SI
E61D              1257      F17:
E61D 45           1258      INC    BP           ; POINT TO NEXT BASE ADDR
E61E 45           1259      INC    BP
E61F 81FDA9F9      1260      CMP     BP,OFFSET F4E ; ALL POSSIBLE ADDRS CHECKED?
E623 76E5         1261      JNE     F16         ; PRT_BASE
E625 B00000        1262      MOV     BX,0        ; POINTER TO RS232 TABLE
E628 BAF403        1263      MOV     DX,3FAH     ; CHECK IF RS232 CD 1 ATTCH?
E62B EC           1264      IN     AL,DX
E62C A8F8         1265      TEST   AL,0F8H
E62E 7506         1266      JNZ    F18
E630 C707F803      1267      MOV     RS232_BASE[BX],3F8H ; SETUP RS232 CD #1 ADDR
E634 43           1268      INC    BX
E635 43           1269      INC    BX
E636              1270      F18:
E636 BAF402        1271      MOV     DX,2FAH     ; CHECK IF RS232 CD 2 ATTCH
E639 EC           1272      IN     AL,DX
E63A A8F8         1273      TEST   AL,0F8H
E63C 7506         1274      JNZ    F19
E63E C707F802      1275      MOV     RS232_BASE[BX],2F8H ; SETUP RS232 CD #2
E642 43           1276      INC    BX
E643 43           1277      INC    BX
1279      ;---- SET UP EQUIP FLAG TO INDICATE NUMBER OF PRINTERS AND RS232 CARDS
1280
E644              1281      F19:
E644 8BC6         1282      MOV     AX,SI       ; BASE END:
E646 B103         1283      OR     CL,3        ; SI HAS 2* NUMBER OF RS232
E648 D2C8         1284      ROR    AL,CL       ; SHIFT COUNT
E64A 0AC3         1285      OR     AL,9L       ; ROTATE RIGHT 3 POSITIONS
E64C A21100       1286      MOV     BYTE PTR EQUIP_FLAG+1,AL ; OR IN THE PRINTER COUNT
E64F BA0102        1287      MOV     DX,201H    ; STORE AS SECOND BYTE
E652 EC           1288      IN     AL,DX
E653 90           1289      NOP
E654 90           1290      NOP
E655 90           1291      NOP
E656 A80F         1292      TEST   AL,0FH
E658 7505         1293      JNZ    F20         ; NO_GAME_CARD
E65A 800E110010    1294      OR     BYTE PTR EQUIP_FLAG+1,16 ;
E65F              1295      F20:
1296                                     ; NO_GAME_CARD:
1297      ;---- ENABLE NMI INTERRUPTS
1298
E65F E461         1299      IN     AL,PORT_B   ; RESET CHECK ENABLES
E661 0C30         1300      OR     AL,30H
E663 E661         1301      OUT    PORT_B,AL
E665 24CF         1302      AND     AL,0CFH
E667 E561         1303      OUT    PORT_B,AL
E669 B080         1304      MOV     AL,80H
E66B E6A0         1305      OUT    0A0H,AL     ; ENABLE NMI INTERRUPTS
E66D              1306      F21:
E66D CD19         1307      INT     19H       ; LOAD BOOT_STRAP;
1308                                     ; GO TO THE BOOT LOADER

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1309 -----
1310 THIS SUBROUTINE PERFORMS A READ/WRITE STORAGE TEST ON A BLOCK ;
1311 OF STORAGE. ;
1312 ENTRY REQUIREMENTS: ;
1313 ES = ADDRESS OF STORAGE SEGMENT BEING TESTED ;
1314 DS = ADDRESS OF STORAGE SEGMENT BEING TESTED ;
1315 CX = WORD COUNT OF STORAGE BLOCK TO BE TESTED ;
1316 EXIT PARAMETERS: ;
1317 ZERO FLAG = 0 IF STORAGE ERROR (DATA COMPARE OR PARITY ;
1318 CHECK). AL=0 DENOTES A PARITY CHECK. ELSE AL=XOR^ED ;
1319 BIT PATTERN OF THE EXPECTED DATA PATTERN VS THE ACTUAL ;
1320 DATA READ. ;
1321 AX,BX,CX,DX,D1, AND SI ARE ALL DESTROYED. ;
1322 -----
1323
E66F STGTST_CNT PROC NEAR
E66F FC 1324 CLD ; SET DIR FLAG TO INCREMENT
E670 2BFF 1325 SUB DI,D1 ; SET DI=OFFSET 0 REL TO ES REG
E672 2B0C 1326 SUB AX,AX ; SETUP FOR 0->FF PATTERN TEST
E674 1327 C2_1:
E674 8B05 1328 MOV [DI],AL ; ON FIRST BYTE
E676 8A05 1329 MOV AL,[DI]
E678 32C4 1330 XOR AL,AH ; O.K.?
E67A 754D 1331 JNZ C7 ; GO ERROR IF NOT
E67C FECA 1332 INC AH
E67E 8AC4 1333 MOV AL,AH
E680 75F2 1334 JNZ C2_1 ; LOOP TILL WRAP THROUGH FF
E682 8B09 1335 MOV BX,CX ; SAVE WORD COUNT OF BLOCK TO TEST
E684 D1E3 1336 SHL BX,1 ; CONVERT TO A BYTE COUNT
E686 B8AAAA 1337 MOV AX,0AAAAH ; GET INITIAL DATA PATTERN TO WRITE
E688 BA55FF 1338 MOV DX,0FF55H ; SETUP OTHER DATA PATTERNS TO USE
E68C F3 1339 REP STOSW ; FILL STORAGE LOCATIONS IN BLOCK
E68D AB
E68E E461 1340 IN AL,PORT_B
E690 0C30 1341 OR AL,00110000B ; TOGGLE PARITY CHECK LATCHES
E692 E661 1342 OUT PORT_B,AL
E694 90 1343 NOP
E695 24CF 1344 AND AL,11001111B
E697 E661 1345 OUT PORT_B,AL
E699 1346
E699 4F 1347 DEC DI ; POINT TO LAST BYTE JUST WRITTEN
E69A FD 1348 STD ; SET DIR FLAG TO GO BACKWARDS
E69B
E69B 8BF7 1349 C4: MOV SI,DI ; INITIALIZE DESTINATION POINTER
E69D 8BCB 1350 MOV CX,BX ; SETUP BYTE COUNT FOR LOOP
E69F 1351 C5: ; INNER TEST LOOP
E69F AC 1352 LODSB ; READ OLD TEST BYTE FROM STG [SI]**
E6A0 32C4 1353 XOR AL,AH ; DATA READ AS EXPECTED?
E6A2 7525 1354 JNE C7 ; NO - GO TO ERROR ROUTINE
E6A4 8AC2 1355 MOV AL,DL ; GET NEXT DATA PATTERN TO WRITE
E6A6 AA 1356 STOSB ; WRITE INTO LOC JUST READ [DI]**
E6A7 E2F6 1357 LOOP C5 ; DECREMENT BYTE COUNT AND LOOP CX
E6A9 22E4 1358 AND AH,AH ; ENDING ZERO PATTERN WRITTEN TO STG ?
E6AB 7416 1359 JZ C6 ; YES - RETURN TO CALLER WITH AL=0
E6AD 8AE0 1360 MOV AH,AL ; SETUP NEW VALUE FOR COMPARE
E6AF 86F2 1361 XCHG DH,DL ; MOVE NEXT DATA PATTERN TO DL
E6B1 22E4 1362 AND AH,AH ; ENDING ZERO PATTERN THIS PASS?
E6B3 7504 1363 JNE C6 ; CONTINUE TEST SEQUENCE TILL ZERO DATA
E6B5 8AD4 1364 MOV DL,AH ; ELSE SET ZERO FOR END READ PATTERN
E6B7 EB0E 1365 JMP C3 ; AND MAKE FINAL BACKWARDS PASS
E6B9
E6B9 FC 1366 C6: CLD ; SET DIR FLAG TO GO FORWARD
E6BA 47 1367 INC DI ; SET POINTER TO BEG LOCATION
E6BB 74DE 1368 JZ C4 ; READ/WRITE FORWARD IN STG
E6BD 4F 1369 DEC DI ; ADJUST POINTER
E6BE BA0100 1370 MOV DX,000010H ; SETUP 01 FOR PARITY BIT AND 00 FOR END
E6C1 EBD6 1371 JMP C3 ; READ/WRITE BACKWARD IN STG
E6C3
E6C3 E462 1372 C6X: IN AL,PORT_C ; DID A PARITY ERROR OCCUR?
E6C5 24C0 1373 AND AL,0C00H ; ZERO FLAG WILL BE OFF PARITY ERROR
E6C7 B000 1374 MOV AL,0000H ; AL=0 DATA COMPARE OK
E6C9
E6C9 FC 1375 C7: CLD ; SET DIRECTION FLAG TO INC
E6CA C3 1376 RET
E6CB STGTST_CNT ENDP
E6CC -----
E6CC 1384 ; PRINT ADDRESS AND ERROR MESSAGE FOR ROM CHECKSUM ERRORS
E6CD 1385 ;
E6CE 1386 -----
E6CB ROM_ERR PROC NEAR
E6CB 52 1387 PUSH DX ; SAVE POINTER
E6CC 50 1388 AX
E6CD 8CDA 1389 MOV DX,DS ; GET ADDRESS POINTER
E6CF 2688361500 1390 MOV ES:IMFG_ERR_FLAG,DX ;
E6D4 81FA00C8 1391 CMP DX,0C800H ;
E6D8 7CDD 1392 JL ROM_ERR_BEEP ; CRT CARD IN ERROR?
E6DA EBFDF8 1393 CALL PRT_SEG ; GIVE CRT CARD FAIL BEEP
E6DD BE0AF990 1394 MOV SI,OFFSET F3A ; PRINT SEGEMENT IN ERROR
E6E1 EBC512 1395 CALL E_MSG ; DISPLAY ERROR MSG
E6E4
E6E4 58 1396 ROM_ERR_END: POP AX
E6E5 5A 1400 POP DX
E6E6 C3 1401 RET
E6E7 1402 ROM_ERR_BEEP:
E6E7 BA0201 1403 MOV ER,0102H ; BEEP 1 LONG, 2 SHORT
E6EA EBEB12 1404 CALL ER_BEEP
E6ED EBF5 1405 JMP SHORT ROM_ERR_END
E6EE 1406 ROM_ERR ENDP
E6EF 1407

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LINE SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82
1408 ;--- INT 19 -----
1409 ; BOOT STRAP LOADER ;
1410 ; TRACK 0, SECTOR 1 IS READ INTO THE ;
1411 ; BOOT LOCATION (SEGMENT 0, OFFSET 7C00) ;
1412 ; AND CONTROL IS TRANSFERRED THERE. ;
1413 ; ;
1414 ; IF THERE IS A HARDWARE ERROR CONTROL IS ;
1415 ; TRANSFERRED TO THE ROM BASIC ENTRY POINT. ;
1416 ;-----
1417 ASSUME CS:CODE,DS:ABS0
1418 ORG 0E6F2H
1419
E6F2 1420 BOOT_STRAP PROC NEAR
E6F2 1421 STI ; ENABLE INTERRUPTS
E6F3 1422 SUB AX,AX ; ESTABLISH ADDRESSING
E6F5 1423 MOV DS,AX
1424
1425 ;----- RESET THE DISK PARAMETER TABLE VECTOR
1426
E6F7 1427 MOV WORD PTR DISK_POINTER, OFFSET DISK_BASE
E6FD 1428 MOV WORD PTR DISK_POINTER+2,CS
1429
1430 ;----- LOAD SYSTEM FROM DISKETTE -- CX HAS RETRY COUNT
1431
E701 1432 MOV CX,4 ; SET RETRY COUNT
E704 1433 HI: ; IPL SYSTEM
E704 1434 PUSH CX ; SAVE RETRY COUNT
E705 1435 MOV AH,0 ; RESET THE DISKETTE SYSTEM
E707 1436 INT 13H ; DISKETTE_IO
E709 1437 JC H2 ; IF ERROR, TRY AGAIN
E70B 1438 MOV AX,201H ; READ IN THE SINGLE SECTOR
E70E 1439 SUB DX,DX ; TO THE BOOT LOCATION
E710 1440 MOV ES,DX
E712 1441 MOV BX,OFFSET BOOT_LOCN
1442
E715 1442 MOV CX,1 ; DRIVE 0, HEAD 0
E718 1443 INT 13H ; SECTOR 1, TRACK 0
E71A 1444 H2: ; DISKETTE_IO
E71A 1445 ;
E71A 1446 POP CX ; RECOVER RETRY COUNT
E71B 1447 JNC H4 ; CF SET BY UNSUCCESSFUL READ
E71D 1448 LOOP H1 ; DO IT FOR RETRY TIMES
1449
1450 ;----- UNABLE TO IPL FROM THE DISKETTE
1451
E71F 1452 H3:
E71F 1453 INT 18H ; GO TO RESIDENT BASIC
1454
1455 ;----- IPL WAS SUCCESSFUL
1456
E721 1457 H4:
E721 1458 JMP BOOT_LOCN
E721 1459 BOOT_STRAP ENDP
1460

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1461 :-----INT 14-----:
1462 : RS232_10 :
1463 : THIS ROUTINE PROVIDES BYTE STREAM I/O TO THE COMMUNICATIONS :
1464 : PORT ACCORDING TO THE PARAMETERS: :
1465 : (AH)=0 INITIALIZE THE COMMUNICATIONS PORT :
1466 : (AL) HAS PARAMETERS FOR INITIALIZATION :
1467 : :
1468 : 7----- 6 5 4 3 2 1 0 :
1469 : ---- BAUD RATE -- -PARITY-- STOPBIT --WORD LENGTH-- :
1470 : 000 - 110 X0 - NONE 0 - 1 10 - 7 BITS :
1471 : 001 - 150 01 - ODD 1 - 2 11 - 8 BITS :
1472 : 010 - 300 11 - EVEN :
1473 : 011 - 600 :
1474 : 100 - 1200 :
1475 : 101 - 2400 :
1476 : 110 - 4800 :
1477 : 111 - 9600 :
1478 : :
1479 : ON RETURN, CONDITIONS SET AS IN CALL TO COMMO STATUS (AH=3) :
1480 : (AH)=1 SEND THE CHARACTER IN (AL) OVER THE COMMO LINE :
1481 : (AL) REGISTER IS PRESERVED :
1482 : ON EXIT, BIT 7 OF AH IS SET IF THE ROUTINE WAS UNABLE :
1483 : TO TRANSMIT THE BYTE OF DATA OVER THE LINE. :
1484 : IF BIT 7 OF AH IS NOT SET, THE REMAINDER OF AH :
1485 : IS SET AS IN A STATUS REQUEST, REFLECTING THE :
1486 : CURRENT STATUS OF THE LINE. :
1487 : (AH)=2 RECEIVE A CHARACTER IN (AL) FROM COMMO LINE BEFORE :
1488 : RETURNING TO CALLER :
1489 : ON EXIT, AH HAS THE CURRENT LINE STATUS, AS SET BY THE :
1490 : THE STATUS ROUTINE, EXCEPT THAT THE ONLY BITS :
1491 : LEFT ON ARE THE ERROR BITS (7,4,3,2,1) :
1492 : IF AH HAS BIT 7 ON (TIME OUT) THE REMAINING :
1493 : BITS ARE NOT PREDICTABLE. :
1494 : THUS, AH IS NON ZERO ONLY WHEN AN ERROR :
1495 : OCCURRED. :
1496 : (AH)=3 RETURN THE COMMO PORT STATUS IN (AX) :
1497 : AH CONTAINS THE LINE STATUS :
1498 : BIT 7 = TIME OUT :
1499 : BIT 6 = TRANS. SHIFT REGISTER EMPTY :
1500 : BIT 5 = TRAN HOLDING REGISTER EMPTY :
1501 : BIT 4 = BREAK DETECT :
1502 : BIT 3 = FRAMING ERROR :
1503 : BIT 2 = PARITY ERROR :
1504 : BIT 1 = OVERRUN ERROR :
1505 : BIT 0 = DATA READY :
1506 : AL CONTAINS THE MODEM STATUS :
1507 : BIT 7 = RECEIVED LINE SIGNAL DETECT :
1508 : BIT 6 = RING INDICATOR :
1509 : BIT 5 = DATA SET READY :
1510 : BIT 4 = CLEAR TO SEND :
1511 : BIT 3 = DELTA RECEIVE LINE SIGNAL DETECT :
1512 : BIT 2 = TRAILING EDGE RING DETECTOR :
1513 : BIT 1 = DELTA DATA SET READY :
1514 : BIT 0 = DELTA CLEAR TO SEND :
1515 : :
1516 : (DX) = PARAMETER INDICATING WHICH RS232 CARD (0,1 ALLOWED) :
1517 : :
1518 : DATA AREA RS232 BASE CONTAINS THE BASE ADDRESS OF THE 8250 ON THE :
1519 : CARD LOCATION 400H CONTAINS UP TO 4 RS232 ADDRESSES POSSIBLE :
1520 : DATA AREA LABEL RS232_TIM_OUT (BYTE) CONTAINS OUTER LOOP COUNT :
1521 : VALUE FOR TIMEOUT (DEFAULT=1) :
1522 : :
1523 : OUTPUT :
1524 : AX MODIFIED ACCORDING TO PARMS OF CALL :
1525 : ALL OTHERS UNCHANGED :
-----:
E729 : ASSUME CS:CODE,DS:DATA :
E729 : A1 LABEL WORD ; TABLE OF INIT VALUES :
E729 : DW 1047 ; 110 BAUD :
E729 : DW 768 ; 150 :
E729 : DW 384 ; 300 :
E729 : DW 192 ; 600 :
E729 : DW 96 ; 1200 :
E729 : DW 48 ; 2400 :
E729 : DW 24 ; 4800 :
E729 : DW 12 ; 9600 :
E729 : :
E739 : RS232_10 PROC FAR :
E739 : :
E740 : ----- VECTOR TO APPROPRIATE ROUTINE :
E739 FB : STI ; INTERRUPTS BACK ON :
E73A 1E : PUSH DS ; SAVE SEGMENT :
E73B 52 : PUSH DX :
E73C 56 : PUSH SI :
E73D 57 : PUSH DI :
E73E 51 : PUSH CX :
E73F 53 : PUSH BX :
E740 8BF2 : MOV SI,DX ; RS232 VALUE TO SI :
E742 8BFA : MOV DI,DX :
E744 D1E6 : SHL SI,1 ; WORD OFFSET :
E746 E81013 : CALL DDI :
E74B 08D2 : OR DX,DX ; GET BASE ADDRESS :
E74D 7413 : JZ A3 ; TEST FOR 0 BASE ADDRESS :
E74F 0AE4 : OR AH,AH ; RETURN :
E751 7416 : JZ A4 ; TEST FOR (AH)=0 :
E753 FECC : DEC AH ; TEST FOR (AH)=1 :
E755 7445 : JZ A5 ; SEND AL :
E757 FECC : DEC AH ; TEST FOR (AH)=2 :
E759 746A : JZ A12 ; RECEIVE INTO AL :
E75B : :
E75B FECC : A2: DEC AH ; TEST FOR (AH)=3 :
E75D 7503 : JNZ A3 :
E75F E98300 : JMP A18 ; COMMUNICATION STATUS :
E762 : :
E762 5B : A3: POP BX ; RETURN FROM RS232 :
E763 59 : POP CX :
E764 5F : POP DI :
E765 5E : POP SI :
E766 5A : POP DX :
E767 1F : POP DS :
E768 CF : IRET ; RETURN TO CALLER, NO ACTION :
1574 :

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1575 ;---- INITIALIZE THE COMMUNICATIONS PORT
1576
1577 A4:
1578 MOV AH,AL ; SAVE INIT PARMS IN AH
1579 ADD DX,3 ; POINT TO 8250 CONTROL REGISTER
1580 MOV AL,80H
1581 OUT DX,AL ; SET DLAB=1
1582
1583 ;---- DETERMINE BAUD RATE DIVISOR
1584
1585 MOV DL,AH ; GET PARMS TO DL
1586 MOV CL,4
1587 ROL DL,CL
1588 AND DX,00EH ; ISOLATE THEM
1589 D1,OFFSET A1 ; BASE OF TABLE
1590 ADD D1,DX ; PUT INTO INDEX REGISTER
1591 MOV DX,RS232_BASE[SI] ; POINT TO HIGH ORDER OF DIVISOR
1592 INC DX
1593 MOV AL,CS:[D1]+1 ; GET HIGH ORDER OF DIVISOR
1594 OUT DX,AL ; SET MS OF DIV TO 0
1595 DEC DX
1596 MOV AL,SC:[D1] ; GET LOW ORDER OF DIVISOR
1597 OUT DX,AL ; SET LOW OF DIVISOR
1598 ADD DX,3
1599 MOV AL,AH ; GET PARMS BACK
1599 AND AL,01FH ; STRIP OFF THE BAUD BITS
1600 OUT DX,AL ; LINE CONTROL TO 8 BITS
1601 INC DX
1602 DEC DX
1603 DEC DX
1604 MOV AL,0
1605 OUT DX,AL ; INTERRUPT ENABLES ALL OFF
1606 JMP SHORT A18 ; COM_STATUS
1607
1608 ;---- SEND CHARACTER IN (AL) OVER COMMO LINE
1609
1610 A5:
1611 PUSH AX ; SAVE CHAR TO SEND
1612 ADD DX,4 ; MODEM CONTROL REGISTER
1613 MOV AL,3 ; DTR AND RTS
1614 OUT DX,AL ; DATA TERMINAL READY, REQUEST TO SEND
1615 INC DX ; MODEM STATUS REGISTER
1616 INC DX
1617 MOV BH,30H ; DATA SET READY & CLEAR TO SEND
1618 CALL WAIT_FOR_STATUS ; ARE BOTH TRUE
1619 JE A9 ; YES, READY TO TRANSMIT CHAR
1620 A7:
1621 POP CX
1622 MOV AL,CL ; RELOAD DATA BYTE
1623 AB:
1624 OR AH,80H ; INDICATE TIME OUT
1625 JMP A3 ; RETURN
1626 A9:
1627 INC DX ; CLEAR TO SEND
1628 DEC DX ; LINE STATUS REGISTER
1629 MOV BH,20H ; WAIT SEND
1630 CALL WAIT_FOR_STATUS ; IS TRANSMITTER READY
1631 JNZ A7 ; TEST FOR TRANSMITTER READY
1632 JNZ A7 ; RETURN WITH TIME OUT SET
1633 A11:
1634 SUB DX,5 ; OUT_CHAR
1635 POP CX ; DATA PORT
1636 MOV AL,CL ; RECOVER IN CX TEMPORARILY
1637 OUT DX,AL ; MOVE CHAR TO AL FOR OUT, STATUS IN AH
1638 JMP A3 ; OUTPUT CHARACTER
1639 ;---- RECEIVE CHARACTER FROM COMMO LINE
1640
1641 A12:
1642 ADD DX,4 ; MODEM CONTROL REGISTER
1643 MOV AL,1 ; DATA TERMINAL READY
1644 OUT DX,AL ; DATA TERMINAL READY
1645 INC DX ; MODEM STATUS REGISTER
1646 INC DX
1647 A13:
1648 MOV BH,20H ; WAIT DSR
1649 CALL WAIT_FOR_STATUS ; DATA SET READY
1650 JNZ A8 ; TEST FOR DSR
1651 A15:
1652 DEC DX ; RETURN WITH ERROR
1653 A16:
1654 MOV BH,1 ; WAIT DSR END
1655 CALL WAIT_FOR_STATUS ; LINE STATUS REGISTER
1656 JNZ A8 ; WAIT RECV
1657 A17:
1658 AND AH,00011110B ; RECETIVE BUFFER FULL
1659 MOV DX,RS232_BASE[SI] ; TEST FOR REC. BUFF. FULL
1660 IN AL,DX ; SET TIME OUT ERROR
1661 JMP A3 ; GET CHAR
1662 ; TEST FOR ERR CONDITIONS ON RECV CHAR
1663 ; DATA PORT
1664 ; GET CHARACTER FROM LINE
1665 ; RETURN
1666 ;---- COMMO PORT STATUS ROUTINE
1667
1668 A18:
1669 MOV DX,RS232_BASE[SI]
1670 ADD DX,5
1671 IN AH,DX ; CONTROL PORT
1672 MOV AL,DX ; GET LINE CONTROL STATUS
1673 INC DX ; PUT IN AH FOR RETURN
1674 IN AL,DX ; POINT TO MODEM STATUS REGISTER
1675 MOV AL,DX ; GET MODEM CONTROL STATUS
1676 JMP A3 ; RETURN

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1673 ; -----
1674 ; WAIT FOR STATUS ROUTINE ;
1675 ; ;
1676 ; ENTRY: ;
1677 ; BH=STATUS BIT(S) TO LOOK FOR, ;
1678 ; DX=ADDR. OF STATUS REG ;
1679 ; EXIT: ZERO FLAG ON = STATUS FOUND ;
1680 ; ZERO FLAG OFF = TIMEOUT. ;
1681 ; AH=LAST STATUS READ ;
1682 ; -----
1683
E7F2 1684 WAIT_FOR_STATUS PROC NEAR
E7F2 8A5D7C 1685 MOV BL,RS232_TIM_OUT[D1] ; LOAD OUTER LOOP COUNT
E7F5 1686 WFS0: SUB CX,CX
E7F5 2BC9 1687
E7F7 1688 WFS1:
E7F7 EC 1689 IN AL,DX ; GET STATUS
E7F8 8AE0 1690 MOV AH,AL ; MOVE TO AH
E7FA 22C7 1691 AND AL,BH ; ISOLATE BITS TO TEST
E7FC 3AC7 1692 CMP AL,BH ; EXACTLY = TO MASK
E7FE 1408 1693 JE WFS_END ; RETURN WITH ZERO FLAG ON
E800 E2F5 1694 LOOP WFS1 ; TRY AGAIN
E802 FECB 1695 DEC BL
E804 75EF 1696 JNZ WFS0
E804 75EF 1697
E806 0AFF 1698 OR BH,BH ; SET ZERO FLAG OFF
E808 1699 WFS_END:
E808 C3 1700 RET
E808 C3 1701 WAIT_FOR_STATUS ENDP
1702 RS232_ID ENDP
1703
E809 4562634F522E20 1704 F3D DB 'ERROR. (RESUME = F1 KEY)',13,10 ; ERROR PROMPT
28524553554D45
203D2022463122
204B455929
E823 0D
E824 0A
1705

```

```

1706 ;---- INT 16
1707 ; KEYBOARD I/O
1708 ; THESE ROUTINES PROVIDE KEYBOARD SUPPORT
1709 ; INPUT
1710 ; (AH)=0 READ THE NEXT ASCII CHARACTER STRUCK FROM THE KEYBOARD
1711 ; RETURN THE RESULT IN (AL), SCAN CODE IN (AH)
1712 ; (AH)=1 SET THE Z FLAG TO INDICATE IF AN ASCII CHARACTER IS
1713 ; AVAILABLE TO BE READ.
1714 ; (ZF)=1 -- NO CODE AVAILABLE
1715 ; (ZF)=0 -- CODE IS AVAILABLE
1716 ; IF ZF = 0, THE NEXT CHARACTER IN THE BUFFER TO BE READ
1717 ; IS IN AX, AND THE ENTRY REMAINS IN THE BUFFER
1718 ; (AH)=2 RETURN THE CURRENT SHIFT STATUS IN AL REGISTER
1719 ; THE BIT SETTINGS FOR THIS CODE ARE INDICATED IN THE
1720 ; THE EQUATES FOR KB_FLAG
1721 ; OUTPUT
1722 ; AS NOTED ABOVE, ONLY AX AND FLAGS CHANGED
1723 ; ALL REGISTERS PRESERVED
-----
1724 ; ASSUME CS:CODE,DS:DATA
1725 ; OR 0E82EH
1726 ; PROC FAR
1727 ; STI
1728 ; INT28H
1729 ; INT28H
1730 ; INT28H
1731 ; INT28H
1732 ; INT28H
1733 ; INT28H
1734 ; INT28H
1735 ; INT28H
1736 ; INT28H
1737 ; INT28H
1738 ; INT28H
1739 ; INT28H
1740 ;---- READ THE KEY TO FIGURE OUT WHAT TO DO
1741 ;
1742 K1:
1743 ; INT28H
1744 ; INT28H
1745 ; INT28H
1746 ; INT28H
1747 ; INT28H
1748 ; INT28H
1749 ; INT28H
1750 ; INT28H
1751 ; INT28H
1752 ; INT28H
1753 ; INT28H
1754 ; INT28H
1755 ; INT28H
1756 ; INT28H
1757 ; INT28H
1758 ; INT28H
1759 ; INT28H
1760 ; INT28H
1761 ; INT28H
1762 ; INT28H
1763 ; INT28H
1764 ; INT28H
1765 ; INT28H
1766 ; INT28H
1767 ; INT28H
1768 ; INT28H
1769 ; INT28H
1770 ; INT28H
1771 ; INT28H
1772 ; INT28H
1773 ; INT28H
1774 ; INT28H
1775 ; INT28H
1776 ; INT28H
1777 ; INT28H
1778 ; INT28H
1779 ; INT28H
1780 ; INT28H
1781 ; INT28H
1782 ; INT28H
1783 ; INT28H
1784 ; INT28H
1785 ; INT28H
1786 ; INT28H
1787 ; INT28H
1788 ; INT28H
1789 ; INT28H
1790 ; INT28H
1791 ; INT28H
1792 ; INT28H
1793 ; INT28H
1794 ; INT28H
1795 ; INT28H
1796 ; INT28H
1797 ; INT28H
1798 ; INT28H
1799 ; INT28H
1800 ; INT28H
1801 ; INT28H
1802 ; INT28H
1803 ; INT28H
1804 ; INT28H
1805 ; INT28H
-----
E82E 1726 KEYBOARD_OR 0E82EH
E82E FB 1727 STI
E82F 1E 1728 INT28H
E830 53 1729 INT28H
E831 E82512 1730 INT28H
E834 0AE4 1731 INT28H
E836 740A 1732 INT28H
E838 FECC 1733 INT28H
E83A 741E 1734 INT28H
E83C FECC 1735 INT28H
E83E 742B 1736 INT28H
E840 EB2C 1737 INT28H
E842 1738 INT28H
E842 FB 1739 INT28H
E843 90 1740 INT28H
E844 FA 1741 INT28H
E845 8B1E1A00 1742 INT28H
E849 3B1E1C00 1743 INT28H
E84D 74F3 1744 INT28H
E84F 8B07 1745 INT28H
E851 E81D00 1746 INT28H
E854 891E1A00 1747 INT28H
E858 EB14 1748 INT28H
E85A 1749 INT28H
E85A FA 1750 INT28H
E85B 8B1E1A00 1751 INT28H
E85F 3B1E1C00 1752 INT28H
E863 8B07 1753 INT28H
E865 FB 1754 INT28H
E866 5B 1755 INT28H
E867 1F 1756 INT28H
E868 CA0200 1757 INT28H
E86B 1758 INT28H
E86B A01700 1759 INT28H
E86E 5B 1760 INT28H
E86F 1F 1761 INT28H
E870 CF 1762 INT28H
E871 1763 INT28H
E871 43 1764 INT28H
E872 43 1765 INT28H
E873 3B1E8200 1766 INT28H
E877 7504 1767 INT28H
E879 8B1E8000 1768 INT28H
E87D 1769 INT28H
E87D C3 1770 INT28H
E87E 1771 INT28H
E87E 52 1772 INT28H
E87F 3A 1773 INT28H
E880 45 1774 INT28H
E881 46 1775 INT28H
E882 38 1776 INT28H
E883 1D 1777 INT28H
E884 2A 1778 INT28H
E885 36 1779 INT28H
0008 1780 INT28H
E886 1781 INT28H
E886 80 1782 INT28H
E887 40 1783 INT28H
E888 20 1784 INT28H
E889 10 1785 INT28H
E88A 08 1786 INT28H
E88B 04 1787 INT28H
E88C 02 1788 INT28H
E88D 01 1789 INT28H
E88E 1B 1790 INT28H
E88F FF 1791 INT28H
E890 00 1792 INT28H
E891 FF 1793 INT28H
E892 FF 1794 INT28H
E893 FF 1795 INT28H
E894 1E 1796 INT28H

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SECTION 5

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LOC OBJECT                LINE  SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82
E895 FF
E896 FF
E897 FF      1806      DB      -1,-1,-1,31,-1,127,-1,17
E898 FF
E899 1F
E89A FF
E89B 7F
E89C FF
E89D 11
E89E 17
E89F 05      1807      DB      23,5,18,20,25,21,9,15
E8A0 12
E8A1 14
E8A2 19
E8A3 15
E8A4 09
E8A5 0F
E8A6 10
E8A7 1B      1808      DB      16,27,29,10,-1,1,19
E8A8 1D
E8A9 0A
E8AA FF
E8AB 01
E8AC 13
E8AD 04      1809      DB      4,6,7,8,10,11,12,-1,-1
E8AE 06
E8AF 07
E8B0 08
E8B1 0A
E8B2 0B
E8B3 0C
E8B4 FF
E8B5 FF
E8B6 FF      1810      DB      -1,-1,28,26,24,3,22,2
E8B7 FF
E8B8 1C
E8B9 1A
E8BA 18
E8BB 03
E8BC 16
E8BD 02
E8BE 0E      1811      DB      14,13,-1,-1,-1,-1,-1,-1
E8BF 0D
E8C0 FF
E8C1 FF
E8C2 FF
E8C3 FF
E8C4 FF
E8C5 FF
E8C6 20      1812      DB      * *,-1
E8C7 FF
E8C8      1813      ;----- CTL TABLE SCAN
E8C8 5E      1814      K9      LABEL BYTE
E8C9 5F      1815      DB      94,95,96,97,98,99,100,101
E8CA 60
E8CB 61
E8CC 62
E8CD 63
E8CE 64
E8CF 65
E8D0 66      1816      DB      102,103,-1,-1,119,-1,132,-1
E8D1 67
E8D2 FF
E8D3 FF
E8D4 77
E8D5 FF
E8D6 84
E8D7 FF
E8D8 73      1817      DB      115,-1,116,-1,117,-1,118,-1
E8D9 FF
E8DA 74
E8DB FF
E8DC 75
E8DD FF
E8DE 76
E8DF FF
E8E0 FF      1818      DB      -1
E8E1      1819      ;----- LC TABLE
E8E1 1B      1820      K10     LABEL BYTE
E8E2 31323334353637  1821      DB      01BH,'1234567890-='',08H,09H
E8E2 3839302D3D
E8EE 08
E8EF 09
E8F0 71776572747975  1822      DB      'qwertyuiop[]',0DH,-1,'asdfghjkl;','027H
E8F0 696F705B5D
E8FC 0D
E8FD FF
E8FE 6173646667686A  1823      DB      60H,-1,5CH,'zxcvbnm,/','-','*','-1',' '
E8FE 6B6C3B
E908 27
E909 60
E90A FF
E90B 5C
E90C 7A786376626E6D  1824      DB      -1
E90C 2C2E2F
E916 FF
E917 2A
E918 FF
E919 20
E91A FF      1825      ;----- UC TABLE
E91A      1826      K11     LABEL BYTE
E91B      1827      DB      27,'@#&','37,05EH','&'()_+','08H,0
E91B 1B
E91C 21402324
E920 25
E921 5E
E922 262A28295F2B
E928 0B
E929 00
E92A 51574552545955  1828      DB      'QWERTYUIOP{}',0DH,-1,'ASDFGHJKL;''
E92A 494F507B7D
E936 0D
E937 FF
E938 4153444647484A

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LOC OBJECT                               LINE SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82
4B4C3A22
E943 7E                                  1829                DB      0'EH,-1,'|ZXCVBNM<->?',-1,0,-1,' ','-1
E944 FF
E945 7C5A584356424E
      4D3C3E3F
E950 FF
E951 00
E952 FF
E953 20
E954 FF

E955                                     1830 ;----- UC TABLE SCAN
E955 54                                  1831 K12 LABEL BYTE
E956 55                                  1832                DB      84,85,86,87,88,89,90
E957 56
E958 57
E959 58
E95A 59
E95B 5A
E95C 5B                                  1833                DB      91,92,93
E95D 5C
E95E 5D

E95F                                     1834 ;----- ALT TABLE SCAN
E95F 68                                  1835 K13 LABEL BYTE
E960 69                                  1836                DB      104,105,106,107,108
E961 6A
E962 6B
E963 6C                                  1837                DB      109,110,111,112,113
E964 6D
E965 6E
E966 6F
E967 70
E968 71

E969                                     1838 ;----- NUM STATE TABLE
E969 3738392D343536                      1839 K14 LABEL BYTE
      2B313233302E                        1840                DB      '789-456+1230.'

E976                                     1841 ;----- BASE CASE TABLE
E976 47                                  1842 K15 LABEL BYTE
E977 48                                  1843                DB      71,72,73,-1,75,-1,77
E978 49
E979 FF
E97A 4B
E97B FF
E97C 4D
E97D FF
E97E 4F                                  1844                DB      -1,79,80,81,82,83
E97F 50
E980 51
E981 52
E982 53

1845 ;----- KEYBOARD INTERRUPT ROUTINE
1846
1847
1848 ORG 0E987H
E987 KB_INT PROC FAR
1850 STI ; ALLOW FURTHER INTERRUPTS
E988 50 1851 PUSH AX
E989 53 1852 PUSH BX
E98A 51 1853 PUSH CX
E98B 52 1854 PUSH DX
E98C 56 1855 PUSH SI
E98D 57 1856 PUSH DI
E98E 1E 1857 PUSH DS
E98F 06 1858 PUSH ES ; FORWARD DIRECTION
E990 FC 1859 CALL DDS
E991 E8C510 1860 IN AL,KB_DATA ; READ IN THE CHARACTER
E994 E460 1861 PUSH AX ; SAVE IT
E996 50 1862 IN AL,KB_CTL ; GET THE CONTROL PORT
E997 E461 1863 MOV AH,AL ; SAVE VALUE
E999 8AE0 1864 OR AL,80H ; RESET BIT FOR KEYBOARD
E99B 0C80 1865 OUT KB_CTL,AL ; GET BACK ORIGINAL CONTROL
E99D E661 1866 XCHG AH,AL ; KB HAS BEEN RESET
E99F 84E0 1867 OUT KB_CTL,AL ; RECOVER SCAN CODE
E9A1 E661 1868 POP AX ; SAVE SCAN CODE IN AH ALSO
E9A3 58 1869 MOV AH,AL
E9A4 8AE0 1870
1871
1872 ;----- TEST FOR OVERRUN SCAN CODE FROM KEYBOARD
1873
1874 CMP AL,OFFH ; IS THIS AN OVERRUN CHAR
E9A6 3CFF 1875 JNZ K16 ; NO, TEST FOR SHIFT KEY
E9A8 7503 1876 JMP K62 ; BUFFER_FULL_BEEP
E9AA E97A02 1877
1878 ;----- TEST FOR SHIFT KEYS
1879
E9AD K16: ; TEST SHIFT
E9AD 247F 1881 AND AL,07FH ; TURN OFF THE BREAK BIT
E9AF 0E 1882 PUSH CS
E9B0 07 1883 POP ES ; ESTABLISH ADDRESS OF SHIFT TABLE
E9B1 BF7EE8 1884 MOV DI,OFFSET K6 ; SHIFT KEY TABLE
E9B4 B90800 1885 MOV CX,K6L ; LENGTH
E9B7 F2 1886 REPNE SCASB ; LOOK THROUGH THE TABLE FOR A MATCH
E9B8 AE ;
E9B9 8AC4 1887 MOV AL,AH ; RECOVER SCAN CODE
E9BB 7403 1888 JE K17 ; JUMP IF MATCH FOUND
E9BD E9B500 1889 JMP K25 ; IF NO MATCH, THEN SHIFT NOT FOUND
1890
1891 ;----- SHIFT KEY FOUND
1892
E9C0 81EF7E8 1893 K17: SUB DI,OFFSET K6+1 ; ADJUST PTR TO SCAN CODE MTCX
E9C4 2E8AA586E8 1894 MOV AH,CS:K7[DI] ; GET MASK INTO AH
E9C9 A880 1895 TEST AL,80H ; TEST FOR BREAK KEY
E9CB 7551 1896 JNZ K23 ; BREAK_SHIFT_FOUND
1897
1898 ;----- SHIFT MAKE FOUND, DETERMINE SET OR TOGGLE
1899
E9CD 80FC10 1900 CMP AH,SCROLL_SHIFT
E9DD 7307 1901 JAE K18 ; IF SCROLL SHIFT OR ABOVE, TOGGLE KEY
1902
1903 ;----- PLAIN SHIFT KEY, SET SHIFT ON
1904
E9D2 08261700 1905 OR KB_FLAG,AH ; TURN ON SHIFT BIT
E9D6 E9B000 1906 JMP K26 ; INTERRUPT_RETURN

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SECTION 5


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LOC OBJECT                LINE  SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82
1907
1908 ;----- TOGGLED SHIFT KEY, TEST FOR 1ST MAKE OR NOT
1909
E9D9                      1910
E9D9 F606170004          1911 K18: TEST    KB_FLAG,CTL_SHIFT      ; SHIFT-TOGGLE
E9DE 7565                1912      JNZ     K25                    ; CHECK CTL_SHIFT STATE
E9E0 3C52                1913      CMP     AL,INS_KEY              ; JUMP IF CTL STATE
E9E2 7522                1914      JNZ     K25                    ; CHECK FOR INSERT KEY
E9E4 F606170008          1915      TEST    KB_FLAG,ALT_SHIFT      ; CHECK FOR ALTERNATE SHIFT
E9E9 755A                1916      JNZ     K25                    ; JUMP IF ALTERNATE SHIFT
E9EB F606170020          1917 K19: TEST    KB_FLAG,NUM_STATE    ; CHECK FOR BASE STATE
E9F0 750D                1918      JNZ     K21                    ; JUMP IF NUM LOCK IS ON
E9F2 F606170003          1919      TEST    KB_FLAG,LEFT_SHIFT+RIGHT_SHIFT
E9F7 740D                1920      JZ      K22                    ; JUMP IF BASE STATE
1921
E9F9                      1922 K20:                ; NUMERIC ZERO, NOT INSERT KEY
E9F9 B83052              1923      MOV     AX,5230H               ; PUT OUT AN ASCII ZERO
E9FC E9D601              1924      JMP     K57                    ; BUFFER FILL
E9FF                      1925 K21:                ; MIGHT BE NUMERIC
E9FF F606170003          1926      TEST    KB_FLAG,LEFT_SHIFT+RIGHT_SHIFT
EA04 74F3                1927      JZ      K20                    ; JUMP NUMERIC, NOT INSERT
1928
EA06                      1929 K22:                ; SHIFT TOGGLE KEY HIT; PROCESS IT
EA06 84261800            1930      TEST    AH,KB_FLAG_1          ; IS KEY ALREADY DEPRESSED
EA0A 754D                1931      JNZ     K26                    ; JUMP IF KEY ALREADY DEPRESSED
EA0C 08261800            1932      OR     KB_FLAG_1,AH           ; INDICATE THAT THE KEY IS DEPRESSED
EA10 30261700            1933      XOR     KB_FLAG,AH            ; TOGGLE THE SHIFT STATE
EA14 3C52                1934      CMP     AL,INS_KEY            ; TEST FOR 1ST MAKE OF INSERT KEY
EA16 7541                1935      JNE     K26                    ; JUMP IF NOT INSERT KEY
EA18 B80052              1936      MOV     AX,INS_KEY*256        ; SET SCAN CODE INTO AH, 0 INTO AL
EA1B E9B701              1937      JMP     K57                    ; PUT INTO OUTPUT BUFFER
1938
1939 ;----- BREAK SHIFT FOUND
1940
EA1E                      1941 K23:                ; BREAK-SHIFT-FOUND
EA1E 80FC10              1942      CMP     AH,SCROLL_SHIFT      ; IS THIS A TOGGLE KEY
EA21 731A                1943      JAE     K24                    ; YES, HANDLE BREAK TOGGLE
EA23 F6D4                1944      NOT     AH                    ; INVERT MASK
EA25 20261700            1945      AND     KB_FLAG,AH            ; TURN OFF SHIFT BIT
EA29 3CB8                1946      CMP     AL,ALT_KEY+80H        ; IS THIS ALTERNATE SHIFT RELEASE
EA2B 752C                1947      JNE     K26                    ; INTERRUPT_RETURN
1948
1949 ;----- ALTERNATE SHIFT KEY RELEASED, GET THE VALUE INTO BUFFER
1950
EA2D A01900              1951      MOV     AL,ALT_INPUT          ;
EA30 B400                1952      MOV     AH,0                  ; SCAN CODE OF 0
EA32 88261900            1953      MOV     ALT_INPUT,AH          ; ZERO OUT THE FIELD
EA36 3C00                1954      CMP     AL,0                  ; WAS THE INPUT=0
EA38 741F                1955      JE      K26                    ; INTERRUPT_RETURN
EA3A E9A101              1956      JMP     K58                    ; IT WASN'T, SO PUT IN BUFFER
EA3D                      1957 K24:                ; BREAK-TOGGLE
EA3D F6D4                1958      NOT     AH                    ; INVERT MASK
EA3F 20261800            1959      AND     KB_FLAG_1,AH          ; INDICATE NO LONGER DEPRESSED
EA43 EB14                1960      JMP     SHORT K26              ; INTERRUPT_RETURN
1961
1962 ;----- TEST FOR HOLD STATE
1963
EA45                      1964 K25:                ; NO-SHIFT-FOUND
EA45 3C80                1965      CMP     AL,80H                ; TEST FOR BREAK KEY
EA47 7310                1966      JAE     K26                    ; NOTHING FOR BREAK CHARS FROM HERE ON
EA49 F606180008          1967      TEST    KB_FLAG_1,HOLD_STATE  ; ARE WE IN HOLD STATE
EA4E 7417                1968      JZ      K28                    ; BRANCH AROUND TEST IF NOT
EA50 3C45                1969      CMP     AL,NUM_KEY            ;
EA52 7405                1970      JE      K26                    ; CAN'T END HOLD ON NUM LOCK
EA54 80261800F7          1971      AND     KB_FLAG_1,NOT_HOLD_STATE ; TURN OFF THE HOLD STATE BIT
EA59                      1972 K26:                ; INTERRUPT-RETURN
EA59 FA                  1973      CLI                             ; TURN OFF INTERRUPTS
EA5A B020                1974      MOV     AL,E01                ; END OF INTERRUPT COMMAND
EA5C E620                1975      OUT     020H,AL              ; SEND COMMAND TO INT CONTROL PORT
EA5E                      1976 K27:                ; INTERRUPT-RETURN-NO-E01
EA5E 07                  1977      POP     ES                    ;
EA5F 1F                  1978      POP     DS                    ;
EA60 5F                  1979      POP     DI                    ;
EA61 5E                  1980      POP     SI                    ;
EA62 5A                  1981      POP     DX                    ;
EA63 59                  1982      POP     CX                    ;
EA64 58                  1983      POP     BX                    ;
EA65 58                  1984      POP     AX                    ; RESTORE STATE
EA66 CF                  1985      IRET                          ; RETURN, INTERRUPTS BACK ON
1986 ; WITH FLAG CHANGE
1987
1988 ;----- NOT IN HOLD STATE, TEST FOR SPECIAL CHARS
1989
EA67                      1990 K28:                ; NO-HOLD-STATE
EA67 F606170008          1991      TEST    KB_FLAG,ALT_SHIFT    ; ARE WE IN ALTERNATE SHIFT
EA6C 7503                1992      JNZ     K29                    ; JUMP IF ALTERNATE SHIFT
EA6E E99100              1993      JMP     K38                    ; JUMP IF NOT ALTERNATE
1994
1995 ;----- TEST FOR RESET KEY SEQUENCE (CTL ALT DEL)
1996
EA71                      1997 K29:                ; TEST-RESET
EA71 F606170004          1998      TEST    KB_FLAG,CTL_SHIFT    ; ARE WE IN CONTROL SHIFT ALSO
EA76 7433                1999      JZ      K31                    ; NO RESET
EA78 3C53                2000      CMP     AL,DEL_KEY            ; SHIFT STATE IS THERE, TEST KEY
EA7A 752F                2001      JNE     K31                    ; NO_RESET
2002
2003 ;----- CTL-ALT-DEL HAS BEEN FOUND, DO I/O CLEANUP
2004
EA7C C70672003412        2005      MOV     RESET_FLAG,1234H      ; SET FLAG FOR RESET FUNCTION
EA82 EA5BE00F0F          2006      JMP     RESET                  ; JUMP TO POWER ON DIAGNOSTICS
2007
EA87                      2008 ;----- ALT-INPUT-TABLE
EA87 52                  2009 K30 LABEL BYTE
EA88 4F                  2010      DB     82,79,80,81,75,76,77
EA89 50
EA8A 51
EA8B 48
EA8C 4C
EA8D 4D
EA8E 47                  2011      DB     71,72,73                ; 10 NUMBERS ON KEYPAD
EA8F 48
EA90 49
2012 ;----- SUPER-SHIFT-TABLE
EA91 10                  2013      DB     16,17,18,19,20,21,22,23 ; A-Z TYPEWRITER CHARS
EA92 11

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LOC OBJECT                               LINE SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82
EA93 12
EA94 13
EA95 14
EA96 15
EA97 16
EA98 17
EA99 18
EA9A 19
EA9B 1E
EA9C 1F
EA9D 20
EA9E 21
EA9F 22
EAA0 23
EAA1 24
EAA2 25
EAA3 26
EAA4 2C
EAA5 2D
EAA6 2E
EAA7 2F
EAA8 30
EAA9 31
EAAA 32
2017
2018 ;----- IN ALTERNATE SHIFT, RESET NOT FOUND
2019
EAB 2020 K31: CMP AL,57 ; NO-RESET
EAB 3C39 2021 JNE K32 ; TEST FOR SPACE KEY
EAD 7505 2022 MOV AL,' ' ; NOT THERE
EAF B020 2023 JMP K57 ; SET SPACE CHAR
EAB1 E92101 2024 ; BUFFER_FILL
2025
2026 ;----- LOOK FOR KEY PAD ENTRY
2027
EAB4 2028 K32: MOV DI,OFFSET K30 ; ALT-KEY-PAD
EAB4 BF87EA 2029 MOV CX,10 ; ALT-INPUT-TABLE
EAB7 B90A00 2030 REPNE SCASB ; LOOK FOR ENTRY USING KEYPAD
EABA F2 2031 ; LOOK FOR MATCH
EAB8 AE
EABC 7512 2032 JNE K33 ; NO ALT-KEYPAD
EABE 81EF88EA 2033 SUB DI,OFFSET K30+1 ; DI NOW HAS ENTRY VALUE
EAC2 A01900 2034 MOV AL,ALT_INPUT ; GET THE CURRENT BYTE
EAC5 B40A 2035 MOV AH,10 ; MULTIPLY BY 10
EACT F6E4 2036 MUL AH
EAC9 03C7 2037 ADD AX,DI ; ADD IN THE LATEST ENTRY
EACB A21900 2038 MOV ALT_INPUT,AL ; STORE IT AWAY
EACE EB89 2039 JMP K26 ; THROW AWAY THAT KEYSTROKE
2040
2041 ;----- LOOK FOR SUPERSHIFT ENTRY
2042
EAD0 2043 K33: ; NO-ALT-KEYPAD
EAD0 C066190000 2044 MOV ALT_INPUT,0 ; ZERO ANY PREVIOUS ENTRY INTO INPUT
EAD5 B91A00 2045 MOV CX,26 ; DI,E,ALREADY POINTING
EAD8 F2 2046 REPNE SCASB ; LOOK FOR MATCH IN ALPHABET
EADA 7505 2047 JNE K34 ; NOT FOUND, FUNCTION KEY OR OTHER
EADC B000 2048 MOV AL,0 ; ASCII CODE OF ZERO
EADE E9F400 2049 JMP K57 ; PUT IT IN THE BUFFER
2050
2051 ;----- LOOK FOR TOP ROW OF ALTERNATE SHIFT
2052
EAE1 2053 K34: ; ALT-TOP-ROW
EAE1 3C02 2054 CMP AL,2 ; KEY WITH '!' ON IT
EAE3 720C 2055 JB K35 ; NOT ONE OF INTERESTING KEYS
EAE5 3C0E 2056 CMP AL,14 ; IS IT IN THE REGION
EAE7 7308 2057 JAE K35 ; ALT-FUNCTION
EAE9 80C476 2058 ADD AH,118 ; CONVERT PSEUDO SCAN CODE TO RANGE
EAE C B000 2059 MOV AL,0 ; INDICATE AS SUCH
EAE E E9E400 2060 JMP K57 ; BUFFER_FILL
2061
2062 ;----- TRANSLATE ALTERNATE SHIFT PSEUDO SCAN CODES
2063
EAF1 2064 K35: ; ALT-FUNCTION
EAF1 3C3B 2065 CMP AL,59 ; TEST FOR IN TABLE
EAF3 7303 2066 JAE K37 ; ALT-CONTINUE
EAF5 2067 K36: JMP K26 ; CLOSE-RETURN
EAF5 E961FF 2068 ; IGNORE THE KEY
EAF8 2069 K37: ; ALT-CONTINUE
EAF8 3C47 2070 CMP AL,71 ; IN KEYPAD REGION
EAF A 73F9 2071 JAE K36 ; IF SO, IGNORE
EAF C B85FE9 2072 MOV BX,OFFSET K13 ; ALT_SHIFT PSEUDO SCAN TABLE
EAF F E91B01 2073 JMP K63 ; TRANSLATE THAT
2074
2075 ;----- NOT IN ALTERNATE SHIFT
2076
EB02 2077 K38: ; NOT-ALT-SHIFT
EB02 F606170004 2078 TEST KB_FLAG,CTL_SHIFT ; ARE WE IN CONTROL SHIFT
EB07 7458 2079 JZ K44 ; NOT-CTL-SHIFT
2080
2081 ;----- CONTROL SHIFT, TEST SPECIAL CHARACTERS
2082 ;----- TEST FOR BREAK AND PAUSE KEYS
2083
EB09 3C46 2084 CMP AL,SCROLL_KEY ; TEST FOR BREAK
EB0B 7518 2085 JNE K39 ; NO-BREAK
EB0D 8B1EB000 2086 MOV BX,BUFFER_START ; RESET BUFFER TO EMPTY
EB11 891E1A07 2087 MOV BUFFER_HEAD,BX
EB15 891E1C00 2088 MOV BUFFER_TAIL,BX
EB19 C066710080 2089 MOV BIOS_BREAK,80H ; TURN ON BIOS BREAK BIT
EB1E CD1B 2090 INT 1BH ; BREAK INTERRUPT VECTOR
EB20 2B00 2091 SUB AX,AX ; PUT OUT DUMMY CHARACTER
EB22 E9B000 2092 JMP K57 ; BUFFER_FILL
EB25 2093 K39: ; NO-BREAK
EB25 3C45 2094 CMP AL,NUM_KEY ; LOOK FOR PAUSE KEY
EB27 7521 2095 JNE K41 ; NO-PAUSE
EB29 800E180080 2096 OR KB_FLAG_1,HOLD_STATE ; TURN ON THE HOLD FLAG
EB2E B020 2097 MOV AL,EO1 ; END OF INTERRUPT TO CONTROL PORT
EB30 E620 2098 OUT 020H,AL ; ALLOW FURTHER KEYSTROKE INTS
2099
2100 ;----- DURING PAUSE INTERVAL, TURN CRT BACK ON
2101
EB32 803E490007 2102 CMP CRT_MODE,7 ; IS THIS BLACK AND WHITE CARD
EB37 7407 2103 JE K40 ; YES, NOTHING TO DO
EB39 BAD803 2104 MOV DX,03D8H ; PORT FOR COLOR CARD
EB3C A06500 2105 MOV AL,CRT_MODE_SET ; GET THE VALUE OF THE CURRENT MODE
EB3F EE 2106 OUT DX,AL ; SET THE CRT MODE, SO THAT CRT IS ON

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LOC OBJECT          LINE SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82
EB40                2107 K40:                : PAUSE-LOOP
EB40 F06E180008     2108                :
EB45 75F9           2109                : TEST KB_FLAG_1,HOLD_STATE
EB47 E914FF         2110                : JNZ K40                : LOOP UNTIL FLAG TURNED OFF
EB4A                2111                : JMP K27                : INTERRUPT_RETURN_NO_E01
EB4A                2112                : K41:                  : NO-PAUSE
EB4A                2113                :
EB4A 3C37           2114                :
EB4C 7506           2115                :
EB4E B80072         2116                : TEST SPECIAL CASE KEY 55
EB51 E98100         2117                :
EB51                2118                :
EB51                2119                :
EB54                2120                :
EB54 BB8EE8         2121                :
EB57 3C3B           2122                :
EB59 7276           2123                :
EB59                2124                :
EB5B B8C8E8         2125                :
EB5E E9BC00         2126                :
EB5E                2127                :
EB5E                2128                :
EB5E                2129                :
EB5E                2130                :
EB5E                2131                :
EB5E                2132                :
EB5E                2133                :
EB61                2134                :
EB61 3C47           2135                :
EB63 732C           2136                :
EB65 F06E170003     2137                :
EB6A 745A           2138                :
EB6A                2139                :
EB6A                2140                :
EB6C 3C0F           2141                :
EB6E 7505           2142                :
EB70 B8000F         2143                :
EB73 EB60           2144                :
EB75                2145                :
EB75 3C37           2146                :
EB77 7509           2147                :
EB77                2148                :
EB77                2149                :
EB79 B020           2150                :
EB7B E620           2151                :
EB7D CD05           2152                :
EB7F E9DCFE         2153                :
EB82                2154                :
EB82 3C3B           2155                :
EB84 7206           2156                :
EB86 B85E99         2157                :
EB89 E99100         2158                :
EB8C                2159                :
EB8C B81BE9         2160                :
EB8F EB40           2161                :
EB8F                2162                :
EB8F                2163                :
EB8F                2164                :
EB8F                2165                :
EB8F                2166                :
EB91                2167                :
EB91 F06E170020     2168                :
EB96 7520           2169                :
EB97 F06E170003     2170                :
EB9D 7520           2171                :
EB9D                2172                :
EB9F                2173                :
EB9F 3C4A           2174                :
EBA1 740B           2175                :
EBA3 3C4E           2176                :
EBA5 740C           2177                :
EBA7 2C47           2178                :
EBA9 B876E9         2179                :
EBAE EB71           2180                :
EBAE                2181                :
EBAE B82D4A         2182                :
EBB1 EB22           2183                :
EBB3                2184                :
EBB3 B82B4E         2185                :
EBB6 EB1D           2186                :
EBB6                2187                :
EBB6                2188                :
EBB6                2189                :
EBB6                2190                :
EBB8                2191                :
EBB8 F06E170003     2192                :
EBBD 750E           2193                :
EBBF                2194                :
EBBF 2C46           2195                :
EBC1 B869E9         2196                :
EBC4 EB0B           2197                :
EBC4                2198                :
EBC4                2199                :
EBC4                2200                :
EBC6                2201                :
EBC6 3C3B           2202                :
EBC8 7204           2203                :
EBCA B000           2204                :
EBCD EB07           2205                :
EBCD                2206                :
EBCD BBE1E8         2207                :
EBCD                2208                :
EBCD                2209                :
EBCD                2210                :
EBCD                2211                :
EBCD                2212                :
EBCD                2213                :
EBCD                2214                :
EBCD                2215                :
EBCD                2216                :
EBCD                2217                :
EBCD                2218                :
EBCD                2219                :
EBCD                2220                :
EBCD                2221                :
EBCD                2222                :
EBD5                2217                :
EBD5 3CFF           2218                :
EBD7 741F           2219                :
EBD9 80FCFF         2220                :
EBDC 741A           2221                :
EBDC                2222                :
K40:                :
K41:                :
K42:                :
K43:                :
K44:                :
K45:                :
K47:                :
K48:                :
K49:                :
K50:                :
K51:                :
K52:                :
K53:                :
K54:                :
K55:                :
K56:                :
K57:                :

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LOC OBJECT          LINE SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82

2223 ;----- HANDLE THE CAPS LOCK PROBLEM
2224
EBDE F606170040    2225 K58:          ; BUFFER-FILL-NOTEST
2226             TEST   KB_FLAG,CAPS_STATE ; ARE WE IN CAPS LOCK STATE
EBE3 7420          2227             JZ     K6T      ; SKIP IF NOT
2228
2229 ;----- IN CAPS LOCK STATE
2230
EBE5 F606170003    2231             TEST   KB_FLAG,LEFT_SHIFT+RIGHT_SHIFT ; TEST FOR SHIFT STATE
EBEA 740F          2232             JZ     K60      ; IF NOT SHIFT, CONVERT LOWER TO UPPER
2233
2234 ;----- CONVERT ANY UPPER CASE TO LOWER CASE
2235
EBEC 3C41          2236             CMP    AL,'A'      ; FIND OUT IF ALPHABETIC
EBEE 7215          2237             JB     K61          ; NOT_CAPS_STATE
EBF0 3C5A          2238             CMP    AL,'Z'      ;
EBF2 7711          2239             JA     K61          ; NOT_CAPS_STATE
EBF4 0420          2240             ADD    AL,'a'-'A'   ; CONVERT TO LOWER CASE
EBF6 EB0D          2241             JMP    SHORT K61    ; NOT_CAPS_STATE
EBF8 E95EFE          2242 K59:          ; NEAR-INTERRUPT-RETURN
2243             JMP    K26      ; INTERRUPT_RETURN
2244
2245 ;----- CONVERT ANY LOWER CASE TO UPPER CASE
2246
EBFB 3C61          2247 K60:          ; LOWER-TO-UPPER
EBFD 7205          2248             JB     K61          ; FIND OUT IF ALPHABETIC
EBFF 3C7A          2249             CMP    AL,'z'      ; NOT_CAPS_STATE
EC01 7702          2250             JA     K61          ; NOT_CAPS_STATE
EC03 2C20          2251             SUB    AL,'a'-'A'   ; CONVERT TO UPPER CASE
EC05 8B1C00        2252 K61:          ; NOT-CAPS-STATE
EC09 8BF3          2253             MOV    BX,BUFFER_TAIL ; GET THE END POINTER TO THE BUFFER
EC0B E863FC        2254             MOV    SI,BX        ; SAVE THE VALUE
EC0E 3B1E1A00      2255             CALL  K4            ; ADVANCE THE TAIL
EC12 7413          2256             CMP    BX,BUFFER_HEAD ; HAS THE BUFFER WRAPPED AROUND
EC14 8904          2257             JE     K62          ; BUFFER FULL_BEEP
EC16 891E1C00      2258             MOV    [SI],AX      ; STORE THE VALUE
EC1A E93CFE        2259             MOV    BUFFER_TAIL,BX ; MOVE THE POINTER UP
2260             JMP    K26      ; INTERRUPT_RETURN
2261
2262 ;----- TRANSLATE SCAN FOR PSEUDO SCAN CODES
2263
EC1D 2C38          2264 K63:          ; TRANSLATE-SCAN
EC1F 2ED7          2265             SUB    AL,59        ; CONVERT ORIGIN TO FUNCTION KEYS
EC21 8AE0          2266 K64:          ; TRANSLATE-SCAN-ORGD
EC23 B000          2267             XLAT  CS:K9        ; CTL TABLE SCAN
EC25 EBAE          2268             MOV    AH,AL        ; PUT VALUE INTO AH
2269             MOV    AL,0      ; ZERO ASCII CODE
2270             JMP    K57      ; PUT IT INTO THE BUFFER
2271
2272 KB_INT ENDP
2273
2274 ;----- BUFFER IS FULL, SOUND THE BEEPER
2275
EC27 2276          2277 K62:          ; BUFFER-FULL-BEEP
EC29 B020          2278             MOV    AL,E0I      ; END OF INTERRUPT COMMAND
EC2B B8B000        2279             OUT   20H,AL       ; SEND COMMAND TO INT CONTROL PORT
EC2E E461          2280             MOV    BX,080H     ; NUMBER OF CYCLES FOR 1/12 SECOND TONE
EC30 50           2281             IN    AL,KB_CTL    ; GET CONTROL INFORMATION
EC31 2282          2282             PUSH  AX           ; SAVE
EC33 24FC          2283 K65:          ; BEEP-CYCLE
EC35 B94800        2284             AND   AL,0FCH      ; TURN OFF TIMER GATE AND SPEAKER DATA
EC38 2285          2285             OUT   KB_CTL,AL    ; OUTPUT TO CONTROL
EC3A 0C02          2286             MOV    CX,48H      ; HALF CYCLE TIME FOR TONE
EC3C E661          2287 K66:          ; SPEAKER OFF
EC3E B94800        2288             OR    AL,2         ; TURN ON SPEAKER BIT
EC41 E2FE          2289             OUT   KB_CTL,AL    ; OUTPUT TO CONTROL
EC43 4B           2290             MOV    CX,48H      ; SET UP COUNT
EC44 75EB          2291 K67:          ; ANOTHER HALF CYCLE
EC46 58           2292             LOOP K67          ; TOTAL TIME COUNT
EC47 E661          2293             DEC   BX          ; DO ANOTHER CYCLE
EC49 E12FE        2294             JNZ  K65          ; RECOVER CONTROL
2295             OUT   KB_CTL,AL    ; OUTPUT THE CONTROL
2296             JMP    K27      ;
2297
EC4C 20333031      2300 F1  DB    ' 301',13,10 ; KEYBOARD ERROR
EC50 0D           2301
EC51 0A           2302
EC52 363031      2303 F3  DB    '601',13,10 ; DISKETTE ERROR
EC55 0D           2304
EC56 0A           2305

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2302 :-- INT 13 -----
2303 : DISKETTE I/O
2304 : THIS INTERFACE PROVIDES ACCESS TO THE 5 1/4 DISKETTE DRIVES
2305 : INPUT
2306 :
2307 : (AH)=0 RESET DISKETTE SYSTEM
2308 : HARD RESET TO NEC, PREPARE COMMAND, RECAL REQUIRED
2309 : ON ALL DRIVES
2310 : (AH)=1 READ THE STATUS OF THE SYSTEM INTO (AL)
2311 : DISKETTE_STATUS FROM LAST OPERATION IS USED
2312 :
2313 : REGISTERS FOR READ/WRITE/VERIFY/FORMAT
2314 : (DL) - DRIVE NUMBER (0-3 ALLOWED, VALUE CHECKED)
2315 : (HEAD) - HEAD NUMBER (0-1 ALLOWED, NOT VALUE CHECKED)
2316 : (CH) - TRACK NUMBER (0-39, NOT VALUE CHECKED)
2317 : (CL) - SECTOR NUMBER (1-8, NOT VALUE CHECKED,
2318 : NOT USED FOR FORMAT)
2319 : (AL) - NUMBER OF SECTORS ( MAX = 8, NOT VALUE CHECKED, NOT USED
2320 : FOR FORMAT)
2321 : (ES:BX) - ADDRESS OF BUFFER ( NOT REQUIRED FOR VERIFY)
2322 :
2323 :
2324 : (AH)=2 READ THE DESIRED SECTORS INTO MEMORY
2325 : (AH)=3 WRITE THE DESIRED SECTORS FROM MEMORY
2326 : (AH)=4 VERIFY THE DESIRED SECTORS
2327 : (AH)=5 FORMAT THE DESIRED TRACK
2328 : FOR THE FORMAT OPERATION, THE BUFFER POINTER (ES:BX)
2329 : MUST POINT TO THE COLLECTION OF DESIRED ADDRESS FIELDS
2330 : FOR THE TRACK. EACH FIELD IS COMPOSED OF 4 BYTES,
2331 : (C,H,R,N), WHERE C = TRACK NUMBER, H=HEAD NUMBER,
2332 : R = SECTOR NUMBER, N= NUMBER OF BYTES PER SECTOR
2333 : (00=128, 01=256, 02=512, 03=1024). THERE MUST BE ONE
2334 : ENTRY FOR EVERY SECTOR ON THE TRACK. THIS INFORMATION
2335 : IS USED TO FIND THE REQUESTED SECTOR DURING READ/WRITE
2336 : ACCESS.
2337 :
2338 : DATA VARIABLE -- DISK_POINTER
2339 : DOUBLE WORD POINTER TO THE CURRENT SET OF DISKETTE PARAMETERS
2340 : OUTPUT
2341 : AH = STATUS OF OPERATION
2342 : STATUS BITS ARE DEFINED IN THE EQUATES FOR
2343 : DISKETTE_STATUS VARIABLE IN THE DATA SEGMENT OF THIS
2344 : MODULE.
2345 :
2346 : CY = 0 SUCCESSFUL OPERATION (AH=0 ON RETURN)
2347 : CY = 1 FAILED OPERATION (AH HAS ERROR REASON)
2348 : FOR READ/WRITE/VERIFY
2349 : DS:BX, CH, CL PRESERVED
2350 : AL = NUMBER OF SECTORS ACTUALLY READ
2351 : ***** AL MAY NOT BE CORRECT IF TIME OUT ERROR OCCURS
2352 : NOTE: IF AN ERROR IS REPORTED BY THE DISKETTE CODE, THE
2353 : APPROPRIATE ACTION IS TO RESET THE DISKETTE, THEN RETRY
2354 : THE OPERATION. ON READ ACCESSES, NO MOTOR START DELAY
2355 : IS TAKEN, SO THAT THREE RETRIES ARE REQUIRED ON READS
2356 : TO ENSURE THAT THE PROBLEM IS NOT DUE TO MOTOR
2357 : START-UP.
-----
2357 : ASSUME CS:DATA,DS:DATA,ES:DATA
2358 : ORG DEC59H
2359 : DISKETTE_IO PROC FAR
EC69 53 STI ; INTERRUPTS BACK ON
EC6A 53 PUSH BX ; SAVE ADDRESS
EC6B 51 PUSH CX
EC6C 1E PUSH DS ; SAVE SEGMENT REGISTER VALUE
EC6D 56 PUSH SI ; SAVE ALL REGISTERS DURING OPERATION
EC6E 57 PUSH DI
EC6F 55 PUSH BP
EC60 52 PUSH DX
EC61 8BEC MOV BP,SP ; SET UP POINTER TO HEAD PARM
EC63 E8F30D CALL DDS ; CALL THE REST TO ENSURE DS RESTORED
EC66 E81C00 CALL J1 ; GET THE MOTOR WAIT PARAMETER
EC69 B80400 MOV BX,4
EC6C E8FD01 CALL GET_PARM
EC6F 8A264000 MOV MOTOR_COUNT,AH ; SET THE TIMER COUNT FOR THE MOTOR
EC73 8A264100 MOV AH,DISKETTE_STATUS ; GET STATUS OF OPERATION
EC77 80FC01 CMP AH,1 ; SET THE CARRY FLAG TO INDICATE
EC7A F5 CMC ; SUCCESS OR FAILURE
EC7B 5A POP DX ; RESTORE ALL REGISTERS
EC7C 5D POP BP
EC7D 5F POP DI
EC7E 5E POP SI
EC7F 1F POP DS
EC80 59 POP CX
EC81 5B POP BX ; RECOVER ADDRESS
EC82 CA0200 RET 2 ; THROW AWAY SAVED FLAGS
2385 : DISKETTE_IO ENDP
2386 :
2387 J1 PROC NEAR
2388 MOV DH,AL ; SAVE # SECTORS IN DH
2389 AND MOTOR_STATUS,07FH ; INDICATE A READ OPERATION
2390 OR AH,AH ; AH=0
2391 JZ DISK_RESET
2392 DEC AH ; AH=1
2393 JZ DISK_STATUS
2394 MOV DISKETTE_STATUS,0 ; RESET THE STATUS INDICATOR
2395 CMP DL,4 ; TEST FOR DRIVE IN 0-3 RANGE
2396 JAE J3 ; ERROR IF ABOVE
2397 DEC AH ; AH=2
2398 JZ DISK_READ
2399 DEC AH ; AH=3
2400 JNZ J2 ; TEST_DISK_VERF
2401 JMP DISK_WRITE ; TEST_DISK_VERF
2402 J2: JZ J2 ; TEST_DISK_VERF
2403 DEC AH ; AH=4
2404 JZ DISK_VERF
2405 DEC AH ; AH=5
2406 JZ DISK_FORMAT
2407 J3: MOV MOV ; BAD COMMAND
2408 MOV DISKETTE_STATUS,BAD_CMD ; ERROR CODE, NO SECTORS TRANSFERRED
2409 RET ; UNDEFINED OPERATION
2410 J1 ENDP
2411

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2412 ;----- RESET THE DISKETTE SYSTEM
2413
ECBT 2414 DISK_RESET PROC NEAR
ECBT 2415 MOV DX,03F2H ; ADAPTER CONTROL PORT
ECBA FA 2416 CLI ; NO INTERRUPTS
ECBB A03F00 2417 MOV AL,MOTOR_STATUS ; WHICH MOTOR IS ON
ECBE B104 2418 MOV CL,4 ; SHIFT COUNT
EC00 D2E0 2419 SAL AL,CL ; MOVE MOTOR VALUE TO HIGH NYBBLE
EC02 A820 2420 TEST AL,20H ; SELECT CORRESPONDING DRIVE
ECC4 750C 2421 JNZ J5 ; JUMP IF MOTOR ONE IS ON
ECC6 A840 2422 TEST AL,40H ;
ECC8 7506 2423 J4 ;
ECCA A880 2424 TEST AL,80H ; JUMP IF MOTOR TWO IS ON
ECCC 7406 2425 JZ J6 ;
ECCE FEC0 2426 INC AL ; JUMP IF MOTOR ZERO IS ON
EC00 2427 J4:
EC00 FEC0 2428 INC AL
EC02 FEC0 2429 J5: INC AL
EC04 2430 J6: INC AL
EC04 0C08 2432 OR AL,8 ; TURN ON INTERRUPT ENABLE
EC06 EE 2433 OUT DX,AL ; RESET THE ADAPTER
EC07 C063E0000 2434 CMP SEEK_STATUS,0 ; SET RECAL REQUIRED ON ALL DRIVES
ECDC C06410000 2435 MOV DISKETTE_STATUS,0 ; SET OK STATUS FOR DISKETTE
ECE1 0C04 2436 OR AL,4 ; TURN OFF RESET
ECE3 EE 2437 OUT DX,AL ; TURN OFF THE RESET
ECE4 FB 2438 STI ; REENABLE THE INTERRUPTS
ECE5 E82A02 2439 CALL CHK_STAT_2 ; DO SENSE INTERRUPT STATUS
2440 ; FOLLOWING RESET
ECE8 A04200 2441 MOV AL,NEC_STATUS ; IGNORE ERROR RETURN AND DO OWN TEST
EC0B 3CC0 2442 CMP AL,0C08H ; TEST FOR DRIVE READY TRANSITION
ECED 7406 2443 JZ J7 ; EVERYTHING OK
ECEF 800E410020 2444 OR DISKETTE_STATUS,BAD_NEC ; SET ERROR CODE
ECF4 C3 2445 RET
2446
2447 ;----- SEND SPECIFY COMMAND TO NEC
2448
ECF5 2449 J7: ; DRIVE READY
ECF5 B403 2450 MOV AH,03H ; SPECIFY COMMAND
ECF7 E84701 2451 CALL NEC_OUTPUT ; OUTPUT THE COMMAND
ECFA BB0100 2452 MOV BX,1 ; FIRST BYTE PARM IN BLOCK
ECFD E86C01 2453 CALL GET_PARM ; TO THE NEC CONTROLLER
ED00 BB0300 2454 MOV BX,3 ; SECOND BYTE PARM IN BLOCK
ED03 E86601 2455 CALL GET_PARM ; TO THE NEC CONTROLLER
ED06 2456 J8: ; RESET RET
ED06 C3 2457 RET ; RETURN TO CALLER
2458
2459 DISK_RESET ENDP
2460 ;----- DISKETTE STATUS ROUTINE
2461
ED07 2462 DISK_STATUS PROC NEAR
ED07 A04100 2463 MOV AL,DISKETTE_STATUS
ED0A C3 2464 RET
2465 DISK_STATUS ENDP
2466
2467 ;----- DISKETTE READ
2468
ED0B 2469 DISK_READ PROC NEAR
ED0B B046 2470 MOV AL,046H ; READ COMMAND FOR DMA
ED0D 2471 J9: ; DISK READ CNT
ED0D E8B801 2472 CALL DMA_SETUP ; SET UP THE DMA
ED10 B4E6 2473 MOV AH,0E6H ; SET UP RD COMMAND FOR NEC CONTROLLER
ED12 EB36 2474 JMP SHORT_RW_OPN ; GO DO THE OPERATION
2475 DISK_READ ENDP
2476
2477 ;----- DISKETTE VERIFY
2478
ED14 2479 DISK_VERF PROC NEAR
ED14 B042 2480 MOV AL,042H ; VERIFY COMMAND FOR DMA
ED16 EBF5 2481 JMP J9 ; DO AS IF DISK READ
2482 DISK_VERF ENDP
2483
2484 ;----- DISKETTE FORMAT
2485
ED18 2486 DISK_FORMAT PROC NEAR
ED18 800E3F0080 2487 OR MOTOR_STATUS,80H ; INDICATE WRITE OPERATION
ED1D B04A 2488 MOV AL,04AH ; WILL WRITE TO THE DISKETTE
ED1F E8A601 2489 CALL DMA_SETUP ; SET UP THE DMA
ED22 B44D 2490 MOV AH,04DH ; ESTABLISH THE FORMAT COMMAND
ED24 EB24 2491 JMP SHORT_RW_OPN ; DO THE OPERATION
ED26 2492 J10: ; CONTINUATION OF RW_OPN FOR FMT
ED26 BB0700 2493 MOV BX,7 ; GET THE
ED29 E84001 2494 CALL GET_PARM ; BYTES/SECTOR VALUE TO NEC
ED2C BB0900 2495 MOV BX,9 ; GET THE
ED2F E83A01 2496 CALL GET_PARM ; SECTORS/TRACK VALUE TO NEC
ED32 BB0F00 2497 MOV BX,75 ; GET THE
ED35 E83401 2498 CALL GET_PARM ; GAP LENGTH VALUE TO NEC
ED38 BB1100 2499 MOV BX,17 ; GET THE FILLER BYTE
ED3B E9A800 2500 JMP J16 ; TO THE CONTROLLER
2501 DISK_FORMAT ENDP
2502
2503 ;----- DISKETTE WRITE ROUTINE
2504
ED3E 2505 DISK_WRITE PROC NEAR
ED3E 800E3F0080 2506 OR MOTOR_STATUS,80H ; INDICATE WRITE OPERATION
ED43 B04A 2507 MOV AL,04AH ; DMA WRITE COMMAND
ED45 E88001 2508 CALL DMA_SETUP ; SET UP THE DMA
ED48 B4C5 2509 MOV AH,0C5H ; NEC COMMAND TO WRITE TO DISKETTE
2510 DISK_WRITE ENDP
2511
2512 ;----- ALLOW WRITE ROUTINE TO FALL INTO RW_OPN
2513

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2514 -----
2515 : RW_OPN
2516 : THIS ROUTINE PERFORMS THE READ/WRITE/VERIFY OPERATION :
2517 : -----
ED4A
ED4A 7308
ED4C C606410009
ED51 B000
ED53 C3
ED54
ED54 50
2525
2526 -----
2527 : TURN ON THE MOTOR AND SELECT THE DRIVE
2528 :
ED55 51
ED56 8ACA
ED58 B001
ED5A D2E0
ED5C FA
2532
ED5D C6064000FF
ED5E 84063F00
ED56 7531
ED68 80263F00F0
ED6D 08063F00
ED71 FB
ED72 B010
ED74 D2E0
ED76 0AC2
ED78 0C0C
ED7A 52
ED7B BAF203
ED7E 5E
ED7F 5A
2548
2549 -----
2550 : WAIT FOR MOTOR IF WRITE OPERATION
2551 :
ED80 F6063F0080
ED85 7412
ED87 B81400
ED8A E8DF00
ED8D 0AE4
ED8F
ED8F 7408
ED91 2BC9
ED93
ED93 E2FE
ED95 FECC
ED97 EBF6
ED99
ED99 FB
ED9A 59
2566
2567 -----
2568 : DO THE SEEK OPERATION
2569 :
ED9B E8DF00
ED9E 58
ED9F 8AFC
EDA1 B600
EDA3 724B
EDA5 BEF0ED90
EDA9 56
2576
2577 -----
2578 : SEND OUT THE PARAMETERS TO THE CONTROLLER
2579 :
EDAA E89400
EDAD 8A6601
EDB0 D0E4
EDB2 D0E4
EDB4 80E404
EDB7 0AE2
EDB9 E88500
2587
2588 -----
2589 : TEST FOR FORMAT COMMAND
2590 :
EDBC 80FF4D
EDBF 7503
EDC1 E962FF
EDC4
EDC4 8AE5
EDC6 E87800
EDC9 8A6601
EDCC E87200
EDCF 8AE1
EDD1 E86D00
EDD4 B80700
EDD7 E89200
EDDA B80900
EDDD E88C00
EDDE B80B00
EDD3 E88600
EDE6 B80D00
EDE9
EDE9 E88000
EDEE 5E
2610
2611 -----
2612 : LET THE OPERATION HAPPEN
2613 :
EDED E84301
EDF0
EDF0 7245
EDF2 E87401
EDF5 723F
2619
2620 -----
2621 : CHECK THE RESULTS RETURNED BY THE CONTROLLER
2622 :
EDF7 FC
EDF8 BE4200
EDFB AC
EDFC 24C0
EDFE 743B
EE00 3C40
EE02 7529
2629

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2630 ;----- ABNORMAL TERMINATION, FIND OUT WHY
2631
2632 L0DS NEC_STATUS ; GET ST1
2633 SAL AL,T ; TEST FOR EOT FOUND
2634 MOV AH,RECORD_NOT_FND ;
2635 J19 ; RW_FAIL
2636 SAL AL,1
2637 SAL AL,1 ; TEST FOR CRC ERROR
2638 MOV AH,BAD_CRC
2639 JC J19 ; RW_FAIL
2640 SAL AL,1 ; TEST FOR DMA OVERRUN
2641 MOV AH,BAD_DMA
2642 J19 ; RW_FAIL
2643 SAL AL,1
2644 SAL AL,1 ; TEST FOR RECORD NOT FOUND
2645 MOV AH,RECORD_NOT_FND
2646 JC J19 ; RW_FAIL
2647 SAL AL,1
2648 MOV AH,WRITE_PROTECT ; TEST FOR WRITE_PROTECT
2649 JC J19 ; RW_FAIL
2650 SAL AL,1 ; TEST MISSING ADDRESS MARK
2651 MOV AH,BAD_ADDR_MARK
2652 JC J19 ; RW_FAIL
2653
2654 ;----- NEC MUST HAVE FAILED
2655
2656 J18: MOV AH,BAD_NEC ; RW-NEC-FAIL
2657
2658 J19: OR DISKETTE_STATUS,AH ; RW-FAIL
2659 OR DISKETTE_STATUS,AH
2660 CALL NUM_TRANS ; HOW MANY WERE REALLY TRANSFERRED
2661 J20: RET ; RETURN TO CALLER
2662 RET ; RW_ERR_RES
2663 J21: CALL RESULTS ; FLUSH THE RESULTS BUFFER
2664 RET
2665
2666 ;----- OPERATION WAS SUCCESSFUL
2667
2668 J22: ; OPN OK
2669 MOV AH,0 ; HOW MANY GOT MOVED
2670 XOR AH,AH ; NO ERRORS
2671
2672 RET
2673 RW_OPN ENDP
2674
2675 ;-----
2676 ; NEC_OUTPUT
2677 ; THIS ROUTINE SENDS A BYTE TO THE NEC CONTROLLER AFTER TESTING
2678 ; FOR CORRECT DIRECTION AND CONTROLLER READY THIS ROUTINE WILL
2679 ; TIME OUT IF THE BYTE IS NOT ACCEPTED WITHIN A REASONABLE
2680 ; AMOUNT OF TIME, SETTING THE DISKETTE STATUS ON COMPLETION.
2681 ; INPUT (AH) BYTE TO BE OUTPUT
2682 ; OUTPUT
2683 ; CY = 0 SUCCESS
2684 ; CY = 1 FAILURE -- DISKETTE STATUS UPDATED
2685 ; IF A FAILURE HAS OCCURRED, THE RETURN IS MADE ONE LEVEL
2686 ; HIGHER THAN THE CALLER OF NEC_OUTPUT.
2687 ; THIS REMOVES THE REQUIREMENT OF TESTING AFTER EVERY
2688 ; CALL OF NEC_OUTPUT.
2689 ; (AL) DESTROYED
2690 ;-----
2691 NEC_OUTPUT PROC NEAR
2692 PUSH DX ; SAVE REGISTERS
2693 PUSH CX
2694 MOV DX,03F4H ; STATUS_PORT
2695 XOR CX,CX ; COUNT FOR TIME OUT
2696 J23:
2697 IN AL,DX ; GET STATUS
2698 TEST AL,040H ; TEST DIRECTION BIT
2699 JZ J25 ; DIRECTION OK
2700 LOOP J23 ; TIME_ERROR
2701 J24: OR DISKETTE_STATUS,TIME_OUT
2702 POP CX
2703 POP DX
2704 POP AX ; SET ERROR CODE AND RESTORE REGS
2705 POP AX ; DISCARD THE RETURN ADDRESS
2706 STC ; INDICATE ERROR TO CALLER
2707 RET
2708 J25:
2709 XOR CX,CX ; RESET THE COUNT
2710 J26:
2711 IN AL,DX ; GET THE STATUS
2712 TEST AL,080H ; IS IT READY
2713 JNZ J27 ; YES, GO OUTPUT
2714 LOOP J26 ; COUNT DOWN AND TRY AGAIN
2715 JMP J24 ; ERROR CONDITION
2716 J27:
2717 MOV AL,AH ; GET BYTE TO OUTPUT
2718 MOV DL,0F5H ; DATA PORT (3F5)
2719 OUT DX,AL ; OUTPUT THE BYTE
2720 POP CX ; RECOVER REGISTERS
2721 POP DX
2722 RET ; CY = 0 FROM TEST INSTRUCTION
2723 NEC_OUTPUT ENDP

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2724 : -----
2725 : GET_PARM :
2726 : THIS ROUTINE FETCHES THE INDEXED POINTER FROM THE DISK BASE :
2727 : BLOCK POINTED AT BY THE DATA VARIABLE DISK_POINTER. A BYTE FROM :
2728 : THAT TABLE IS THEN MOVED INTO AH, THE INDEX OF THAT BYTE BEING :
2729 : THE PARM IN BX :
2730 : ENTRY -- :
2731 : BX = INDEX OF BYTE TO BE FETCHED * 2 :
2732 : IF THE LOW BIT OF BX IS ON, THE BYTE IS IMMEDIATELY OUTPUT :
2733 : TO THE NEC CONTROLLER :
2734 : EXIT -- :
2735 : AH = THAT BYTE FROM BLOCK :
2736 : -----
EE6C GET_PARM PROC NEAR :
EE6C 1E 2738 PUSH DS ; SAVE SEGMENT
EE6D 28C0 2739 SUB AX,AX ; ZERO TO AX
EE6F 8ED8 2740 MOV DS,AX
2741 ASSUME DS:ABS0
EE71 C5367800 2742 LDS SI,DISK_POINTER ; POINT TO BLOCK
EE75 D1EB 2743 SHR BX,1 ; DIVIDE BX BY 2, AND SET FLAG
2744 ; FOR EXIT
EE77 8A20 2745 MOV AH,[SI+BX] ; GET THE WORD
EE79 1F 2746 POP DS ; RESTORE SEGMENT
EE7A 72C5 2747 ASSUME DS:DATA
EE7C C3 2748 JC NEC_OUTPUT ; IF FLAG SET, OUTPUT TO CONTROLLER
2749 RET ; RETURN TO CALLER
2750 GET_PARM ENDP
2751 : -----
2752 : SEEK :
2753 : THIS ROUTINE WILL MOVE THE HEAD ON THE NAMED DRIVE TO THE :
2754 : NAMED TRACK. IF THE DRIVE HAS NOT BEEN ACCESSED SINCE THE :
2755 : DRIVE RESET COMMAND WAS ISSUED, THE DRIVE WILL BE RECALIBRATED. :
2756 : INPUT :
2757 : (DL) = DRIVE TO SEEK ON :
2758 : (CH) = TRACK TO SEEK TO :
2759 : OUTPUT :
2760 : CY = 0 SUCCESS :
2761 : CY = 1 FAILURE -- DISKETTE_STATUS SET ACCORDINGLY :
2762 : (AX) DESTROYED :
2763 : -----
EETD 2764 PROC NEAR :
EETD B001 2765 MOV AL,1 ; ESTABLISH MASK FOR RECAL TEST
EET7 51 2766 PUSH AX ; SAVE INPUT VALUES
EE80 8ACA 2767 MOV CL,DL ; GET DRIVE VALUE INTO CL
EE82 D2C0 2768 ROL AL,CL ; SHIFT IT BY THE DRIVE VALUE
EE84 59 2769 POP CX ; RECOVER TRACK VALUE
EE85 84063E00 2770 TEST AL,SEEK_STATUS ; TEST FOR RECAL REQUIRED
EE89 7513 2771 JNZ J28 ; NO RECAL
EE8B 08063E00 2772 OR SEEK_STATUS,AL ; TURN ON THE NO RECAL BIT IN FLAG
EE8F 8407 2773 MOV AH,07H ; RECALIBRATE COMMAND
EE91 E8ADFF 2774 CALL NEC_OUTPUT
EE94 8AE2 2775 MOV AH,DL
EE96 E8A8FF 2776 CALL NEC_OUTPUT ; OUTPUT THE DRIVE NUMBER
EE99 E81600 2777 CALL CHK_STAT_2 ; GET THE INTERRUPT AND SENSE INT STATUS
EE9C 7229 2778 JC J32 ; SEEK_ERROR
2779
2780 ;----- DRIVE IS IN SYNCH WITH CONTROLLER, SEEK TO TRACK
2781
EE9E 2782 J28:
EE9E B40F 2783 MOV AH,0FH ; SEEK COMMAND TO NEC
EEAD E89EFF 2784 CALL NEC_OUTPUT
EEA3 8AE2 2785 MOV AH,DL ; DRIVE NUMBER
EEA5 E899FF 2786 CALL NEC_OUTPUT
EEA8 8AE5 2787 MOV AH,CH ; TRACK NUMBER
EEAA E894FF 2788 CALL NEC_OUTPUT
EEAD E86200 2789 CALL CHK_STAT_2 ; GET ENDING INTERRUPT AND
2790 ; SENSE STATUS
2791
2792 ;----- WAIT FOR HEAD SETTLE
2793
EEO0 9C 2794 PUSHF ; SAVE STATUS FLAGS
EEO1 BB1200 2795 MOV BX,18 ; GET HEAD SETTLE PARAMETER
EEO4 E895FF 2796 CALL GET_PARM
EEO7 51 2797 PUSH CX ; SAVE REGISTER
EEO8 2798 J29: ; HEAD SETTLE
EEOB B92602 2799 MOV CX,550 ; 1 MS LOOP
EEOB 0AE4 2800 OR AH,AH ; TEST FOR TIME EXPIRED
EEOB 7406 2801 JZ J31
EEOB E2FE 2802 J30: LOOP J30 ; DELAY FOR 1 MS
EEC1 FECC 2804 DEC AH ; DECREMENT THE COUNT
EEC3 EBF3 2805 JMP J29 ; DO IT SOME MORE
EEC5 2806 J31: ; RECOVER STATE
EEC5 59 2807 POP CX
EEC6 9D 2808 POPF
EEC7 2809 J32: ; SEEK_ERROR
EEC7 C3 2810 RET ; RETURN TO CALLER
2811 SEEK ENDP

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2812 ;-----;
2813 ; DMA_SETUP ;
2814 ; THIS ROUTINE SETS UP THE DMA FOR READ/WRITE/VERIFY OPERATIONS. ;
2815 ; INPUT ;
2816 ; (AL) = MODE BYTE FOR THE DMA ;
2817 ; (ES:BX) = ADDRESS TO READ/WRITE THE DATA ;
2818 ; OUTPUT ;
2819 ; (AX) DESTROYED ;
2820 ;-----;
EEC8 DMA_SETUP PROC NEAR
EEC8 51 PUSHPUSH CX ; SAVE THE REGISTER
EEC9 FA 82C3 CLI ; NO MORE INTERRUPTS
EECA E60C 2824 OUT DMA+12,AL ; SET THE FIRST/LAST F/F
EECC 50 2825 PUSHPUSH AX
EECD 58 2826 POP AX
EECE E60B 2827 OUT DMA+11,AL ; OUTPUT THE MODE BYTE
EEED 8CC0 2828 MOV AX,ES ; GET THE ES VALUE
EEED B104 2829 MOV CL,4 ; SHIFT COUNT
EEED 83C0 2830 ROL AX,CL ; ROTATE LEFT
EEED 8AE8 2831 MOV CH,AL ; GET HIGHEST NYBBLE OF ES TO CH
EEED 24F0 2832 AND AL,0F0H ; ZERO THE LOW NYBBLE FROM SEGMENT
EEED A3C3 2833 ADD AX,BX ; TEST FOR CARRY FROM ADDITION
EEED 7302 2834 JNC J33 ;
EEED FEC5 2835 INC CH ; CARRY MEANS HIGH 4 BITS MUST BE INC
EEED E00 2836 J33: PUSHPUSH AX ; SAVE START ADDRESS
EEED E604 2838 OUT DMA+4,AL ; OUTPUT LOW ADDRESS
EEED 8AC4 2839 MOV AL,AH ;
EEED E604 2840 OUT DMA+4,AL ; OUTPUT HIGH ADDRESS
EEED 8AC5 2841 MOV AL,CH ; GET HIGH 4 BITS
EEED 240F 2842 AND AL,0FH ;
EEED E681 2843 OUT 081H,AL ; OUTPUT THE HIGH 4 BITS TO
2844 ; THE PAGE REGISTER
2845 ;-----;
2846 ;----- DETERMINE COUNT
2847 ;
2848 MOV AH,DH ; NUMBER OF SECTORS
2849 SUB AL,AL ; TIMES 256 INTO AX
2850 SHR AX,1 ; SECTORS * 128 INTO AX
2851 PUSHPUSH AX
2852 MOV BX,6 ;
2853 CALL GET_PARM ; GET THE BYTES/SECTOR PARM
2854 MOV CL,AH ; USE A5 SHIFT COUNT (0=128, 1=256 ETC)
2855 POP AX ;
2856 SHL AX,CL ; MULTIPLY BY CORRECT AMOUNT
2857 DEC AX ; -1 FOR DMA VALUE
2858 PUSHPUSH AX ; SAVE COUNT VALUE
2859 OUT DMA+5,AL ; LOW BYTE OF COUNT
2860 MOV AL,AH ;
2861 OUT DMA+5,AL ; HIGH BYTE OF COUNT
2862 STI ; INTERRUPTS BACK ON
2863 POP CX ; RECOVER COUNT VALUE
2864 POP AX ; RECOVER ADDRESS VALUE
2865 ADD AX,CX ; ADD, TEST FOR 64K OVERFLOW
2866 POP CX ; RECOVER REGISTER
2867 MOV AL,2 ; MODE FOR 8237
2868 OUT DMA+10,AL ; INITIALIZE THE DISKETTE CHANNEL
2869 RET ; RETURN TO CALLER,
2870 ; CFL SET BY ABOVE IF ERROR
2871 DMA_SETUP ENDP
2872 ;-----;
2873 ; CHK_STAT_2 ;
2874 ; THIS ROUTINE HANDLES THE INTERRUPT RECEIVED AFTER A ;
2875 ; RECALIBRATE, SEEK, OR RESET TO THE ADAPTER. ;
2876 ; THE INTERRUPT IS WAITED FOR, THE INTERRUPT STATUS SENSED, ;
2877 ; AND THE RESULT RETURNED TO THE CALLER. ;
2878 ; INPUT ;
2879 ; NONE ;
2880 ; OUTPUT ;
2881 ; CY = 0 SUCCESS ;
2882 ; CY = 1 FAILURE -- ERROR IS IN DISKETTE_STATUS ;
2883 ; (AX) DESTROYED ;
2884 ;-----;
EF12 CHK_STAT_2 PROC NEAR
EF12 E81E00 CALL WAIT_INT ; WAIT FOR THE INTERRUPT
EF15 7214 JC J34 ; IF ERROR, RETURN IT
EF17 B408 MOV AH,08H ; SENSE INTERRUPT STATUS COMMAND
EF19 E825FF CALL NEC_OUTPUT ; READ IN THE RESULTS
EF1C E84A00 CALL RESULTS ; CHK2 RETURN
EF1F 720A JC J34 ; GET THE FIRST STATUS BYTE
EF21 A04200 MOV AL,NEC_STATUS ; ISOLATE THE BITS
EF24 2460 AND AL,060H ; TEST FOR CORRECT VALUE
EF26 3C60 CMP AL,060H ; IF ERROR, GO MARK IT
EF28 7402 JZ J35 ; GOOD RETURN
EF2A F8 CLC ;
EF2B J34: RET ; RETURN TO CALLER
EF2B C3 J35: RET ; CHK2_ERROR
EF2C 2899 ;
EF2C 800E410040 2900 OR DISKETTE_STATUS,BAD_SEEK ; ERROR RETURN CODE
EF31 F9 2901 STC ;
EF32 C3 2902 RET ;
2903 CHK_STAT_2 ENDP

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2904 :-----:
2905 : WAIT_INT :
2906 : THIS ROUTINE WAITS FOR AN INTERRUPT TO OCCUR. A TIME OUT :
2907 : ROUTINE TAKES PLACE DURING THE WAIT, SO THAT AN ERROR MAY BE :
2908 : RETURNED IF THE DRIVE IS NOT READY. :
2909 : INPUT :
2910 : NONE :
2911 : OUTPUT :
2912 : CY = 0 SUCCESS :
2913 : CY = 1 FAILURE -- DISKETTE_STATUS IS SET ACCORDINGLY :
2914 : (AX) DESTROYED :
2915 :-----:
EF33 FB 2916 WAIT_INT PROC NEAR :
EF33 FB 2917 STI : ; TURN ON INTERRUPTS, JUST IN CASE
EF34 53 2918 PUSH BX :
EF35 51 2919 PUSH CX : ; SAVE REGISTERS
EF36 B302 2920 MOV BL,2 : ; CLEAR THE COUNTERS
EF38 33C9 2921 XOR CX,CX : ; FOR 2 SECOND WAIT
EF3A 2922 J36:
EF3A F6063E0080 2923 TEST SEEK_STATUS,INT_FLAG ; TEST FOR INTERRUPT OCCURRING
EF3F 750C 2924 JNZ J36 ;
EF41 E2F7 2925 LOOP J36 ; COUNT DOWN WHILE WAITING
EF43 FECB 2926 DEC BL ; SECOND LEVEL COUNTER
EF45 75F3 2927 JNZ J36 ;
EF47 800E410080 2928 OR DISKETTE_STATUS,TIME_OUT ; NOTHING HAPPENED
EF4C F9 2929 STC ; ERROR RETURN
EF4D 2930 J37:
EF4D 9C 2931 PUSHF ; SAVE CURRENT CARRY
EF4E 80263E007F 2932 AND SEEK_STATUS,NOT_INT_FLAG ; TURN OFF INTERRUPT FLAG
EF53 9D 2933 POPF ; RECOVER CARRY
EF54 59 2934 POP CX ;
EF55 5B 2935 POP BX ; RECOVER REGISTERS
EF56 C3 2936 RET ; GOOD RETURN CODE COMES
2937 ; FROM TEST INST
2938 WAIT_INT ENDP
2939 :-----:
2940 : DISK_INT :
2941 : THIS ROUTINE HANDLES THE DISKETTE INTERRUPT :
2942 : INPUT :
2943 : NONE :
2944 : OUTPUT :
2945 : THE INTERRUPT FLAG IS SET IS SEEK_STATUS :
2946 :-----:
EF57 2947 ORG 0EF57H
EF57 FB 2948 DISK_INT PROC FAR ; RE ENABLE INTERRUPTS
EF58 IE 2949 STI :
EF59 50 2950 PUSH DS :
EF5A EBF0A 2951 CALL DDS :
EF5D 800E3E0080 2952 OR SEEK_STATUS,INT_FLAG ;
EF62 B020 2953 MOV AL,20H ; END OF INTERRUPT MARKER
EF64 E620 2954 OUT 20H,AL ; INTERRUPT CONTROL PORT
EF66 58 2955 POP AX :
EF67 1F 2956 POP DS ; RECOVER SYSTEM
EF68 CF 2957 IRET ; RETURN FROM INTERRUPT
2958 DISK_INT ENDP
2959 :-----:
2960 : RESULTS :
2961 : THIS ROUTINE WILL READ ANYTHING THAT THE NEC CONTROLLER HAS :
2962 : TO SAY FOLLOWING AN INTERRUPT. :
2963 : INPUT :
2964 : NONE :
2965 : OUTPUT :
2966 : CY = 0 SUCCESSFUL TRANSFER :
2967 : CY = 1 FAILURE -- TIME OUT IN WAITING FOR STATUS :
2968 : NEC_STATUS AREA HAS STATUS BYTE LOADED INTO IT :
2969 : (AH) DESTROYED :
2970 :-----:
EF69 2971 RESULTS PROC NEAR
EF69 FC 2972 CLD
EF6A BF4200 2973 MOV DI,OFFSET_NEC_STATUS ; POINTER TO DATA AREA
EF6D 51 2974 PUSH CX ; SAVE COUNTER
EF6E 52 2975 PUSH DX ;
EF6F 53 2976 PUSH BX ;
EF70 B307 2977 MOV BL,7 ; MAX STATUS BYTES
2978 ;
2979 :----- WAIT FOR REQUEST FOR MASTER
EF72 2980 J38:
EF72 33C9 2981 XOR CX,CX ; INPUT LOOP
EF74 BAF403 2982 MOV DX,03F4H ; COUNTER
EF77 EC 2983 J39:
EF77 EC 2984 MOV DX,03F4H ; STATUS PORT
EF78 A880 2985 IN AL,DX ; WAIT FOR MASTER
EF7A 750C 2986 TEST AL,080H ; GET STATUS
EF7C E2F7 2987 JNZ J40A ; MASTER READY
EF7E 800E410080 2988 LOOP J39 ; TEST DIR
EF83 2989 OR DISKETTE_STATUS,TIME_OUT ; WAIT_MASTER
EF83 F9 2990 J40:
EF84 5B 2991 STC ; RESULTS_ERROR
EF85 5A 2992 POP BX ; SET ERROR RETURN
EF86 59 2993 POP DX ;
EF87 C3 2994 POP CX ;
2995 RET ;
2996 :----- TEST THE DIRECTION BIT
EF88 3000 J40A:
EF88 EC 3001 IN AL,DX ; GET STATUS REG AGAIN
EF89 A840 3002 TEST AL,040H ; TEST DIRECTION BIT
EF8B 7507 3003 JNZ J42 ; OK TO READ STATUS
EF8D 3004 J41:
EF8D 800E410020 3005 OR DISKETTE_STATUS,BAD_NEC ; NEC_FAIL
EF92 EBEF 3006 JMP J40 ; RESULTS_ERROR
3007 ;
3008 :----- READ IN THE STATUS
3009 :
3010 J42:
3011 INC DX ; INPUT_STAT
3012 IN AL,DX ; POINT AT DATA PORT
3013 MOV [DI],AL ; GET THE DATA
3014 INC DI ; STORE THE BYTE
3015 MOV CX,10 ; INCREMENT THE POINTER
3016 LOOP J43 ; LOOP TO KILL TIME FOR NEC
3017 J43:
3018 DEC DX ;
3019 IN AL,DX ; POINT AT STATUS PORT
3020 TEST AL,010H ; GET STATUS
; TEST FOR NEC STILL BUSY

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LOC OBJECT          LINE SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82
EFA2 7406          3020          JZ      J44          ; RESULTS DONE
EFA4 FECB          3021          DEC     BL           ; DECREMENT THE STATUS COUNTER
EFA6 75CA          3022          JNZ     J38          ; GO BACK FOR MORE
EFA8 EBEB          3023          JMP     J41          ; CHIP HAS FAILED
3024
3025          ;----- RESULT OPERATION IS DONE
3026
EFAA              3027          J44:
EFAA 5B            3028          POP     BX
EFA4 5A            3029          POP     DX
EFA6 59            3030          POP     CX          ; RECOVER REGISTERS
EFA8 C3            3031          RET          ; GOOD RETURN CODE FROM TEST INST
3032
-----
3033          ; NUM_TRANS
3034          ; THIS ROUTINE CALCULATES THE NUMBER OF SECTORS THAT
3035          ; WERE ACTUALLY TRANSFERRED TO/FROM THE DISKETTE
3036          ; INPUT
3037          ; (CH) = CYLINDER OF OPERATION
3038          ; (CL) = START SECTOR OF OPERATION
3039          ; OUTPUT
3040          ; (AL) = NUMBER ACTUALLY TRANSFERRED
3041          ; NO OTHER REGISTERS MODIFIED
3042
-----
EFAE              3043          NUM_TRANS  PROC  NEAR
EFAE A04500        3044          MOV     AL,_NEC_STATUS+3          ; GET CYLINDER ENDED UP ON
EFA1 3AC5          3045          CMP     AL,_CH                  ; SAME AS WE STARTED
EFB3 A04700        3046          MOV     AL,_NEC_STATUS+5        ; GET ENDING SECTOR
EFB6 740A          3047          JZ      J45                      ; IF ON SAME CYL, THEN NO ADJUST
EFB8 BB0800        3048          MOV     BX,_8
EFBB E8AEFE        3049          CALL   GET_PARM                 ; GET EOT VALUE
EFBE 8AC4          3050          MOV     AL,_AH                  ; INTO AL
EFC0 FEC0          3051          INC     AL                      ; USE EOT+1 FOR CALCULATION
EFC2              3052          J45:  SUB     AL,_CL             ; SUBTRACT START FROM END
EFC2 2AC1          3053          RET
EFC4 C3            3054          RET
3055          NUM_TRANS  ENDP
3056          RESULTS ENDP
3057          ;-----
3058          ; DISK_BASE
3059          ; THIS IS THE SET OF PARAMETERS REQUIRED FOR DISKETTE OPERATION.
3060          ; THEY ARE POINTED AT BY THE DATA VARIABLE DISK_POINTER. TO
3061          ; MODIFY THE PARAMETERS, BUILD ANOTHER PARAMETER BLOCK AND POINT
3062          ; DISK_POINTER TO IT.
3063          ;-----
EFC7              3064          ORG     0EFC7H
EFC7              3065          DISK_BASE LABEL  BYTE
EFC7 CF            3066          DB     11001111B          ; SRT=C, HD UNLOAD=0F - 1ST SPECIFY BYTE
EFC8 02            3067          DB     2                      ; HD LOAD=1, MODE=DMA - 2ND SPECIFY BYTE
EFC9 25            3068          DB     MOTOR_WAIT           ; WAIT AFTER OPN TIL MOTOR OFF
EFCB 02            3069          DB     2                      ; 512 BYTES/SECTOR
EFCB 08            3070          DB     8                      ; EOT (LAST SECTOR ON TRACK)
EFC8 2A            3071          DB     02AH                  ; GAP LENGTH
EFC8 FF            3072          DB     0FFH                  ; DTL
EFC8 50            3073          DB     050H                  ; GAP LENGTH FOR FORMAT
EFCF F6            3074          DB     0F6H                  ; FILL BYTE FOR FORMAT
EFD0 19            3075          DB     25                    ; HEAD SETTLE TIME (MILLISECONDS)
EFD1 04            3076          DB     4                      ; MOTOR START TIME (1/8 SECONDS)
3077

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3078 :--- INT 17
3079 : PRINTER_IO
3080 : THIS ROUTINE PROVIDES COMMUNICATION WITH THE PRINTER
3081 : INPUT
3082 : (AH)=0 PRINT THE CHARACTER IN (AL)
3083 : ON RETURN, AH=1 IF CHARACTER COULD NOT BE PRINTED
3084 : (TIME OUT), OTHER BITS SET AS ON NORMAL STATUS CALL
3085 : (AH)=1 INITIALIZE THE PRINTER PORT
3086 : RETURNS WITH (AH) SET WITH PRINTER STATUS
3087 : (AH)=2 READ THE PRINTER STATUS INTO (AH)
3088 :
3089 :
3090 :
3091 :
3092 :
3093 :
3094 :
3095 :
3096 :
3097 : (DX) = PRINTER TO BE USED (0,1,2) CORRESPONDING TO ACTUAL
3098 : VALUES IN PRINTER_BASE AREA
3099 :
3100 : DATA AREA PRINTER_BASE CONTAINS THE BASE ADDRESS OF THE PRINTER
3101 : CARD(S) AVAILABLE (LOCATED AT BEGINNING OF DATA SEGMENT,
3102 : 408H ABSOLUTE, 3 WORDS)
3103 :
3104 : DATA AREA PRINT_TIM_OUT (BYTE) MAY BE CHANGED TO CAUSE DIFFERENT
3105 : TIME-OUT WAITS. DEFAULT=20
3106 :
3107 : REGISTERS AH IS MODIFIED
3108 : ALL OTHERS UNCHANGED
3109 :-----
3110 : ASSUME CS=CODE,DS=DATA
3111 : ORG 0EFD2H
3112 : PRINTER_IO PROC FAR
3113 : STI
3114 : PUSH DS ; INTERRUPTS BACK ON
3115 : PUSH DX ; SAVE SEGMENT
3116 : PUSH SI
3117 : PUSH CX
3118 : PUSH BX
3119 : CALL DDS
3120 : MOV SI,DX ; GET PRINTER PARM
3121 : MOV BL,PRINT_TIM_OUT[SI] ; LOAD TIME-OUT PARM
3122 : SHL SI,1 ; WORD OFFSET INTO TABLE
3123 : MOV DX,PRINTER_BASE[SI] ; GET BASE ADDRESS FOR PRINTER CARD
3124 : OR DX,DX ; TEST DX FOR ZERO,
3125 : ; INDICATING NO PRINTER
3126 : JZ B1 ; RETURN
3127 : OR AH,AH ; TEST FOR (AH)=0
3128 : JZ B2 ; PRINT AL
3129 : DEC AH ; TEST FOR (AH)=1
3130 : JZ B8 ; INIT_PRT
3131 : DEC AH ; TEST FOR (AH)=2
3132 : JZ B5 ; PRINTER STATUS
3133 : B1: ; RETURN
3134 : POP BX
3135 : POP CX
3136 : POP SI ; RECOVER REGISTERS
3137 : POP DX ; RECOVER REGISTERS
3138 : POP DS
3139 : IRET
3140 :
3141 : ;----- PRINT THE CHARACTER IN (AL)
3142 :
3143 : B2:
3144 : PUSH AX ; SAVE VALUE TO PRINT
3145 : OUT DX,AL ; OUTPUT CHAR TO PORT
3146 : INC DX ; POINT TO STATUS PORT
3147 : B3:
3148 : SUB CX,CX ; WAIT_BUSY
3149 : B3_1:
3150 : IN AL,DX ; GET STATUS
3151 : MOV AH,AL ; STATUS TO AH ALSO
3152 : TEST AL,80H ; IS THE PRINTER CURRENTLY BUSY
3153 : JNZ B4 ; OUT_STROBE
3154 : LOOP B3_1 ; TRY AGAIN
3155 : DEC BL ; DROP LOOP COUNT
3156 : JNZ B3 ; GO TILL TIMEOUT ENDS
3157 : OR AH,1 ; SET ERROR FLAG
3158 : AND AH,0F9H ; TURN OFF THE OTHER BITS
3159 : JMP SHORT B7 ; RETURN WITH ERROR FLAG SET
3160 : B4:
3161 : MOV AL,0DH ; SET THE STROBE HIGH
3162 : INC DX ; STROBE IS BIT 0 OF PORT C OF 8255
3163 : OUT DX,AL
3164 : MOV AL,0CH ; SET THE STROBE LOW
3165 : OUT DX,AL
3166 : POP AX ; RECOVER THE OUTPUT CHAR
3167 :
3168 : ;----- PRINTER STATUS
3169 :
3170 : B5:
3171 : PUSH AX ; SAVE AL REG
3172 : B6:
3173 : MOV DX,PRINTER_BASE[SI]
3174 : INC DX
3175 : IN AL,DX ; GET PRINTER STATUS
3176 : MOV AH,AL
3177 : AND AH,0F8H ; TURN OFF UNUSED BITS
3178 : ; STATUS SET
3179 : B7:
3180 : POP DX ; RECOVER AL REG
3181 : MOV AL,DL ; GET CHARACTER INTO AL
3182 : XOR AH,48H ; FLIP A COUPLE OF BITS
3183 : JMP B1 ; RETURN FROM ROUTINE

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LOC OBJECT

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3183
3184 ;----- INITIALIZE THE PRINTER PORT
3185
3186 B8:
3187     PUSH    AX           ; SAVE AL
3188     INC     DX           ; POINT TO OUTPUT PORT
3189     INC     DX
3190     MOV     AL,8         ; SET INIT LINE LOW
3191     OUT    DX,AL
3192     MOV    AX,1000
3193 B9:
3194     DEC    AX           ; INIT_LOOP
3195     JNZ   B9           ; LOOP FOR RESET TO TAKE
3196     MOV    AL,0CH      ; INIT_LOOP
3197     ; NO INTERRUPTS, NON AUTO LF,
3198     OUT    DX,AL      ; INIT HIGH
3199     JMP   B6
3200     PRINTER_IO      ; PRT_STATUS_1
3201     ENDP
3201
```

```

3202
3203
3204 : VIDEO ID
3205 : THESE ROUTINES PROVIDE THE CRT INTERFACE
3206 : THE FOLLOWING FUNCTIONS ARE PROVIDED:
3207 : (AH)=0 SET MODE (AL) CONTAINS MODE VALUE
3208 : (AL)=1 40X25 BW (POWER ON DEFAULT)
3209 : (AL)=2 80X25 BW
3210 : (AL)=3 80X25 COLOR
3211 : GRAPHICS MODES
3212 : (AL)=4 320X200 COLOR
3213 : (AL)=5 320X200 BW
3214 : (AL)=6 640X200 BW
3215 : CRT MODES
3216 : (AH)=7 80X25 BW CARD (USED INTERNAL TO VIDEO ONLY)
3217 : *** NOTE BW MODES OPERATE SAME AS COLOR MODES, BUT
3218 : COLOR BURST IS NOT ENABLED
3219 :
3220 : (AH)=1 SET CURSOR TYPE
3221 : (CH) = BITS 4-0 = START LINE FOR CURSOR
3222 : ** HARDWARE WILL ALWAYS CAUSE BLIN
3223 : ** SETTING BIT 5 OR 6 WILL CAUSE ERRATIC
3224 : BLINKING OR NO CURSOR AT ALL
3225 : (CL) = BITS 4-0 = END LINE FOR CURSOR
3226 :
3227 : (AH)=2 SET CURSOR POSITION
3228 : (BH) = PAGE NUMBER (MUST BE 0 FOR GRAPHICS MODES)
3229 : (AH)=3 READ CURSOR POSITION
3230 : (BH) = PAGE NUMBER (MUST BE 0 FOR GRAPHICS MODES)
3231 : ON EXIT (DH,DL) = ROW,COLUMN OF CURRENT CURSOR
3232 : (CH,CL) CURSOR MODE CURRENTLY SET
3233 :
3234 : (AH)=4 READ LIGHT PEN POSITION
3235 : ON EXIT:
3236 : (AH) = 0 -- LIGHT PEN SWITCH NOT DOWN/NOT TRIGGERED
3237 : (AH) = 1 -- VALID LIGHT PEN VALUE IN REGISTERS
3238 : (DH,DL) = ROW,COLUMN OF CHARACTER LP POSN
3239 : (CH) = RASTER LINE (0-199)
3240 : (BX) = PIXEL COLUMN (0-319,639)
3241 :
3242 : (AH)=5 SELECT ACTIVE DISPLAY PAGE (VALID ONLY FOR ALPHA MODES)
3243 : (AL)=NEW PAGE VAL (0-7 FOR MODES 041, 0-3 FOR MODES 2431)
3244 :
3245 : (AH)=6 SCROLL ACTIVE PAGE UP
3246 : (AL) = NUMBER OF LINES, INPUT LINES BLANKED AT BOTTOM
3247 : OF WINDOW
3248 : AL = 0 MEANS BLANK ENTIRE WINDOW
3249 : (CH,CL) = ROW,COLUMN OF UPPER LEFT CORNER OF SCROLL
3250 : (DH,DL) = ROW,COLUMN OF LOWER RIGHT CORNER OF SCROLL
3251 : (BH) = ATTRIBUTE TO BE USED ON BLANK LINE
3252 :
3253 : (AH)=7 SCROLL ACTIVE PAGE DOWN
3254 : (AL) = NUMBER OF LINES, INPUT LINES BLANKED AT TOP
3255 : OF WINDOW
3256 : AL = 0 MEANS BLANK ENTIRE WINDOW
3257 : (CH,CL) = ROW,COLUMN OF UPPER LEFT CORNER OF SCROLL
3258 : (DH,DL) = ROW,COLUMN OF LOWER RIGHT CORNER OF SCROLL
3259 : (BH) = ATTRIBUTE TO BE USED ON BLANK LINE
3260 :
3261 : CHARACTER HANDLING ROUTINES
3262 :
3263 : (AH) = 8 READ ATTRIBUTE/CHARACTER AT CURRENT CURSOR POSITION
3264 : (BH) = DISPLAY PAGE (VALID FOR ALPHA MODES ONLY)
3265 : ON EXIT:
3266 : (AL) = CHAR READ
3267 : (AH) = ATTRIBUTE OF CHARACTER READ (ALPHA MODES ONLY)
3268 :
3269 : (AH) = 9 WRITE ATTRIBUTE/CHARACTER AT CURRENT CURSOR POSITION
3270 : (BH) = DISPLAY PAGE (VALID FOR ALPHA MODES ONLY)
3271 : (CX) = COUNT OF CHARACTERS TO WRITE
3272 : (AL) = CHAR TO WRITE
3273 : (BL) = ATTRIBUTE OF CHARACTER (ALPHA)/COLOR OF CHAR
3274 : (GRAPHICS)
3275 : SEE NOTE ON WRITE DOT FOR BIT 7 OF BL = 1.
3276 : (AH)= 10 WRITE CHARACTER ONLY AT CURRENT CURSOR POSITION
3277 : (BH) = DISPLAY PAGE (VALID FOR ALPHA MODES ONLY)
3278 : (CX) = COUNT OF CHARACTERS TO WRITE
3279 : (AL) = CHAR TO WRITE
3280 :
3281 : FOR READ/WRITE CHARACTER INTERFACE WHILE IN GRAPHICS MODE, THE
3282 : CHARACTERS ARE FORMED FROM A CHARACTER GENERATOR IMAGE
3283 : MAINTAINED IN THE SYSTEM ROM. ONLY THE 128 128 CHARS
3284 : ARE CONTAINED THERE. TO READ/WRITE THE SECOND 128
3285 : CHARS, THE USER MUST INITIALIZE THE POINTER AT
3286 : INTERRUPT 1FH (LOCATION 0007CH) TO POINT TO THE 1K BYTE
3287 : TABLE CONTAINING THE CODE POINTS FOR THE SECOND
3288 : 128 CHARS (128-255).
3289 :
3290 : FOR WRITE CHARACTER INTERFACE IN GRAPHICS MODE, THE REPLICATION
3291 : FACTOR CONTAINED IN (CX) ON ENTRY WILL PRODUCE VALID
3292 : RESULTS ONLY FOR CHARACTERS CONTAINED ON THE SAME ROW.
3293 : CONTINUATION TO SUCCEEDING LINES WILL NOT PRODUCE
3294 : CORRECTLY.
3295 :
3296 : GRAPHICS INTERFACE
3297 : (AH) = 11 SET COLOR PALETTE
3298 : (BH) = PALETTE COLOR ID BEING SET (0-127)
3299 : (BL) = COLOR VALUE TO BE USED WITH THAT COLOR ID
3300 : NOTE: FOR THE CURRENT COLOR CARD, THIS ENTRY POINT
3301 : HAS MEANING ONLY FOR 320X200 GRAPHICS.
3302 : COLOR ID = 0 SELECTS THE BACKGROUND COLOR (0-15);
3303 : COLOR ID = 1 SELECTS THE PALETTE TO BE USED:
3304 : 0 = GREEN(1)/RED(2)/YELLOW(3)
3305 : 1 = CYAN(1)/MAGENTA(2)/WHITE(3)
3306 : IN 40X25 OR 80X25 ALPHA MODES, THE VALUE SET
3307 : FOR PALETTE COLOR 0 INDICATES THE
3308 : BORDER COLOR TO BE USED (VALUES 0-31,
3309 : WHERE 16-31 SELECT THE HIGH INTENSITY
3310 : BACKGROUND SET.
3311 :
3312 : (AH) = 12 WRITE DOT
3313 : (DX) = ROW NUMBER
3314 : (CX) = COLUMN NUMBER
3315 : (AL) = COLOR VALUE
3316 : IF BIT 7 OF AL = 1, THEN THE COLOR VALUE IS
3317 : EXCLUSIVE OR'D WITH THE CURRENT CONTENTS OF
3318 : THE DOT
3319 :
3320 : (AH) = 13 READ DOT
3321 : (DX) = ROW NUMBER
3322 : (CX) = COLUMN NUMBER
3323 : (AL) RETURNS THE DOT READ

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3314 ;
3315 ; ASCII TELETYPE ROUTINE FOR OUTPUT
3316 ;
3317 ; (AH) = 14 WRITE TELETYPE TO ACTIVE PAGE
3318 ; (AL) = CHAR TO WRITE
3319 ; (BL) = FOREGROUND COLOR IN GRAPHICS MODE
3320 ; NOTE -- SCREEN WIDTH IS CONTROLLED BY PREVIOUS MODE SET
3321 ;
3322 ; (AH) = 15 CURRENT VIDEO STATE
3323 ; RETURNS THE CURRENT VIDEO STATE
3324 ; (AL) = MODE CURRENTLY SET ( SEE AH=0 FOR EXPLANATION)
3325 ; (AH) = NUMBER OF CHARACTER COLUMNS ON SCREEN
3326 ; (BH) = CURRENT ACTIVE DISPLAY PAGE
3327 ;
3328 ; CS,SS,DS,ES,BX,CX,DX PRESERVED DURING CALL
3329 ; ALL OTHERS DESTROYED
3330 ;
-----
3331 ; ASSUME CS:CODE,DS:DATA,ES:VIDEO_RAM
3332 ;
F045 3332 MI 3332 ORG 0F045H
F045 F045 FCF0 3333 LABEL
F047 CDF1 3335 DW OFFSET SET_MODE ; TABLE OF ROUTINES WITHIN VIDEO I/O
F049 EEF1 3336 DW OFFSET SET_CTYPE
F04B 39F2 3337 DW OFFSET SET_CPOS
F04D 9CF7 3338 DW OFFSET READ_CURSOR
F04F 17F2 3339 DW OFFSET READ_LPEN
F051 96F2 3340 DW OFFSET ACT_DISP_PAGE
F053 38F3 3341 DW OFFSET SCROLL_UP
F055 74F3 3342 DW OFFSET SCROLL_DOWN
F057 B9F3 3343 DW OFFSET READ_AC_CURRENT
F059 ECF3 3344 DW OFFSET WRITE_AC_CURRENT
F05B 4EF2 3345 DW OFFSET SET_COLOR
F05D 2FF4 3346 DW OFFSET WRITE_DOT
F05F 1EF4 3347 DW OFFSET READ_DOT
F061 18F7 3348 DW OFFSET WRITE_TTY
F063 74F2 3349 DW OFFSET VIDEO_STATE
0020 3350 MIL EQU $-M1
3351 ;
F065 3352 ORG 0F065H
F065 3353 VIDEO_IO PROC NEAR
F065 FB 3354 STI ; INTERRUPTS BACK ON
F066 FC 3355 CLD ; SET DIRECTION FORWARD
F067 06 3356 PUSH ES
F068 1E 3357 PUSH DS ; SAVE SEGMENT REGISTERS
F069 52 3358 PUSH DX
F06A 51 3359 PUSH CX
F06B 53 3360 PUSH BX
F06C 56 3361 PUSH SI
F06D 57 3362 PUSH DI
F06E 50 3363 PUSH AX ; SAVE AX VALUE
F06F 8AC4 3364 MOV AL,AH ; GET INTO LOW BYTE
F071 32E4 3365 XOR AH,AH ; ZERO TO HIGH BYTE
F073 D1E0 3366 SAL AX,1 ; *2 FOR TABLE LOOKUP
F075 8BF0 3367 MOV SI,AX ; PUT INTO SI FOR BRANCH
F077 302000 3368 CMP AX,M1L ; TEST FOR WITHIN RANGE
F07A 7204 3369 JB M2 ; BRANCH AROUND BRANCH
F07C 58 3370 POP AX ; THROW AWAY THE PARAMETER
F07D E94501 3371 JMP VIDEO_RETURN ; DO NOTHING IF NOT IN RANGE
F080 3372 M2:
F080 E8D609 3373 CALL DDS
F083 B800B8 3374 MOV AX,0B800H ; SEGMENT FOR COLOR CARD
F086 8B3E1000 3375 MOV D1,EQUIP_FLAG ; GET EQUIPMENT SETTING
F08A 81E13000 3376 AND D1,30H ; ISOLATE CRT SWITCHES
F08E 83FF30 3377 CMP D1,30H ; IS SETTING FOR BW CARD?
F091 7502 3378 JNE M3
F093 B4B0 3379 MOV AH,0B0H ; SEGMENT FOR BW CARD
F095 3380 M3:
F095 8EC0 3381 MOV ES,AX ; SET UP TO POINT AT VIDEO RAM AREAS
F097 58 3382 POP AX ; RECOVER VALUE
F098 8A264900 3383 MOV AH,CRT_MODE ; GET CURRENT MODE INTO AH
F09C 2EFA445F0 3384 JMP WORD PTR CS:[SI+OFFSET M1]
3385 VIDEO_IO ENDP

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LOC OBJECT

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3386 ;-----
3387 ; SET_MODE ;
3388 ; THIS ROUTINE INITIALIZES THE ATTACHMENT TO ;
3389 ; THE SELECTED MODE. THE SCREEN IS BLANKED. ;
3390 ; INPUT (AL) = MODE SELECTED (RANGE 0-9) ;
3391 ; ;
3392 ; OUTPUT ;
3393 ; NONE ;
3394 ;-----
3395
3396 ;----- TABLES FOR USE IN SETTING OF MODE
3397
3398 ORG 0F0A4H
3399 VIDEO_PARS LABEL BYTE
3400 ;----- INIT_TABLE
3401 DB 38H,28H,2DH,0AH,1FH,6,19H ; SET UP FOR 40X25

FOA4 38
FOA5 28
FOA6 2D
FOA7 0A
FOA8 1F
FOA9 06
FOAA 19
FOAB 1C
FOAC 02
FOAD 07
FOAE 06
FOAF 07
FOB0 00
FOB1 00
FOB2 00
FOB3 00
0010

FOB4 71
FOB5 50
FOB6 5A
FOB7 0A
FOB8 1F
FOB9 06
FOBA 19
FOBB 1C
FOBC 02
3402 DB 1CH,2,7,6,7

FOBD 07
FOBE 06
FOBF 07
FOC0 00
FOC1 00
FOC2 00
FOC3 00
3403 DB 0,0,0,0

FOC4 38
FOC5 28
FOC6 2D
FOC7 0A
FOC8 7F
FOC9 06
FOCA 64
FOCB 70
FOCC 02
FOCD 01
FOCE 06
FOCF 07
FOD0 00
FOD1 00
FOD2 00
FOD3 00
3404 M4 EQU %-VIDEO_PARS
3405
3406 DB 71H,50H,5AH,0AH,1FH,6,19H ; SET UP FOR 80X25

FOD4 61
FOD5 50
FOD6 52
FOD7 0F
FOD8 19
FOD9 06
FODA 19
FODB 19
FODC 02
FODD 0D
FODE 0B
FODF 0C
FOE0 00
FOE1 00
FOE2 00
FOE3 00
3407 DB 1CH,2,7,6,7
3408 DB 0,0,0,0
3409
3410 DB 38H,28H,2DH,0AH,7FH,6,64H ; SET UP FOR GRAPHICS

FOE4 38
FOE4 0008
FOE4 0010
FOE8 0040
FOEA 0040
3411 DB 70H,2,1,6,7
3412 DB 0,0,0,0
3413
3414 DB 61H,50H,52H,0FH,19H,6,19H ; SET UP FOR 80X25 B&W CARD

FOEB 19
FOEC 28
FOED 28
FOEE 50
FOEF 50
FOF0 28
FOF1 28
FOF2 50
FOF3 50
3415 DB 19H,2,0DH,0BH,0CH
3416 DB 0,0,0,0
3417
3418 M5 LABEL WORD ; TABLE OF REGEN LENGTHS
3419 DW 2048 ; 40X25
3420 DW 4096 ; 80X25
3421 DW 16384 ; GRAPHICS
3422 DW 16384
3423
3424 ;----- COLUMNS
3425
3426 M6 LABEL BYTE
3427 DB 40,40,80,80,40,40,80,80

FOF4
FOF4 2C
FOF5 28
FOF6 2D
FOF7 29
FOF8 2A
FOF9 2E
FOFA 1E
FOFB 29
3428
3429 ;----- C_REG_TAB
3430
3431 M7 LABEL BYTE ; TABLE OF MODE SETS
3432 DB 2CH,28H,2DH,29H,2AH,2EH,1EH,29H

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3433
3434 SET_MODE PROC NEAR
3435 MOV DX,03D4H ; ADDRESS OF COLOR CARD
3436 BL 0 ; MODE SET FOR COLOR CARD
3437 CMP DI,30H ; IS BW CARD INSTALLED
3438 JNE M8 ; OK WITH COLOR
3439 MOV AL,7 ; INDICATE BW CARD MODE
3440 MOV DL,0B4H ; ADDRESS OF BW CARD (3B4)
3441 INC BL ; MODE SET FOR BW CARD
3442
3443 M8:
3444 MOV AH,AL ; SAVE MODE IN AH
3445 MOV CRT_MODE,AL ; SAVE IN GLOBAL VARIABLE
3446 MOV ADDR_6845,DX ; SAVE ADDRESS OF BASE
3447 PUSH DS ; SAVE POINTER TO DATA SEGMENT
3448 AX ; SAVE MODE
3449 PUSH DX ; SAVE OUTPUT PORT VALUE
3450 ADD DX,4 ; POINT TO CONTROL REGISTER
3451 MOV AL,BL ; GET MODE SET FOR CARD
3452 OUT DX,AL ; RESET VIDEO
3453 POP DX ; BACK TO BASE REGISTER
3454 SUB AX,AX ; SET UP FOR ABS0 SEGMENT
3455 MOV DS,AX ; ESTABLISH VECTOR TABLE ADDRESSING
3456 ASSUME DS:ABS0
3457 LDS BX,PARAM_PTR ; GET POINTER TO VIDEO PARMS
3458 POP AX ; RECOVER PARMS
3459 ASSUME DS:CODE
3460 MOV CX,M4 ; LENGTH OF EACH ROW OF TABLE
3461 CMP AH,2 ; DETERMINE WHICH ONE TO USE
3462 JC M9 ; MODE 15 0 OR 1
3463 ADD BX,CX ; MOVE TO NEXT ROW OF INIT TABLE
3464 CMP AH,4 ;
3465 JC M9 ; MODE 15 2 OR 3
3466 ADD BX,CX ; MOVE TO GRAPHICS ROW OF INIT_TABLE
3467 CMP AH,7 ;
3468 JC M9 ; MODE 15 4,5, OR 6
3469 ADD BX,CX ; MOVE TO BW CARD ROW OF INIT_TABLE
3470
3471 I----- BX POINTS TO CORRECT ROW OF INITIALIZATION TABLE
3472
3473 M9: ; OUT INIT
3474 PUSH AX ; SAVE MODE IN AH
3475 XOR AH,AH ; AH WILL SERVE AS REGISTER
3476 ; NUMBER DURING LOOP
3477
3478 I----- LOOP THROUGH TABLE, OUTPUTTING REG ADDRESS, THEN VALUE FROM TABLE
3479
3480 M10: ; INIT LOOP
3481 MOV AL,AH ; GET 6845 REGISTER NUMBER
3482 INC DX ; POINT TO DATA PORT
3483 INC AH ; NEXT REGISTER VALUE
3484 MOV AL,[BX] ; GET TABLE VALUE
3485 OUT DX,AL ; OUT TO CHIP
3486 INC BX ; NEXT IN TABLE
3487 DEC DX ; BACK TO POINTER REGISTER
3488 LOD M10 ; DO THE WHOLE TABLE
3489 POP AX ; GET MODE BACK
3490 POP DS ; RECOVER SEGMENT VALUE
3491 ASSUME DS:DATA
3492
3493 I----- FILL REGEN AREA WITH BLANK
3494
3495 XOR DI,DI ; SET UP POINTER FOR REGEN
3496 MOV CRT_START,DI ; START ADDRESS SAVED IN GLOBAL
3497 MOV ACTIVE_PAGE,0 ; SET PAGE VALUE
3498 MOV CX,8192 ; NUMBER OF WORDS IN COLOR CARD
3499 CMP AH,4 ; TEST FOR GRAPHICS
3500 JC M12 ; NO GRAPHICS INIT
3501 CMP AH,7 ; TEST FOR BW CARD
3502 JE M11 ; BW CARD INIT
3503 XOR AX,AX ; FILL FOR GRAPHICS MODE
3504 JMP SHORT M13 ; CLEAR BUFFER
3505 ; BW CARD INIT
3506 M11: MOV CH,08H ; BUFFER SIZE ON BW CARD
3507 ; NO GRAPHICS INIT
3508 M12: MOV AX,' '*7*256 ; FILL CHAR FOR ALPHA
3509 M13: ; CLEAR BUFFER
3510 REP STOSW ; FILL THE REGEN BUFFER WITH BLANKS
3511
3512 I----- ENABLE VIDEO AND CORRECT PORT SETTING
3513
3514 MOV CURSOR_MODE,607H ; SET CURRENT CURSOR MODE
3515 MOV AL,CRT_MODE ; GET THE MODE
3516 XOR AH,AH ; INTO AX REGISTER
3517 MOV SI,AX ; TABLE POINTER INDEXED BY MODE
3518 MOV DX,ADDR_6845 ; PREPARE TO OUTPUT TO
3519 ; VIDEO ENABLE PORT
3520 ADD DX,4
3521 MOV AL,CS:[SI+OFFSET M7] ; SET VIDEO ENABLE PORT
3522 OUT DX,AL ; SAVE THAT VALUE
3523 MOV CRT_MODE_SET,AL
3524
3525 I----- DETERMINE NUMBER OF COLUMNS, BOTH FOR ENTIRE DISPLAY
3526 I----- AND THE NUMBER TO BE USED FOR TTY INTERFACE
3527
3528 MOV AL,CS:[SI+OFFSET M6]
3529 XOR AH,AH
3530 MOV CRT_COLS,AX ; NUMBER OF COLUMNS IN THIS SCREEN
3531
3532 I----- SET CURSOR POSITIONS
3533
3534 AND SI,0EH ; WORD OFFSET INTO CLEAR LENGTH TABLE
3535 MOV CX,CS:[SI+OFFSET M5] ; LENGTH TO CLEAR
3536 CRT_LEN,CX ; SAVE LENGTH OF CRT -- NOT USED FOR BW
3537 MOV CX,8 ; CLEAR ALL CURSOR POSITIONS
3538 MOV DI,OFFSET CURSOR_POSN
3539 PUSH DS ; ESTABLISH SEGMENT
3540 ES ; ADDRESSING
3541 XOR AX,AX
3542 REP STOSW ; FILL WITH ZEROS

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3543
3544 ;----- SET UP OVERSCAN REGISTER
3545
F1B5 42             3546             INC     DX                ; SET OVERSCAN PORT TO A DEFAULT
F1B6 B030          3547             MOV     AL,30H           ; VALUE OF 30H FOR ALL MODES
3548             ; EXCEPT 640X200
3549             ; SEE IF THE MODE IS 640X200 BW
F1B8 803E490006   3549             CMP     CRT_MODE,6       ; IF IT ISNT 640X200, THEN GOTO REGULAR
F1B9 7502          3550             JNZ    M14              ; IF IT IS 640X200, THEN PUT IN 3FH
F1BF B03F          3551             MOV     AL,3FH
F1C1              3552             M14:
F1C1 EE           3553             OUT     DX,AL           ; OUTPUT THE CORRECT VALUE TO 3D9 PORT
F1C2 A26600       3554             MOV     CRT_PALETTE,AL  ; SAVE THE VALUE FOR FUTURE USE
3555
3556 ;----- NORMAL RETURN FROM ALL VIDEO RETURNS
3557
F1C5              3558             VIDEO_RETURN:
F1C5 5F           3559             POP     DI
F1C6 5E           3560             POP     SI
F1C7 5B           3561             POP     BX
3562             M15:
F1C8              3562             ; VIDEO_RETURN_C
F1C8 59           3563             POP     CX
F1C9 5A           3564             POP     DX
F1CA 1F           3565             POP     DS
F1CB 07           3566             POP     ES                ; RECOVER SEGMENTS
F1CC CF           3567             IRET                    ; ALL DONE
3568
3569 ;-----
3570 ; SET_CTYPE
3571 ; THIS ROUTINE SETS THE CURSOR VALUE
3572 ; INPUT
3573 ; (CX) HAS CURSOR VALUE CH-START LINE, CL-STOP LINE
3574 ; OUTPUT
3575 ; NONE
3576 ;-----
F1CD              3577             SET_CTYPE   PROC   NEAR
F1CD B40A         3578             MOV     AH,10            ; 6845 REGISTER FOR CURSOR SET
F1CE 890E6000    3579             MOV     CURSOR_MODE,CX  ; SAVE IN DATA AREA
F1D3 E80200      3580             CALL    M16              ; OUTPUT CX REG
F1D6 EBED        3581             JMP     VIDEO_RETURN
3582
3583 ;----- THIS ROUTINE OUTPUTS THE CX REGISTER TO THE 6845 REGS NAMED IN AH
3584
F1D8              3585             M16:
F1D8 8B166300    3586             MOV     DX,ADDR_6845    ; ADDRESS REGISTER
F1DE EA          3587             OUT     DX,AL           ; GET VALUE
F1DF 42          3588             INC     DX              ; REGISTER SET
F1E0 8AC5        3589             MOV     AL,CH           ; DATA REGISTER
F1E2 EE          3590             OUT     DX,AL           ; DATA
F1E3 4A          3591             DEC     DX
F1E4 8AC4        3592             MOV     AL,AH
F1E6 FEC0        3593             INC     AL              ; POINT TO OTHER DATA REGISTER
F1E8 EE          3594             OUT     DX,AL           ; SET FOR SECOND REGISTER
F1E9 42          3595             INC     DX
F1EA 8AC1        3596             MOV     AL,CL           ; SECOND DATA VALUE
F1EC EE          3597             OUT     DX,AL           ; ALL DONE
F1ED C3          3598             RET
3599
3600             SET_CTYPE   ENDP
3601 ;-----
3602 ; SET_CPOS
3603 ; THIS ROUTINE SETS THE CURRENT CURSOR
3604 ; POSITION TO THE NEW X-Y VALUES PASSED
3605 ; INPUT
3606 ; DX - ROW,COLUMN OF NEW CURSOR
3607 ; BH - DISPLAY PAGE OF CURSOR
3608 ; OUTPUT
3609 ; CURSOR IS SET AT 6845 IF DISPLAY PAGE
3610 ; IS CURRENT DISPLAY
3611 ;-----
F1EE 8ACF        3612             SET_CPOS   PROC   NEAR
F1EE 32ED        3613             MOV     CL,BH           ; GET VALUE
F1F0 32ED        3614             XOR     CH,CH           ; ESTABLISH LOOP COUNT
F1F2 D1E1        3615             SAL     CX,1           ; WORD OFFSET
F1F4 8BF1        3616             MOV     SI,CX           ; USE INDEX REGISTER
F1F6 895450      3617             MOV     [SI-OFFSET_CURSOR_POSN],DX ; SAVE THE POINTER
F1F9 3B3E6200    3618             CMP     ACTIVE_PAGE,BH ;
F1FD 7505        3619             JNZ    M17              ; SET_CPOS_RETURN
F1FF 8BC2        3620             MOV     AX,DX           ; GET_ROW/COLUMN TO AX
F201 E80200     3621             CALL    M18              ; CURSOR SET
F204              3622             M17:
F204 EBBF        3623             JMP     VIDEO_RETURN    ; SET_CPOS_RETURN
3624             SET_CPOS   ENDP
3625
3626 ;----- SET CURSOR POSITION, AX HAS ROW/COLUMN FOR CURSOR
3627
F206              3628             M18
F206 E87C00     3629             CALL    POSITION         ; DETERMINE LOCATION IN REGEN BUFFER
F209 8BC8        3630             MOV     CX,AX
F20B 030E4E00   3631             ADD     CX,CRT_START   ; ADD IN THE START ADDR FOR THIS PAGE
F20F D1F9        3632             SAR     CX,1           ; DIVIDE BY 2 FOR CHAR ONLY COUNT
F211 B40E        3633             MOV     AH,14          ; REGISTER NUMBER FOR CURSOR
F213 E8C2FF     3634             CALL    M16            ; OUTPUT THE VALUE TO THE 6845
F216 C3         3635             RET
3636             M18
3636             ENDP

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3637 -----
3638 : ACT_DISP_PAGE :
3639 : THIS ROUTINE SETS THE ACTIVE DISPLAY PAGE, ALLOWING THE :
3640 : FULL USE OF THE RAM SET ASIDE FOR THE VIDEO ATTACHMENT :
3641 : INPUT :
3642 : AL HAS THE NEW ACTIVE DISPLAY PAGE :
3643 : OUTPUT :
3644 : THE 6845 IS RESET TO DISPLAY THAT PAGE :
3645 -----
F217 3646 ACT_DISP_PAGE PROC NEAR
F218 3647 MOV ACTIVE_PAGE,AL ; SAVE ACTIVE PAGE VALUE
F21A 8B0E4C00 MOV CX,CRT_LEN ; GET SAVED LENGTH OF REGEN BUFFER
F21E 98 CBW ; CONVERT AL TO WORD
F21F 50 PUSH AX ; SAVE PAGE VALUE
F220 F7E1 MUL CX ; DISPLAY PAGE TIMES REGEN LENGTH
F222 A34E00 MOV CRT_START,AX ; SAVE START ADDRESS FOR
3653 ; LATER REQUIREMENTS
F225 8BC8 MOV CX,AX ; START ADDRESS TO CX
F227 D1F9 SAR CX,1 ; DIVIDE BY 2 FOR 6845 HANDLING
F229 B40C MOV AH,12 ; 6845 REGISTER FOR START ADDRESS
F22B E8AAFF CALL M16
F22E 5B POP BX ; RECOVER PAGE VALUE
F22F D1E3 SAL BX,1 ; *2 FOR WORD OFFSET
F231 8B4750 MOV AX,[BX + OFFSET CURSOR_POSN] ; GET CURSOR FOR THIS PAGE
F234 E8CFFF CALL M18 ; SET THE CURSOR POSITION
F237 EB8C JMP SHORT VIDEO_RETURN
3663 ACT_DISP_PAGE ENDP
3664 -----
3665 : READ_CURSOR :
3666 : THIS ROUTINE READS THE CURRENT CURSOR VALUE FROM THE :
3667 : 6845, FORMATS IT, AND SENDS IT BACK TO THE CALLER :
3668 : INPUT :
3669 : BH - PAGE OF CURSOR :
3670 : OUTPUT :
3671 : DX - ROW, COLUMN OF THE CURRENT CURSOR POSITION :
3672 : CX - CURRENT CURSOR MODE :
3673 -----
F239 3674 READ_CURSOR PROC NEAR
F239 8ADF 3675 MOV BL,BH
F23B 32FF 3676 XOR BH,BH
F23D D1E3 3677 SAL BX,1 ; WORD OFFSET
F23F 8B5750 3678 MOV DX,[BX+OFFSET CURSOR_POSN]
F242 8B0E6000 3679 MOV CX,CURSOR_MODE
F246 5F 3680 POP DI
F247 5E 3681 POP SI
F248 5B 3682 POP BX
F249 58 3683 POP AX ; DISCARD SAVED CX AND DX
F24A 58 3684 POP AX
F24B 1F 3685 POP DS
F24C 07 3686 POP ES
F24D CF 3687 IRET
3688 READ_CURSOR ENDP
3689 -----
3690 : SET_COLOR :
3691 : THIS ROUTINE WILL ESTABLISH THE BACKGROUND COLOR, THE OVERSCAN :
3692 : COLOR, AND THE FOREGROUND COLOR SET FOR MEDIUM RESOLUTION :
3693 : GRAPHICS :
3694 : INPUT :
3695 : (BH) HAS COLOR ID :
3696 : IF BH=0, THE BACKGROUND COLOR VALUE IS SET :
3697 : FROM THE LOW BITS OF BL (0-31) :
3698 : IF BH=1, THE PALETTE SELECTION IS MADE :
3699 : BASED ON THE LOW BIT OF BL :
3700 : 0=GREEN, RED, YELLOW FOR COLORS 1,2,3 :
3701 : 1=BLUE, CYAN, MAGENTA FOR COLORS 1,2,3 :
3702 : (BL) HAS THE COLOR VALUE TO BE USED :
3703 : OUTPUT :
3704 : THE COLOR SELECTION IS UPDATED :
3705 -----
F24E 3706 SET_COLOR PROC NEAR
F24E 8B166300 3707 MOV DX,ADDR_6845 ; I/O PORT FOR PALETTE
F252 83C205 3708 ADD DX,5 ; OVERSCAN PORT
F255 A06600 3709 MOV AL,CRT_PALETTE ; GET THE CURRENT PALETTE VALUE
F258 0AFF 3710 OR BH,BH ; IS THIS COLOR 0?
F25A 750E 3711 JNZ M20 ; OUTPUT COLOR I
3712 ;
3713 ;---- HANDLE COLOR 0 BY SETTING THE BACKGROUND COLOR
3714 ;
F25C 24E0 3715 AND AL,0E0H ; TURN OFF LOW 5 BITS OF CURRENT
F25E 80E31F 3716 AND BL,01FH ; TURN OFF HIGH 3 BITS OF INPUT VALUE
F261 0AC3 3717 OR AL,BL ; PUT VALUE INTO REGISTER
F263 3718 M19: ; OUTPUT THE PALETTE
F263 EE 3719 OUT DX,AL ; OUTPUT COLOR SELECTION TO 3D9 PORT
F264 A26600 3720 MOV CRT_PALETTE,AL ; SAVE THE COLOR VALUE
F267 E95BFF 3721 JMP SHORT VIDEO_RETURN
3722 ;
3723 ;---- HANDLE COLOR 1 BY SELECTING THE PALETTE TO BE USED
3724 ;
F26A 3725 M20:
F26A 24DF 3726 AND AL,0DFH ; TURN OFF PALETTE SELECT BIT
F26C D0EB 3727 SHR BL,1 ; TEST THE LOW ORDER BIT OF BL
F26E 73F3 3728 JNC M19 ; ALREADY DONE
F270 0C20 3729 OR AL,20H ; TURN ON PALETTE SELECT BIT
F272 EBFF 3730 JMP M19 ; GO DO IT
3731 SET_COLOR ENDP
3732 ;
3733 : VIDEO_STATE :
3734 : RETURNS THE CURRENT VIDEO STATE IN AX :
3735 : AH = NUMBER OF COLUMNS ON THE SCREEN :
3736 : AL = CURRENT VIDEO MODE :
3737 : BH = CURRENT ACTIVE PAGE :
3738 -----
F274 3739 VIDEO_STATE PROC NEAR
F274 8A264A00 3740 MOV AH,BYTE PTR CRT_COLS ; GET NUMBER OF COLUMNS
F278 A04900 3741 MOV AL,CRT_MODE ; CURRENT MODE
F27B 8A3E6200 3742 MOV BH,ACTIVE_PAGE ; GET CURRENT ACTIVE PAGE
F27F 5F 3743 POP DI ; RECOVER REGISTERS
F280 5E 3744 POP SI
F281 59 3745 POP CX ; DISCARD SAVED BX
F282 E943FF 3746 JMP M15 ; RETURN TO CALLER
3747 VIDEO_STATE ENDP

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SECTION 5

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3748 ;-----
3749 ; POSITION
3750 ; THIS SERVICE ROUTINE CALCULATES THE REGEN
3751 ; BUFFER ADDRESS OF A CHARACTER IN THE ALPHA MODE
3752 ; INPUT
3753 ; AX = ROW, COLUMN POSITION
3754 ; OUTPUT
3755 ; AX = OFFSET OF CHAR POSITION IN REGEN BUFFER
3756 ;-----
F285 POSITION PROC NEAR
F285 53 PUSH BX ; SAVE REGISTER
F286 8BD8 MOV BX,AX
F288 8AC4 MOV AL,AH ; ROWS TO AL
F28A F6264A00 MUL BYTE PTR CRT_COLS ; DETERMINE BYTES TO ROW
F28C 32FF XOR BH,BH
F290 09C3 ADD AX,BX ; ADD IN COLUMN VALUE
F292 D1E0 SAL AX,1 ; * 2 FOR ATTRIBUTE BYTES
F294 5B POP BX
F295 C3 RET
3767 POSITION ENDP
3768 ;-----
3769 ; SCROLL_UP
3770 ; THIS ROUTINE MOVES A BLOCK OF CHARACTERS UP
3771 ; ON THE SCREEN
3772 ; INPUT
3773 ; (AH) = CURRENT CRT MODE
3774 ; (AL) = NUMBER OF ROWS TO SCROLL
3775 ; (CX) = ROW/COLUMN OF UPPER LEFT CORNER
3776 ; (DX) = ROW/COLUMN OF LOWER RIGHT CORNER
3777 ; (BH) = ATTRIBUTE TO BE USED ON BLANKED LINE
3778 ; (DS) = DATA SEGMENT
3779 ; (ES) = REGEN BUFFER SEGMENT
3780 ; OUTPUT
3781 ; NONE -- THE REGEN BUFFER IS MODIFIED
3782 ;-----
3783 ASSUME CS:CODE,DS:DATA,ES:DATA
F296 SCROLL_UP PROC NEAR
F296 8AD8 MOV BL,AL ; SAVE LINE COUNT IN BL
F298 80FC04 CMP AH,4 ; TEST FOR GRAPHICS MODE
F29B 7208 JC N1,4 ; HANDLE SEPARATELY
F29D 80FC0F CMP AH,7 ; TEST FOR BW CARD
F2A0 7403 JNE N1
F2A2 E9F001 JMP GRAPHICS_UP
F2A5 ; UP CONTINUE
F2A5 53 PUSH BX ; SAVE FILL ATTRIBUTE IN BH
F2A6 8BC1 MOV AX,CX ; UPPER LEFT POSITION
F2A8 E83700 CALL SCROLL_POSITION ; DO SETUP FOR SCROLL
F2AB 7431 JZ N7 ; BLANK FIELD
F2AD 03F0 ADD SI,AX ; FROM ADDRESS
F2AF 8AE6 MOV AH,DH ; # ROWS IN BLOCK
F2B1 2AE3 SUB AH,BL ; # ROWS TO BE MOVED
F2B3 ; ROW LOOP
F2B3 E87200 N2: CALL N10 ; MOVE ONE ROW
F2B6 03F5 ADD SI,BP
F2B8 03FD ADD DI,BP ; POINT TO NEXT LINE IN BLOCK
F2BA FECC DEC AH ; COUNT OF LINES TO MOVE
F2BC 75F5 JNZ N2 ; ROW LOOP
F2BE ; CLEAR ENTRY
F2BE 58 POP AX ; RECOVER ATTRIBUTE IN AH
F2BF B020 MOV AL,' ' ; FILL WITH BLANKS
F2C1 E86D00 N4: CALL N11 ; CLEAR LOOP
F2C4 03FD ADD DI,BP ; CLEAR THE ROW
F2C6 FECD DEC BL ; POINT TO NEXT LINE
F2C8 75F7 JNZ N4 ; COUNTER OF LINES TO SCROLL
F2CA ; CLEAR LOOP
F2CA E88C07 N5: CALL DD5 ; SCROLL_END
F2CD 803E490007 CMP CRT_MODE,7 ; IS THIS THE BLACK AND WHITE CARD
F2D2 7407 JB N6 ; IF SO, SKIP THE MODE RESET
F2D4 A06500 MOV AL,CRT_MODE_SET ; GET THE VALUE OF THE MODE SET
F2D7 BAD803 MOV DX,03D8H ; ALWAYS SET COLOR CARD PORT
F2DA EE OUT DX,AL
F2DB ; VIDEO_RET HERE
F2DB E9E7FE N6: JMP VIDEO_RETURN ; VIDEO_RET HERE
F2DE ; BLANK FIELD
F2DE 8ADE MOV BL,DH ; GET ROW COUNT
F2E0 E8DC N7: JMP N3 ; GO CLEAR THAT AREA
3782 SCROLL_UP ENDP
3782 ;-----
3782 ;----- HANDLE COMMON SCROLL SET UP HERE
3782 SCROLL_POSITION PROC NEAR
F2E2 803E490002 N8: CMP CRT_MODE,2 ; TEST FOR SPECIAL CASE HERE
F2E7 7218 JB N9 ; HAVE TO HANDLE 80X25 SEPARATELY
F2E9 803E490003 CMP CRT_MODE,3
F2EE 7711 JA N9
3783 ;----- 80X25 COLOR CARD SCROLL
F2F0 52 3837 PUSH DX
F2F1 BADA03 3838 MOV DX,3DAH ; GUARANTEED TO BE COLOR CARD HERE
F2F4 50 3839 PUSH AX
F2F5 ; WAIT DISP_ENABLE
F2F5 EC 3841 IN AL,DX ; GET PORT
F2F6 A808 3842 TEST AL,8 ; WAIT FOR VERTICAL RETRACE
F2F8 74FB 3843 JB N8 ; WAIT DISP_ENABLE
F2FA B025 3844 MOV AL,25H
F2FC B028 3845 MOV DL,0D8H ; DX=3DB
F2FE EE 3846 OUT DX,AL ; TURN OFF VIDEO
F2FF 58 3847 POP AX ; DURING VERTICAL RETRACE
F300 5A 3848 POP DX
F301 ;
F301 E881FF N9: CALL POSITION
F304 03064E00 ADD AX,CRT_START ; CONVERT TO REGEN POINTER
F308 8BF8 3852 MOV DI,AX ; OFFSET OF ACTIVE PAGE
F30A 8BF0 3853 MOV SI,AX ; TO ADDRESS FOR SCROLL
F30C 2B01 3854 SUB DX,CX ; FROM ADDRESS FOR SCROLL
F30E FEC6 3855 INC DH ; DX = # ROWS, #COLS IN BLOCK
F310 FEC2 3856 INC DL ; INCREMENT FOR 0 ORIGIN
F312 32E0 3857 XOR CH,CH ; SET HIGH BYTE OF COUNT TO ZERO
F314 8B2E4A00 3858 BP,CRT_COLS ; GET NUMBER OF COLUMNS IN DISPLAY
F318 03E0 3859 ADD BP,BP ; TIMES 2 FOR ATTRIBUTE BYTE
F31A 8AC3 3860 MOV AL,BL ; GET LINE COUNT
F31C F6264A00 3861 MUL BYTE PTR CRT_COLS ; DETERMINE OFFSET TO FROM ADDRESS
F320 03C0 3862 AX,AX ; *2 FOR ATTRIBUTE BYTE
F322 06 3863 PUSH ES ; ESTABLISH ADDRESSING TO REGEN BUFFER

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LOC OBJECT          LINE SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82
F323 1F            3864          POP      DS          ; FOR BOTH POINTERS
F324 80FB00        3865          CMP      BL,0        ; 0 SCROLL MEANS BLANK FIELD
F327 C3            3866          RET                          ; RETURN WITH FLAGS SET
                    3867          SCROLL_POSITION ENDP
                    3868
                    3869          ;----- MOVE_ROW
                    3870
F328              3871          N10     PROC      NEAR
F328 8ACA          3872          MOV      CL,DL       ; GET # OF COLS TO MOVE
F32A 56            3873          PUSH    SI
F32E 57            3874          PUSH    DI           ; SAVE START ADDRESS
F32F F3            3875          REP     MOVSX        ; MOVE THAT LINE ON SCREEN
F32D A5
F32E 5F            3876          POP     DI
F32F 5E            3877          POP     SI           ; RECOVER ADDRESSES
F330 C3            3878          RET
                    3879          N10     ENDP
                    3880
                    3881          ;----- CLEAR_ROW
                    3882
F331              3883          N11     PROC      NEAR
F331 8ACA          3884          MOV      CL,DL       ; GET # COLUMNS TO CLEAR
F333 57            3885          PUSH    DI
F334 F3            3886          REP     STOSW        ; STORE THE FILL CHARACTER
F335 AB
F336 5F            3887          POP     DI
F337 C3            3888          RET
                    3889          N11     ENDP
                    3890          ;-----
                    3891          ; SCROLL_DOWN
                    3892          ; THIS ROUTINE MOVES THE CHARACTERS WITHIN A
                    3893          ; DEFINED BLOCK DOWN ON THE SCREEN, FILLING THE
                    3894          ; TOP LINES WITH A DEFINED CHARACTER
                    3895          ; INPUT
                    3896          ; (AH) = CURRENT CRT MODE
                    3897          ; (AL) = NUMBER OF LINES TO SCROLL
                    3898          ; (CX) = UPPER LEFT CORNER OF REGION
                    3899          ; (DX) = LOWER RIGHT CORNER OF REGION
                    3900          ; (BH) = FILL CHARACTER
                    3901          ; (DS) = DATA SEGMENT
                    3902          ; (ES) = REGEN SEGMENT
                    3903          ; OUTPUT
                    3904          ; NONE -- SCREEN IS SCROLLED
                    3905          ;-----
F338              3906          SCROLL_DOWN PROC NEAR
F338 FD            3907          STD
F339 8AD8          3908          MOV     BL,AL        ; DIRECTION FOR SCROLL DOWN
F33B 80FC04        3909          CMP     AH,4         ; LINE COUNT TO BL
F33C 7208          3910          JC     N12           ; TEST FOR GRAPHICS
F340 80FC07        3911          CMP     AH,7         ; TEST FOR BW CARD
F343 7403          3912          JE     N12
F345 E9A601        3913          JMP     GRAPHICS_DOWN
F348              3914          N12:
                    3915          PUSH   BX           ; CONTINUE DOWN
                    3916          MOV     AX,DX        ; SAVE ATTRIBUTE IN BH
                    3917          CALL  SCROLL_POSITION ; LOWER RIGHT CORNER
                    3918          JZ     N16         ; GET REGEN LOCATION
F350 2BF0          3919          SUB     SI,AX         ; SI IS FROM ADDRESS
F352 8AE6          3920          MOV     AH,DI        ; GET TOTAL # ROWS
F354 2AE3          3921          SUB     AH,BL        ; COUNT TO MOVE IN SCROLL
F356              3922          N13:
                    3923          CALL  N10           ; MOVE ONE ROW
F356 E8CFFF        3924          SUB     SI,BP
F359 2BF5          3925          SUB     DI,BP
F35D FECC          3926          DEC     AH
F35F 75F5          3927          JNZ    N13
F361              3928          N14:
                    3929          POP     AX           ; RECOVER ATTRIBUTE IN AH
F361 58            3930          MOV     AL,' '
F362 B020          3931          N15:
F364              3932          CALL  N11           ; CLEAR ONE ROW
F364 E8CAFF        3933          SUB     DI,BP        ; GO TO NEXT ROW
F367 2BFD          3934          DEC     BL
F369 FECD          3935          JNZ    N15
F36D E95AFF        3936          JMP     N5           ; SCROLL_END
F370              3937          N16:
F370 8ADE          3938          MOV     BL,DH
F372 EBED          3939          JMP     N14
                    3940          SCROLL_DOWN ENDP

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3941 |-----|
3942 | READ_AC_CURRENT |
3943 | THIS ROUTINE READS THE ATTRIBUTE AND CHARACTER |
3944 | AT THE CURRENT CURSOR POSITION AND RETURNS THEM |
3945 | TO THE CALLER |
3946 | INPUT |
3947 | (AH) = CURRENT CRT MODE |
3948 | (BH) = DISPLAY PAGE ( ALPHA MODES ONLY ) |
3949 | (DS) = DATA SEGMENT |
3950 | (ES) = REGEN SEGMENT |
3951 | OUTPUT |
3952 | (AL) = CHAR READ |
3953 | (AH) = ATTRIBUTE READ |
3954 |-----|
3955 | ASSUME CS:CODE,DS:DATA,ES:DATA |
3956 | READ_AC_CURRENT PROC NEAR |
3957 | CMP AH,4 | IS THIS GRAPHICS
F374 80FC04 |
3958 | JC P1 |
F377 7208 |
3959 | CMP AH,7 | IS THIS BW CARD
F379 80FC07 |
F37C 7403 |
3960 | JE P1 |
F37E E9A802 |
3961 | P1: |
3962 | CALL FIND_POSITION | READ_AC_CONTINUE
F381 E81A00 |
3963 | MOV SI,BX | ESTABLISH ADDRESSING IN SI
F384 8BF3 |
3964 |
3965 |
3966 |-----| WAIT FOR HORIZONTAL RETRACE
3967 |
3968 | MOV DX,ADDR_6845 | GET BASE ADDRESS
F386 8B166300 |
3969 | ADD DX,6 | POINT AT STATUS PORT
F38A 83C206 |
3970 | PUSH DS |
F38D 06 |
3971 | POP DS | GET SEGMENT FOR QUICK ACCESS
F38F 1F |
3972 | P2: |
3973 | IN AL,DX | WAIT FOR RETRACE LOW
F38F EC |
3974 | TEST AL,1 | GET STATUS
F390 A801 |
3975 | JNZ P2 | IS HORZ RETRACE LOW
F392 75FB |
3976 | CLI | WAIT UNTIL IT IS
F394 FA |
3977 | P3: | NO MORE INTERRUPTS
F395 |
3978 | IN AL,DX | WAIT FOR RETRACE HIGH
F395 EC |
3979 | TEST AL,1 | GET STATUS
F398 74FB |
3980 | JZ P3 | IS IT HIGH
F39A AD |
3981 | LODSW | WAIT UNTIL IT IS
F39B E927FE |
3982 | JMP VIDEO_RETURN | GET THE CHAR/ATTR
3983 | READ_AC_CURRENT ENDP
3984 |
3985 | FIND_POSITION PROC NEAR
F39E |
3986 | MOV CL,BH | DISPLAY PAGE TO CX
F39E 8ACF |
3987 | XOR CH,CH |
F3A0 32ED |
3988 | MOV SI,CX | MOVE TO SI FOR INDEX
F3A2 8BF1 |
3989 | SAL SI,1 | * 2 FOR WORD OFFSET
F3A4 D1E6 |
3990 | MOV AX,[SI+OFFSET CURSOR_POSN] | GET ROW/COLUMN OF THAT PAGE
F3A6 8B4450 |
3991 | XOR BX,BX | SET START ADDRESS TO ZERO
F3A9 33DB |
3992 | JCXZ P5 | NO PAGE
F3AB E306 |
3993 | P4: | PAGE_LOOP
F3AD |
3994 | ADD BX,CRT_LEN | LENGTH OF BUFFER
F3AD 031E4C00 |
3995 | LOOP P4 |
F3B1 E2FA |
3996 | P5: | NO PAGE
F3B3 |
3997 | CALL POSITION | DETERMINE LOCATION IN REGEN
F3B3 8BCFFE |
3998 | ADD BX,AX |
F3B6 03D8 |
3999 | RET | ADD TO START OF REGEN
F3B8 C3 |
4000 | FIND_POSITION ENDP
4001 |-----|
4002 | WRITE_AC_CURRENT PROC NEAR |
4003 | THIS ROUTINE WRITES THE ATTRIBUTE |
4004 | AND CHARACTER AT THE CURRENT CURSOR |
4005 | POSITION |
4006 | INPUT |
4007 | (AH) = CURRENT CRT MODE |
4008 | (BH) = DISPLAY PAGE |
4009 | (CX) = COUNT OF CHARACTERS TO WRITE |
4010 | (AL) = CHAR TO WRITE |
4011 | (BL) = ATTRIBUTE OF CHAR TO WRITE |
4012 | (DS) = DATA SEGMENT |
4013 | (ES) = REGEN SEGMENT |
4014 | OUTPUT |
4015 | NONE |
4016 |-----|
F3B9 80FC04 |
4017 | WRITE_AC_CURRENT PROC NEAR |
F3B9 80FC04 |
4018 | CMP AH,4 | IS THIS GRAPHICS
F3BC 7208 |
4019 | JC P6 |
F3BE 80FC07 |
4020 | CMP AH,7 | IS THIS BW CARD
F3C1 7403 |
4021 | JE P6 |
F3C3 E9B201 |
4022 | JMP GRAPHICS_WRITE |
F3C6 |
4023 | P6: | WRITE AC_CONTINUE
F3C6 8AE3 |
4024 | MOV AH,BL | GET ATTRIBUTE TO AH
F3C8 50 |
4025 | AX |
4026 | PUSH CX | SAVE ON STACK
F3C9 51 |
4027 | CALL FIND_POSITION | SAVE WRITE COUNT
F3CA E8D1FF |
4028 | MOV DI,BX | ADDRESS TO DI REGISTER
F3CD 8BF8 |
4029 | POP CX | WRITE COUNT
F3D0 5B |
4030 | POP BX | CHARACTER IN BX REG
F3D1 |
4031 | P7: | WRITE_LOOP
4032 |
4033 |-----| WAIT FOR HORIZONTAL RETRACE
4034 |
4035 | MOV DX,ADDR_6845 | GET BASE ADDRESS
F3D1 8B166300 |
4036 | ADD DX,6 | POINT AT STATUS PORT
F3D8 83C206 |
4037 | P8: |
4038 | IN AL,DX | GET STATUS
F3D8 EC |
4039 | TEST AL,1 | IS IT LOW
F3D9 A801 |
4040 | JNZ P8 | WAIT UNTIL IT IS
F3DB 75FB |
4041 | CLI | NO MORE INTERRUPTS
F3DD FA |
4042 | P9: |
F3DE |
4043 | IN AL,DX | GET STATUS
F3DE EC |
4044 | TEST AL,1 | IS IT HIGH
F3E1 74FB |
4045 | JZ P9 | WAIT UNTIL IT IS
F3E3 8BC3 |
4046 | MOV AX,BX | RECOVER THE CHAR/ATTR
F3E5 AB |
4047 | STOSW | PUT THE CHAR/ATTR
F3E6 FB |
4048 | ST | INTERRUPTS BACK ON
F3E7 E2E8 |
4049 | LOOP P7 | AS MANY TIMES AS REQUESTED
F3E9 E9D9FD |
4050 | JMP VIDEO_RETURN |
4051 | WRITE_AC_CURRENT ENDP

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4052 -----
4053 : WRITE_C_CURRENT
4054 : THIS ROUTINE WRITES THE CHARACTER AT
4055 : THE CURRENT CURSOR POSITION, ATTRIBUTE
4056 : UNCHANGED
4057 : INPUT
4058 : (AH) = CURRENT CRT MODE
4059 : (BH) = DISPLAY PAGE
4060 : (CX) = COUNT OF CHARACTERS TO WRITE
4061 : (AL) = CHAR TO WRITE
4062 : (DS) = DATA SEGMENT
4063 : (ES) = REGEN SEGMENT
4064 : OUTPUT
4065 : NONE
4066 -----
F3EC 4067 WRITE_C_CURRENT PROC NEAR
F3FA 51 4068 CMP AH,4 ; IS THIS GRAPHICS
F3EF 7208 4069 JC P10
F3F1 80FC07 4070 CMP AH,7 ; IS THIS BW CARD
F3F4 7403 4071 JE P10
F3F6 E97F01 4072 JMP GRAPHICS_WRITE
F3F9 4073 P10:
F3F9 50 4074 PUSH AX ; SAVE ON STACK
F3FA 51 4075 PUSH CX ; SAVE WRITE COUNT
F3FB E8A0FF 4076 CALL FIND_POSITION
F3FE 8BFB 4077 MOV DI,BX ; ADDRESS TO DI
F400 59 4078 POP CX ; WRITE COUNT
F401 5B 4079 POP BX ; BL HAS CHAR TO WRITE
F402 4080 P11: ; WRITE_LOOP
4081
4082 ;----- WAIT FOR HORIZONTAL RETRACE
4083
F402 8B166300 4084 MOV DX,ADDR_6845 ; GET BASE ADDRESS
F406 83C206 4085 ADD DX,6 ; POINT AT STATUS PORT
F409 4086 P12:
F409 EC 4087 IN AL,DX ; GET STATUS
F40A A801 4088 TEST AL,1 ; IS IT LOW
F40C 75FB 4089 JNZ P12 ; WAIT UNTIL IT IS
F40E FA 4090 CLF ; NO MORE INTERRUPTS
F40F 4091 P13:
F40F EC 4092 IN AL,DX ; GET STATUS
F410 A801 4093 TEST AL,1 ; IS IT HIGH
F412 74FB 4094 JZ P13 ; WAIT UNTIL IT IS
F414 8AC3 4095 MOV AL,BL ; RECOVER CHAR
F416 AA 4096 STOSB ; PUT THE CHAR/ATTR
F417 FB 4097 STI ; INTERRUPTS BACK ON
F418 47 4098 INC DI ; BUMP POINTER PAST ATTRIBUTE
F419 E2E7 4099 LOOP P11 ; AS MANY TIMES AS REQUESTED
F41B E9A7FD 4100 JMP VIDEO_RETURN
4101 WRITE_C_CURRENT ENDP
4102 -----
4103 : READ_DOT -- WRITE DOT
4104 : THESE ROUTINES WILL WRITE A DOT, OR READ THE DOT AT
4105 : THE INDICATED LOCATION
4106 : ENTRY
4107 : DX = ROW (0-199) (THE ACTUAL VALUE DEPENDS ON THE MODE)
4108 : CX = COLUMN ( 0-639) ( THE VALUES ARE NOT RANGE CHECKED)
4109 : AL = DOT VALUE TO WRITE (1,2 OR 4 BITS DEPENDING ON MODE,
4110 : REQ'D FOR WRITE DOT ONLY, RIGHT JUSTIFIED)
4111 : BIT 7 OF AL=1 INDICATES XOR THE VALUE INTO THE LOCATION
4112 : DS = DATA SEGMENT
4113 : ES = REGEN SEGMENT
4114 :
4115 : EXIT
4116 : AL = DOT VALUE READ, RIGHT JUSTIFIED, READ ONLY
4117 -----
4118 ASSUME CS:CODE,DS:DATA,ES:DATA
F41E 4119 READ_DOT PROC NEAR
F41E E83100 4120 CALL R3 ; DETERMINE BYTE POSITION OF DOT
F421 268A04 4121 MOV AL,ES:[SI] ; GET THE BYTE
F424 22C4 4122 AND AL,AH ; MASK OFF THE OTHER BITS IN THE BYTE
F426 D2E0 4123 SHL AL,CL ; LEFT JUSTIFY THE VALUE
F428 8ACE 4124 MOV CL,DH ; GET NUMBER OF BITS IN RESULT
F42A D2C0 4125 ROL AL,CL ; RIGHT JUSTIFY THE RESULT
F42C E996FD 4126 JMP VIDEO_RETURN ; RETURN FROM VIDEO IO
4127 READ_DOT ENDP
4128
F42F 4129 WRITE_DOT PROC NEAR
F42F 50 4130 PUSH AX ; SAVE DOT VALUE
F430 50 4131 PUSH AX ; TWICE
F431 E81E00 4132 CALL R3 ; DETERMINE BYTE POSITION OF THE DOT
F434 D2E8 4133 SHR AL,CL ; SHIFT TO SET UP THE BITS FOR OUTPUT
F436 22C4 4134 AND AL,AH ; STRIP OFF THE OTHER BITS
F438 268A0C 4135 MOV CL,ES:[SI] ; GET THE CURRENT BYTE
F43B 5B 4136 POP BX ; RECOVER XOR FLAG
F43C F6C380 4137 TEST BL,80H ; IS IT ON
F43F 750D 4138 JNZ R2 ; YES, XOR THE DOT
F441 F6D4 4139 NOT AH ; SET THE MASK TO REMOVE THE
F443 22CC 4140 AND CL,AH ; INDICATED BITS
F445 0AC1 4141 OR AL,CL ; OR IN THE NEW VALUE OF THOSE BITS
F447 4142 R1: ; FINISH DOT
F447 268804 4143 MOV ES:[SI],AL ; RESTORE THE BYTE IN MEMORY
F44A 58 4144 POP AX
F44B E977FD 4145 JMP VIDEO_RETURN ; RETURN FROM VIDEO IO
F44E 4146 R2: ; XOR DOT
F44E 32C1 4147 XOR AL,CL ; EXCLUSIVE OR THE DOTS
F450 EBF5 4148 JMP R1 ; FINISH UP THE WRITING
4149 WRITE_DOT ENDP

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LOC OBJECT

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4150 ;-----
4151 ; THIS SUBROUTINE DETERMINES THE REGEN BYTE LOCATION ;
4152 ; OF THE INDICATED ROW COLUMN VALUE IN GRAPHICS MODE. ;
4153 ; ENTRY -- ;
4154 ; DX = ROW VALUE (0-199) ;
4155 ; CX = COLUMN VALUE (0-639) ;
4156 ; EXIT -- ;
4157 ; SI = OFFSET INTO REGEN BUFFER FOR BYTE OF INTEREST ;
4158 ; AH = MASK TO STRIP OFF THE BITS OF INTEREST ;
4159 ; CL = BITS TO SHIFT TO RIGHT JUSTIFY THE MASK IN AH ;
4160 ; DH = # BITS IN RESULT ;
4161 ;-----
F452 4162 R3 PROC NEAR
F452 53 4163 PUSH BX ; SAVE BX DURING OPERATION
F453 50 4164 PUSH AX ; WILL SAVE AL DURING OPERATION
4165
4166 ;----- DETERMINE 1ST BYTE IN IDICATED ROW BY MULTIPLYING ROW VALUE BY 40
4167 ;----- ( LOW BIT OF ROW DETERMINES EVEN/ODD, 80 BYTES/ROW
4168
4169 MOV AL,40
4170 PUSH DX ; SAVE ROW VALUE
4171 AND DL,0FEH ; STRIP OFF ODD/EVEN BIT
4172 MUL DL ; AX HAS ADDRESS OF 1ST BYTE
4173 ; OF INDICATED ROW
4174 POP DX ; RECOVER IT
4175 JZ TEST DL,I ; TEST FOR EVEN/ODD
4176 JZ R4 ; JUMP IF EVEN ROW
4177 ADD AX,2000H ; OFFSET TO LOCATION OF ODD ROWS
4178 R4: ; EVEN ROW
4179 MOV SI,AX ; MOVE POINTER TO SI
4180 POP AX ; RECOVER AL VALUE
4181 MOV DX,CX ; COLUMN VALUE TO DX
4182
4183 ;----- DETERMINE GRAPHICS MODE CURRENTLY IN EFFECT
4184
4185 ;-----
4186 ; SET UP THE REGISTERS ACCORDING TO THE MODE ;
4187 ; CH = MASK FOR LOW OF COLUMN ADDRESS ( 1/3 FOR HIGH/MED RES) ;
4188 ; CL = # OF ADDRESS BITS IN COLUMN VALUE ( 3/2 FOR H/M) ;
4189 ; BL = MASK TO SELECT BITS FROM POINTED BYTE (80H/COH FOR H/M) ;
4190 ; BH = NUMBER OF VALID BITS IN POINTED BYTE ( 1/2 FOR H/M) ;
4191 ;-----
4192
F46A BBC002 4193 MOV BX,2COH
F46D B90203 4194 MOV CX,300H ; SET PARMS FOR MED RES
F470 803E490006 4195 CMP CRT_MODE,6
F475 7206 4196 JC R5 ; HANDLE IF MED ARES
F477 BB8001 4197 MOV BX,180H
F47A B90307 4198 MOV CX,103H ; SET PARMS FOR HIGH RES
4199
4200 ;----- DETERMINE BIT OFFSET IN BYTE FROM COLUMN MASK
4201
F47D 4202 R5:
F47D 22EA 4203 AND CH,DL ; ADDRESS OF PEL WITHIN BYTE TO CH
4204
4205 ;----- DETERMINE BYTE OFFSET FOR THIS LOCATION IN COLUMN
4206
F47F D3EA 4207 SHR DX,CL ; SHIFT BY CORRECT AMOUNT
F481 03F2 4208 ADD SI,DX ; INCREMENT THE POINTER
F483 8AF7 4209 MOV DH,BH ; GET THE # OF BITS IN RESULT TO DH
4210
4211 ;----- MULTIPLY BH (VALID BITS IN BYTE) BY CH (BIT OFFSET)
4212
4213 SUB CL,CL ; ZERO INTO STORAGE LOCATION
4214 R6:
4215 ROR AL,1 ; LEFT JUSTIFY THE VALUE
4216 ; IN AL (FOR WRITE)
4217 ADD CL,CH ; ADD IN THE BIT OFFSET VALUE
4218 DEC BH ; LOOP CONTROL
4219 JNZ R6 ; ON EXIT, CL HAS SHIFT COUNT
4220 ; TO RESTORE BITS
4221 MOV AH,BL ; GET MASK TO AH
4222 SHR AH,CL ; MOVE THE MASK TO CORRECT LOCATION
4223 POP BX ; RECOVER REG
4224 RET ; RETURN WITH EVERYTHING SET UP
4225 ENDP
R3
4226
4227 ;-----
4228 ; SCROLL UP THIS ROUTINE SCROLLS UP THE INFORMATION ON THE CRT ;
4229 ; ENTRY ;
4230 ; CH,CL = UPPER LEFT CORNER OF REGION TO SCROLL ;
4231 ; DH,DL = LOWER RIGHT CORNER OF REGION TO SCROLL ;
4232 ; BOTH OF THE ABOVE ARE IN CHARACTER POSITIONS ;
4233 ; BH = FILL VALUE FOR BLANKED LINES ;
4234 ; AL = # LINES TO SCROLL (AL=0 MEANS BLANK THE ENTIRE ;
4235 ; FIELD) ;
4236 ; DS = DATA SEGMENT ;
4237 ; ES = REGEN SEGMENT ;
4238 ; EXIT ;
4239 ; NOTHING, THE SCREEN IS SCROLLED ;
4240 ;-----
F495 4241 GRAPHICS_UP PROC NEAR
F495 8AD8 4242 MOV BL,AL ; SAVE LINE COUNT IN BL
F497 8BC1 4243 MOV AX,CX ; GET UPPER LEFT POSITION INTO AX REG
4244
4245 ;----- USE CHARACTER SUBROUTINE FOR POSITIONING
4246 ;----- ADDRESS RETURNED IS MULTIPLIED BY 2 FROM CORRECT VALUE
4247
F499 E86902 4248 CALL GRAPH_POSN
F49C 8BF8 4249 MOV DI,AX ; SAVE RESULT AS DESTINATION ADDRESS
4250
4251 ;----- DETERMINE SIZE OF WINDOW
4252
F49E 2BD1 4253 SUB DX,CX
F4A0 81C20101 4254 ADD DX,101H ; ADJUST VALUES
F4A4 D0E6 4255 SAL DH,1 ; MULTIPLY # ROWS BY 4
4256 ; SINCE 8 VERT DOTS/CHAR
F4A6 D0E6 4257 SAL DH,1 ; AND EVEN/ODD ROWS
4258
4259 ;----- DETERMINE CRT MODE
4260
F4A8 803E490006 4261 CMP CRT_MODE,6 ; TEST FOR MEDIUM RES
F4AD 7304 4262 JNC RT ; FIND_SOURCE

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4263
4264 ;----- MEDIUM RES UP
4265
F4AF D0E2          4266     SAL     DL,I          ; # COLUMNS * 2, SINCE 2 BYTES/CHAR
F4B1 D1E7          4267     SAL     DI,I          ; OFFSET *2 SINCE 2 BYTES/CHAR
4268
4269 ;----- DETERMINE THE SOURCE ADDRESS IN THE BUFFER
4270
F4B3              4271 R7:
F4B3 06           4272     PUSH   ES          ; FIND SOURCE
F4B4 1F           4273     POP    DS          ; GET SEGMENTS BOTH POINTING TO REGEN
F4B5 2AED        4274     SUB    CH,CH        ; ZERO TO HIGH OF COUNT REG
F4B7 D0E3        4275     SAL    BL,I          ; MULTIPLY NUMBER OF LINES BY 4
F4B9 D0E3        4276     SAL    BL,I          ;
F4BB 742D        4277     JZ     R11          ; IF ZERO, THEN BLANK ENTIRE FIELD
F4BD 8AC3        4278     MOV    AL,BL         ; GET NUMBER OF LINES IN AL
F4BF B450        4279     MOV    AH,80         ; 80 BYTES/ROW
F4C1 F6E4        4280     MUL   AH            ; DETERMINE OFFSET TO SOURCE
F4C3 8BF7        4281     MOV    SI,DI         ; SET UP SOURCE
F4C5 03F0        4282     ADD    SI,AX         ; ADD IN OFFSET TO IT
F4C7 8AE6        4283     MOV    AH,DH         ; NUMBER OF ROWS IN FIELD
F4C9 2AE3        4284     SUB    AH,BL         ; DETERMINE NUMBER TO MOVE
4285
4286 ;----- LOOP THROUGH, MOVING ONE ROW AT A TIME, BOTH EVEN AND ODD FIELDS
4287
F4CB              4288 R8:
F4CB E88000      4289     CALL   R17           ; ROW_LOOP
F4CE 81EEB01F    4290     SUB    SI,2000H-80   ; MOVE TO NEXT ROW
F4D2 81EFB01F    4291     SUB    DI,2000H-80   ;
F4D6 FECC        4292     DEC   AX            ; NUMBER OF ROWS TO MOVE
F4D8 75F1        4293     JNZ   R8            ; CONTINUE TILL ALL MOVED
4294
4295 ;----- FILL IN THE VACATED LINE(S)
4296
F4DA              4297 R9:
F4DA 8AC7        4298     MOV    AL,BH         ; CLEAR ENTRY
F4DC              4299     ; ATTRIBUTE TO FILL WITH
F4DC E88000      4300 R10:
F4DE 81EFB01F    4301     CALL  R18           ; CLEAR THAT ROW
F4E3 FECD        4302     SUB    DI,2000H-80   ; POINT TO NEXT LINE
F4E5 75F5        4303     DEC   BL            ; NUMBER OF LINES TO FILL
F4E7 E9DBFC      4304     JNZ   R10           ; CLEAR LOOP
F4EA              4305 R11:
F4EA 8ADE        4306     MOV    BL,DH         ; BLANK FIELD
F4EC EBEC        4307     ; SET BLANK COUNT TO
4308     JMP    R9         ; EVERYTHING IN FIELD
4309     ENDP        ; CLEAR THE FIELD
4310
4311 ;-----
4312 ; SCROLL DOWN
4313 ; THIS ROUTINE SCROLLS DOWN THE INFORMATION ON THE CRT
4314 ; ENTRY
4315 ; CH,CL = UPPER LEFT CORNER OF REGION TO SCROLL
4316 ; DH,DL = LOWER RIGHT CORNER OF REGION TO SCROLL
4317 ; BOTH OF THE ABOVE ARE IN CHARACTER POSITIONS
4318 ; BH = FILL VALUE FOR BLANKED LINES
4319 ; AL = # LINES TO SCROLL (AL=0 MEANS BLANK THE ENTIRE
4320 ; FIELD)
4321 ; DS = DATA SEGMENT
4322 ; ES = REGEN SEGMENT
4323 ; EXIT
4324 ; NOTHING, THE SCREEN IS SCROLLED
4325
F4EE              4325 GRAPHICS_DOWN PROC NEAR
F4EE FD          4326     STD
F4EF 8ADB        4327     MOV    BL,AL         ; SET DIRECTION
F4F1 8BC2        4328     MOV    AX,DX         ; SAVE LINE COUNT IN BL
4329     ; GET LOWER RIGHT POSITION INTO AX REG
4330
4331 ;----- USE CHARACTER SUBROUTINE FOR POSITIONING
4332 ;----- ADDRESS RETURNED IS MULTIPLIED BY 2 FROM CORRECT VALUE
4333
F4F3 E80F02      4333     CALL   GRAPH_POSN
F4F6 8BF8        4334     MOV    DI,AX         ; SAVE RESULT AS DESTINATION ADDRESS
4335
4336 ;----- DETERMINE SIZE OF WINDOW
4337
F4F8 2BD1        4338     SUB    DX,CX
F4FA 81C20101    4339     ADD    DX,101H
F4FE D0E6        4340     SAL    DH,I          ; ADJUST VALUES
4341     ; MULTIPLY # ROWS BY 4
4342     ; SINCE 8 VERT DOTS/CHAR
4343     ; AND EVEN/ODD ROWS
4344
F500 D0E6        4344     SAL    DH,I
4345
4346 ;----- DETERMINE CRT MODE
4347
F502 803E490006  4346     CMP    CRT_MODE,6
F507 7305        4347     JNC   R12           ; TEST FOR MEDIUM RES
4348     ; FIND_SOURCE_DOWN
4349
4350 ;----- MEDIUM RES DOWN
4351
F509 D0E2        4351     SAL    DL,I          ; # COLUMNS * 2, SINCE
4352     ; 2 BYTES/CHAR (OFFSET OK)
F50B D1E7        4353     SAL    DI,I          ; OFFSET *2 SINCE 2 BYTES/CHAR
F50D 47          4354     INC   DI            ; POINT TO LAST BYTE
4355
4356 ;----- DETERMINE THE SOURCE ADDRESS IN THE BUFFER
4357
F50E              4357 R12:
F50E 06           4358     ; FIND_SOURCE_DOWN
F50F 1F           4359     ; BOTH SEGMENTS TO REGEN
F510 2AED        4360     POP    DS
F512 81C7F000    4361     SUB    DI,240        ; ZERO TO HIGH OF COUNT REG
F516 D0E3        4362     ADD    DI,240        ; POINT TO LAST ROW OF PIXELS
F518 D0E3        4363     SAL    BL,I          ; MULTIPLY NUMBER OF LINES BY 4
F51A 742E        4364     JZ     R16          ; IF ZERO, THEN BLANK ENTIRE FIELD
F51C 8AC3        4365     MOV    AL,BL         ; GET NUMBER OF LINES IN AL
F51E B450        4366     MOV    AH,80         ; 80 BYTES/ROW
F520 F6E4        4367     MUL   AH            ; DETERMINE OFFSET TO SOURCE
F522 8BF7        4368     MOV    SI,DI         ; SET UP SOURCE
F524 2BF0        4369     SUB    SI,AX         ; SUBTRACT THE OFFSET
F526 8AE6        4370     MOV    AH,DH         ; NUMBER OF ROWS IN FIELD
F528 2AE3        4371     SUB    AH,BL         ; DETERMINE NUMBER TO MOVE
4372

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4373
4374 ;----- LOOP THROUGH, MOVING ONE ROW AT A TIME, BOTH EVEN AND ODD FIELDS
4375
F52A      R13:
F52A E82100      4377      CALL    R17      ; ROW LOOP DOWN
F52D 81EE5020   4378      SUB     S1,2000H+80 ; MOVE ONE ROW
F531 81EF5020   4379      SUB     D1,2000H+80 ; MOVE TO NEXT ROW
F536 FECC       4380      DEC     AH          ; NUMBER OF ROWS TO MOVE
F537 75F1       4381      JNZ    R13         ; CONTINUE TILL ALL MOVED
4382
4383 ;----- FILL IN THE VACATED LINE(S)
4384
F539      R14:
F539 8AC7       4386      MOV     AL,BH       ; CLEAR ENTRY DOWN
F53B      R15:
F53B E82900     4387      MOV     AL,BH       ; ATTRIBUTE TO FILL WITH
F53C 81EF5020   4388      CALL    R18         ; CLEAR_LOOP DOWN
F542 FECC       4389      SUB     D1,2000H+80 ; CLEAR A ROW
F544 75F5       4390      DEC     BL          ; POINT TO NEXT LINE
F546 FC         4391      JNZ    R15         ; NUMBER OF LINES TO FILL
F547 E97BFC     4392      CLD          ; CLEAR_LOOP DOWN
F54A      R16:
F54A 8ADE       4393      JMP     VIDEO_RETURN ; RESET THE DIRECTION FLAG
F54B      R16:
F54B 8ADE       4394      MOV     BL,DH       ; EVERYTHING DONE
F54C EBEB       4395      MOV     BL,DH       ; BLANK_FIELD DOWN
F54C EBEB       4396      JMP     R14         ; SET BLANK COUNT TO EVERYTHING
F54C EBEB       4397      GRAPHICS_DOWN     ; IN FIELD
F54C EBEB       4398      ENDP          ; CLEAR THE FIELD
F54C EBEB       4399
F54C EBEB       4400 ;----- ROUTINE TO MOVE ONE ROW OF INFORMATION
F54C EBEB       4401
F54E      R17
F54E 8ACA       4402      PROC    NEAR
F550 56         4403      MOV     CL,DL       ; NUMBER OF BYTES IN THE ROW
F551 57         4404      PUSH    SI
F552 F3         4405      PUSH    DI
F553 A4         4406      REP     MOVSB       ; SAVE POINTERS
F554 5F         4407      POP     DI          ; MOVE THE EVEN FIELD
F555 5E         4408      POP     SI
F556 81C60020   4409      ADD     SI,2000H
F55A 81C70020   4410      ADD     D1,2000H    ; POINT TO THE ODD FIELD
F55E 56         4411      PUSH    SI
F55F 57         4412      PUSH    DI          ; SAVE THE POINTERS
F560 8ACA       4413      MOV     CL,DL       ; COUNT BACK
F562 F3         4414      REP     MOVSB       ; MOVE THE ODD FIELD
F563 A4         4415      POP     DI
F564 5F         4416      POP     SI          ; POINTERS BACK
F565 5E         4417      RET
F566 C3         4418      R17      ENDP      ; RETURN TO CALLER
F566 C3         4419
F566 C3         4420 ;----- CLEAR A SINGLE ROW
F566 C3         4421
F567      R18
F567 8ACA       4422      PROC    NEAR
F569 57         4423      MOV     CL,DL       ; NUMBER OF BYTES IN FIELD
F56A F3         4424      PUSH    DI          ; SAVE POINTER
F56B AA         4425      REP     STOSB      ; STORE THE NEW VALUE
F56C 5F         4426      POP     DI          ; POINTER BACK
F56D 81C70020   4427      ADD     D1,2000H    ; POINT TO ODD FIELD
F571 57         4428      PUSH    DI
F572 8ACA       4429      MOV     CL,DL       ; SAVE POINTER
F574 F3         4430      REP     STOSB      ; FILL THE ODD FIELD
F575 AA         4431      POP     DI
F576 5F         4432      RET
F577 C3         4433      R18      ENDP      ; RETURN TO CALLER
F577 C3         4434
-----
4435 ; GRAPHICS WRITE
4436 ; THIS ROUTINE WRITES THE ASCII CHARACTER TO THE
4437 ; CURRENT POSITION ON THE SCREEN.
4438 ; ENTRY
4439 ; AL = CHARACTER TO WRITE
4440 ; BL = COLOR ATTRIBUTE TO BE USED FOR FOREGROUND COLOR
4441 ; IF BIT 7 IS SET, THE CHAR IS XOR'D INTO THE REGEN
4442 ; BUFFER (0 IS USED FOR THE BACKGROUND COLOR)
4443 ; CX = NUMBER OF CHARS TO WRITE
4444 ; DS = DATA SEGMENT
4445 ; ES = REGEN SEGMENT
4446 ; EXIT
4447 ; NOTHING IS RETURNED
4448 ;
4449 ; GRAPHICS READ
4450 ; THIS ROUTINE READS THE ASCII CHARACTER AT THE CURRENT
4451 ; CURSOR POSITION ON THE SCREEN BY MATCHING THE DOTS ON
4452 ; THE SCREEN TO THE CHARACTER GENERATOR CODE POINTS
4453 ; ENTRY
4454 ; NONE (0 IS ASSUMED AS THE BACKGROUND COLOR)
4455 ; EXIT
4456 ; AL = CHARACTER READ AT THAT POSITION (0 RETURNED IF
4457 ; NONE FOUND)
4458 ;
4459 ; FOR BOTH ROUTINES, THE IMAGES USED TO FORM CHARS ARE
4460 ; CONTAINED IN ROM FOR THE 1ST 128 CHARS. TO ACCESS CHARS
4461 ; IN THE SECOND HALF, THE USER MUST INITIALIZE THE VECTOR AT
4462 ; INTERRUPT 1FH (LOCATION 0007CH) TO POINT TO THE USER
4463 ; SUPPLIED TABLE OF GRAPHIC IMAGES (8X8 BOXES).
4464 ; FAILURE TO DO SO WILL CAUSE IN STRANGE RESULTS
4465 ;-----
4466      ASSUME  CS:CODE,DS:DATA,ES:DATA
F578      GRAPHICS_WRITE  PROC    NEAR
F578 B400       4467      MOV     AH,0        ; ZERO TO HIGH OF CODE POINT
F57A 50         4469      PUSH    AX          ; SAVE CODE POINT VALUE
4470
4471 ;----- DETERMINE POSITION IN REGEN BUFFER TO PUT CODE POINTS
4472
F57B E88401     4473      CALL    S26         ; FIND LOCATION IN REGEN BUFFER
F57E 8BF5       4474      MOV     DI,AX       ; REGEN POINTER IN DI
4475
4476 ;----- DETERMINE REGION TO GET CODE POINTS FROM
4477
F580 58         4477      POP     AX          ; RECOVER CODE POINT
F581 3C80       4478      CMP     AL,80H      ; IS IT IN SECOND HALF
F583 7306       4479      JAE     SI          ; YES
4480

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	4481		
	4482	----- IMAGE IS IN FIRST HALF, CONTAINED IN ROM	
	4483		
F585 BE6EFA	4484	MOV SI,0FA6EH	: CRT_CHAR_GEN (OFFSET OF IMAGES)
F588 0E	4485	PUSH CS	: SAVE SEGMENT ON STACK
F589 EBOF	4486	JMP SHORT S2	: DETERMINE_MODE
	4487		
	4488	----- IMAGE IS IN SECOND HALF, IN USER RAM	
	4489		
F58B	4490	S1:	: EXTEND CHAR
F58B 2C80	4491	SUB AL,80H	: ZERO ORIGIN FOR SECOND HALF
F58D 1E	4492	PUSH DS	: SAVE DATA POINTER
F58E 2BF6	4493	SUB SI,S1	
F590 8EDE	4494	MOV DS,S1	: ESTABLISH VECTOR ADDRESSING
	4495	ASSUME DS:AB50	
F592 C5367C00	4496	LDS SI,EXT_PTR	: GET THE OFFSET OF THE TABLE
F596 8CDA	4497	MOV DX,DS	: GET THE SEGMENT OF THE TABLE
	4498	ASSUME DS:DATA	
F598 1F	4499	POP DS	: RECOVER DATA SEGMENT
F599 52	4500	PUSH DX	: SAVE TABLE SEGMENT ON STACK
	4501		
	4502	----- DETERMINE GRAPHICS MODE IN OPERATION	
	4503		
F59A	4504	S2:	: DETERMINE_MODE
F59A D1E0	4505	SAL AX,1	: MULTIPLY CODE POINT
F59C D1E0	4506	SAL AX,1	: VALUE BY 8
F59E D1E0	4507	SAL AX,1	
F5A0 03F0	4508	ADD SI,AX	: SI HAS OFFSET OF DESIRED CODES
F5A2 803E490006	4509	CMP CRT_MODE,6	
F5A7 1F	4510	POP DS	: RECOVER TABLE POINTER SEGMENT
F5A8 722C	4511	JC S2	: TEST FOR MEDIUM RESOLUTION MODE
	4512		
	4513	----- HIGH RESOLUTION MODE	
	4514		
F5AA	4515	S3:	: HIGH CHAR
F5AA 57	4516	PUSH DI	: SAVE REGEN POINTER
F5AB 56	4517	PUSH SI	: SAVE CODE POINTER
F5AC B604	4518	MOV DH,4	: NUMBER OF TIMES THROUGH LOOP
F5AE	4519	S4:	
F5AE AC	4520	LDSB	: GET BYTE FROM CODE POINTS
F5AF F6C380	4521	TEST BL,80H	: SHOULD WE USE THE FUNCTION
F5B2 7516	4522	JNZ S6	: TO PUT CHAR IN
	4523		
F5B4 AA	4523	STOSB	: STORE IN REGEN BUFFER
F5B5 AC	4524	LDSB	
F5B6	4525	S5:	
F5B6 268885FF1F	4526	MOV ES:[DI+2000H-1],AL	: STORE IN SECOND HALF
F5B8 83C74F	4527	ADD DI,79	: MOVE TO NEXT ROW IN REGEN
F5BE FECE	4528	DEC DH	: DONE WITH LOOP
F5C0 75EC	4529	JNZ S4	
F5C2 5F	4530	POP SI	
F5C3 5F	4531	POP DI	: RECOVER REGEN POINTER
F5C4 47	4532	INC DI	: POINT TO NEXT CHAR POSITION
F5C5 E2E3	4533	LOOP S3	: MORE CHARS TO WRITE
F5C7 E9FBFB	4534	JMP VIDEO_RETURN	
F5CA	4535	S6:	
F5CA 263205	4536	XOR AL,ES:[DI]	: EXCLUSIVE OR WITH CURRENT
F5CD AA	4537	STOSB	: STORE THE CODE POINT
F5CE AC	4538	LDSB	: AGAIN FOR ODD FIELD
F5CF 263285FF1F	4539	XOR AL,ES:[DI+2000H-1]	
F5D4 EBE0	4540	JMP S5	: BACK TO MAINSTREAM
	4541		
	4542	----- MEDIUM RESOLUTION WRITE	
	4543		
F5D6	4544	S7:	: MED_RES_WRITE
F5D6 8AD3	4545	MOV DL,BL	: SAVE HIGH COLOR BIT
F5D8 D1E7	4546	SAL DI,1	: OFFSET *2 SINCE 2 BYTES/CHAR
F5DA E8D100	4547	CALL S19	: EXPAND BL TO FULL WORD OF COLOR
F5DD	4548	S8:	: MED_CHAR
F5DD 57	4549	PUSH DI	: SAVE REGEN POINTER
F5DE 56	4550	PUSH SI	: SAVE THE CODE POINTER
F5DF B604	4551	MOV DH,4	: NUMBER OF LOOPS
F5E1	4552	S9:	
F5E1 AC	4553	LDSB	: GET CODE POINT
F5E2 E8DE00	4554	CALL S21	: DOUBLE UP ALL THE BITS
F5E5 23C3	4555	AND AX,BX	: CONVERT THEM TO FOREGROUND
	4556		: COLOR (0 BACK)
F5E7 F6C280	4557	TEST DL,80H	: IS THIS XOR FUNCTION
F5EA 7407	4558	JZ S10	: NO, STORE IT IN AS IT IS
F5EC 263225	4559	XOR AH,ES:[DI]	: DO FUNCTION WITH HALF
F5EF 26324501	4560	XOR AL,ES:[DI+1]	: AND WITH OTHER HALF
F5F3	4561	S10:	
F5F3 268825	4562	MOV ES:[DI],AH	: STORE FIRST BYTE
F5F6 26884501	4563	MOV ES:[DI+1],AL	: STORE SECOND BYTE
F5FA AC	4564	LDSB	: GET CODE POINT
F5FB E8C500	4565	CALL S21	
F5FE 23C3	4566	AND AX,BX	: CONVERT TO COLOR
F600 F6C280	4567	TEST DL,80H	: AGAIN, IS THIS XOR FUNCTION
F603 740A	4568	JZ S11	: NO, JUST STORE THE VALUES
F605 2632A50020	4569	XOR AH,ES:[DI+2000H]	: FUNCTION WITH FIRST HALF
F60A 2632850120	4570	XOR AL,ES:[DI+2001H]	: AND WITH SECOND HALF
F60F	4571	S11:	
F60F 2688A50020	4572	MOV ES:[DI+2000H],AH	
F614 2688A50120	4573	MOV ES:[DI+2000H+1],AL	: STORE IN SECOND PORTION OF BUFFER
F619 83C750	4574	ADD DI,80	: POINT TO NEXT LOCATION
F61C FECE	4575	DEC DH	
F61E 75C1	4576	JNZ S9	: KEEP GOING
F620 5E	4577	POP SI	: RECOVER CODE POINTER
F621 5F	4578	POP DI	: RECOVER REGEN POINTER
F622 47	4579	INC DI	: POINT TO NEXT CHAR POSITION
F623 47	4580	INC DI	
F624 E2B7	4581	LOOP S8	: MORE TO WRITE
F626 E99CFB	4582	JMP VIDEO_RETURN	
	4583	GRAPHICS_WRITE	ENDP

```

4584 :-----
4585 : GRAPHICS_READ :
4586 :-----
F629 587 GRAPHICS_READ PROC NEAR
F629 E8D600 4588 CALL S26 ; CONVERTED TO OFFSET IN REGEN
F62C 8BF0 4589 MOV SI,AX ; SAVE IN SI
F62E 83EC08 4590 SUB SP,8 ; ALLOCATE SPACE TO SAVE THE
4591 ; READ CODE POINT
F631 8BEC 4592 MOV BP,SP ; POINTER TO SAVE AREA
4593
4594 ;----- DETERMINE GRAPHICS MODES
4595
F633 803E490006 4596 CMP CRT_MODE,6
F638 06 4597 PUSH ES
F639 1F 4598 POP DS ; POINT TO REGEN SEGMENT
F63A 721A 4599 JC S13 ; MEDIUM RESOLUTION
4600
4601 ;----- HIGH RESOLUTION READ
4602
4603 ;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
4604
F63C B604 4605 MOV DH,4 ; NUMBER OF PASSES
F63E 4606
F63E S12: 4607 MOV AL,[SI] ; GET FIRST BYTE
F640 884600 4608 MOV [BP],AL ; SAVE IN STORAGE AREA
F643 45 4609 INC BP ; NEXT LOCATION
F644 8A840020 4610 MOV AL,[SI+2000H] ; GET LOWER REGION BYTE
F648 884600 4611 MOV [BP],AL ; ADJUST AND STORE
F64B 45 4612 INC BP
F64C 83C650 4613 ADD SI,80 ; POINTER INTO REGEN
F64F FECE 4614 DEC DH ; LOOP CONTROL
F651 75E8 4615 JNZ S12 ; DO IT SOME MORE
F653 EB1790 4616 JMP S15 ; GO MATCH THE SAVED CODE POINTS
4617
4618 ;----- MEDIUM RESOLUTION READ
4619
F656 4620
F656 DIE6 4621 S13: SAL SI,1 ; MED_RES_READ
F658 B604 4622 MOV DH,4 ; OFFSET*2 SINCE 2 BYTES/CHAR
F65A 4623 S14: ; NUMBER OF PASSES
F65A E88800 4624 CALL S23 ; GET PAIR BYTES FROM REGEN
4625 ; INTO SINGLE SAVE
F65D 81C60020 4626 ADD SI,2000H ; GO TO LOWER REGION
F661 EB8100 4627 CALL S23 ; GET THIS PAIR INTO SAVE
F664 81EEB01F 4628 SUB SI,2000H-80 ; ADJUST POINTER BACK INTO UPPER
F668 FECE 4629 DEC DH
F66A 75EE 4630 JNZ S14 ; KEEP GOING UNTIL ALL 8 DONE
4631
4632 ;----- SAVE AREA HAS CHARACTER IN IT, MATCH IT
4633
F66C 4634
F66C BF6EFA90 4635 S15: ; FIND CHAR
F670 0E 4636 MOV DI,OFFSET CRT_CHAR_GEN ; ESTABLISH ADDRESSING
F671 07 4637 POP ES ; ESTABLISH ADDRESSING
F672 83ED08 4638 SUB BP,8 ; CODE POINTS IN CS
4639 ; ADJUST POINTER TO BEGINNING
F675 8BF5 4640 MOV SI,BP ; OF SAVE AREA
F677 FC 4641 CLD ; ENSURE DIRECTION
F678 B000 4642 MOV AL,0 ; CURRENT CODE POINT BEING MATCHED
F67A 16 4643 S16: PUSH SS ; ESTABLISH ADDRESSING TO STACK
F67B 1F 4644 POP DS ; FOR THE STRING COMPARE
F67C BA8000 4645 MOV DX,128 ; NUMBER TO TEST AGAINST
F67F 4646 S17:
F67F 56 4648 PUSH SI ; SAVE SAVE AREA POINTER
F680 57 4649 PUSH DI ; SAVE CODE POINTER
F681 B90800 4650 MOV CX,8 ; NUMBER OF BYTES TO MATCH
F684 F3 4651 REPE CMPSB ; COMPARE THE 8 BYTES
F685 A6 4652 POP DI ; RECOVER THE POINTERS
F686 5F 4653 POP SI
F687 5E 4654 JZ S18 ; IF ZERO FLAG SET, THEN MATCH OCCURRED
F688 741E 4655 INC AL ; NO MATCH, MOVE ON TO NEXT
F68A FECD 4656 ADD DI,8 ; NEXT CODE POINT
F68C 83C708 4657 DEC DX ; LOOP CONTROL
F68F 4A 4658 JNZ S17 ; DO ALL OF THEM
F690 75ED 4659
4660 ;----- CHAR NOT MATCHED, MIGHT BE IN USER SUPPLIED SECOND HALF
4661
F692 3C00 4662 CMP AL,0 ; AL < 0 IF ONLY 1ST HALF SCANNED
F694 7412 4663 JE S18 ; IF = 0, THEN ALL HAS BEEN SCANNED
F696 2BC0 4664 SUB AX,AX ; ESTABLISH ADDRESSING TO VECTOR
F698 8ED8 4665 MOV DS,AX
4666 ; ESTABLISH ADDRESSING TO VECTOR
F69A C43E7C00 4667 LES DI,EXT_PTR ; GET POINTER
F69E 8CC0 4668 MOV AX,ES ; SEE IF THE POINTER REALLY EXISTS
F6A0 0BC7 4669 OR AX,DI ; IF ALL 0, THEN DOESN'T EXIST
F6A2 7404 4670 JZ S18 ; NO SENSE LOOKING
F6A4 B080 4671 MOV SI,16 ; ORIGIN FOR SECOND HALF
F6A6 EBD2 4672 JMP ASSUME DS:DATA ; GO BACK AND TRY FOR IT
4673
4674
4675 ;----- CHARACTER IS FOUND (AL=0 IF NOT FOUND)
4676
F6A8 4677 S18:
F6A8 83C408 4678 ADD SP,8 ; READJUST THE STACK, THROW AWAY SAVE
F6AB E917FB 4679 JMP VDED_RETURN ; ALL DONE
4680 GRAPHICS_READ ENDF

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4681 -----
4682 ; EXPAND_MED_COLOR
4683 ; THIS ROUTINE EXPANDS THE LOW 2 BITS IN BL TO
4684 ; FILL THE ENTIRE BX REGISTER
4685 ; ENTRY
4686 ; BL = COLOR TO BE USED ( LOW 2 BITS )
4687 ; EXIT
4688 ; BX = COLOR TO BE USED ( 8 REPLICATIONS OF THE
4689 ; 2 COLOR BITS )
4690 -----
F6AE 4691 S19 PROC NEAR
F6AE 4692 AND BL,3 ; ISOLATE THE COLOR BITS
F6B1 4693 MOV AL,BL ; COPY TO AL
F6B3 4694 PUSH CX ; SAVE REGISTER
F6B4 4695 MOV CX,3 ; NUMBER OF TIMES TO DO THIS
F6B7 4696 S20:
F6B7 4697 SAL AL,1
F6B9 4698 SAL AL,1 ; LEFT SHIFT BY 2
F6BB 4699 OR BL,AL ; ANOTHER COLOR VERSION INTO BL
F6BD 4700 LOOP S20 ; FILL ALL OF BL
F6BF 4701 MOV BH,BL ; FILL UPPER PORTION
F6C1 4702 POP CX ; REGISTER BACK
F6C2 4703 RET ; ALL DONE
4704 S19 ENDP
4705 -----
4706 ; EXPAND_BYTE
4707 ; THIS ROUTINE TAKES THE BYTE IN AL AND DOUBLES
4708 ; ALL OF THE BITS, TURNING THE 8 BITS INTO
4709 ; 16 BITS. THE RESULT IS LEFT IN AX
4710 -----
F6C3 4711 S21 PROC NEAR
F6C3 4712 PUSH DX ; SAVE REGISTERS
F6C4 4713 PUSH CX
F6C5 4714 PUSH BX
F6C6 4715 SUB DX,DX ; RESULT REGISTER
F6C8 4716 MOV CX,1 ; MASK REGISTER
F6CB 4717 S22:
F6CB 4718 MOV BX,AX ; BASE INTO TEMP
F6CD 4719 AND BX,CX ; USE MASK TO EXTRACT A BIT
F6CF 4720 OR DX,BX ; PUT INTO RESULT REGISTER
F6D1 4721 AX,1
F6D3 4722 SHL CX,1 ; SHIFT BASE AND MASK BY 1
F6D5 4723 MOV BX,AX ; BASE TO TEMP
F6D7 4724 AND BX,CX ; EXTRACT THE SAME BIT
F6D9 4725 OR DX,BX ; PUT INTO RESULT
F6DB 4726 SHL CX,1 ; SHIFT ONLY MASK NOW,
; MOVING TO NEXT BASE
; USE MASK BIT COMING OUT TO TERMINATE
F6DD 4728 JNC S22 ; RESULT TO PARAM REGISTER
F6DF 4729 MOV AX,DX
F6E1 4730 POP CX
F6E2 4731 POP CX ; RECOVER REGISTERS
F6E3 4732 POP DX
F6E4 4733 RET ; ALL DONE
4734 S21 ENDP
4735 -----
4736 ; MED_READ_BYTE
4737 ; THIS ROUTINE WILL TAKE 2 BYTES FROM THE REGEN
4738 ; BUFFER, COMPARE AGAINST THE CURRENT FOREGROUND
4739 ; COLOR, AND PLACE THE CORRESPONDING ON/OFF BIT
4740 ; PATTERN INTO THE CURRENT POSITION IN THE SAVE
4741 ; AREA
4742 ; ENTRY
4743 ; SI,DS = POINTER TO REGEN AREA OF INTEREST
4744 ; BX = EXPANDED FOREGROUND COLOR
4745 ; BP = POINTER TO SAVE AREA
4746 ; EXIT
4747 ; BP IS INCREMENT AFTER SAVE
4748 -----
F6E5 4749 S23 PROC NEAR
F6E5 4750 MOV AH,[SI] ; GET FIRST BYTE
F6E7 4751 MOV AL,[SI+1] ; GET SECOND BYTE
F6EA 4752 MOV CX,0C000H ; 2 BIT MASK TO TEST THE ENTRIES
F6ED 4753 MOV DL,0 ; RESULT REGISTER
F6EF 4754 S24:
F6EF 4755 TEST AX,CX ; IS THIS SECTION BACKGROUND?
F6F1 4756 CLC ; CLEAR CARRY IN HOPES THAT IT IS
F6F2 4757 JZ S25 ; IF ZERO, IT IS BACKGROUND
F6F4 4758 STC ; WASN'T, SO SET CARRY
F6F5 4759 S25: RCL DL,1 ; MOVE THAT BIT INTO THE RESULT
F6F7 4760 SHR CX,1
F6F9 4761 SHR CX,1 ; MOVE THE MASK TO THE RIGHT BY 2 BITS
F6FB 4762 JNC S24 ; DO IT AGAIN IF MASK DIDN'T FALL OUT
F6FD 4763 MOV [BP],DL ; STORE RESULT IN SAVE AREA
F700 4764 INC BP ; ADJUST POINTER
F701 4765 RET ; ALL DONE
4766 S23 ENDP
4767 -----
4768 ; v4_POSITION
4769 ; THIS ROUTINE TAKES THE CURSOR POSITION
4770 ; CONTAINED IN THE MEMORY LOCATION, AND
4771 ; CONVERTS IT INTO AN OFFSET INTO THE
4772 ; REGEN BUFFER, ASSUMING ONE BYTE/CHAR.
4773 ; FOR MEDIUM RESOLUTION GRAPHICS.
4774 ; THE NUMBER MUST BE DOUBLED.
4775 ; ENTRY
4776 ; NO REGISTERS, MEMORY LOCATION
4777 ; CURSOR_POSN IS USED
4778 ; EXIT
4779 ; AX CONTAINS OFFSET INTO REGEN BUFFER
4780 -----
F702 4781 S26 PROC NEAR
F702 4782 MOV AX,CURSOR_POSN ; GET CURRENT CURSOR
F705 4783 GRAPH_POSN LABEL NEAR
F705 4784 PUSH BX ; SAVE REGISTER
F706 4785 MOV BX,AX ; SAVE A COPY OF CURRENT CURSOR
F708 4786 MOV AL,AH ; GET ROWS TO AL
F70A 4787 MUL BYTE PTR CRT_COLS ; MULTIPLY BY BYTES/COLUMN
F70C 4788 SHL AX,1 ; MULTIPLY * 4 SINCE 4 ROWS/BYTE
F710 4789 SHL AX,1
F712 4790 SUB BH,BH ; ISOLATE COLUMN VALUE
F714 4791 ADD AX,BX ; DETERMINE OFFSET
F716 4792 POP BX ; RECOVER POINTER
F717 4793 RET ; ALL DONE
4794 S26 ENDP

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4795 :-----
4796 : WRITE_TTY
4797 : THIS INTERFACE PROVIDES A TELETYPE LIKE INTERFACE TO THE VIDEO ;
4798 : CARD. THE INPUT CHARACTER IS WRITTEN TO THE CURRENT CURSOR ;
4799 : POSITION, AND THE CURSOR IS MOVED TO THE NEXT POSITION. IF THE ;
4800 : CURSOR LEAVES THE LAST COLUMN OF THE FIELD, THE COLUMN IS SET ;
4801 : TO ZERO, AND THE ROW VALUE IS INCREMENTED. IF THE ROW VALUE ;
4802 : LEAVES THE FIELD, THE CURSOR IS PLACED ON THE LAST ROW, FIRST ;
4803 : COLUMN, AND THE ENTIRE SCREEN IS SCROLLED UP ONE LINE. WHEN ;
4804 : THE SCREEN IS SCROLLED UP, THE ATTRIBUTE FOR FILLING THE NEWLY ;
4805 : BLANKED LINE IS READ FROM THE CURSOR POSITION ON THE PREVIOUS ;
4806 : LINE BEFORE THE SCROLL, IN CHARACTER MODE. IN GRAPHICS MODE, ;
4807 : THE 0 COLOR IS USED.
4808 : ENTRY
4809 : (AH) = CURRENT CRT MODE
4810 : (AL) = CHARACTER TO BE WRITTEN
4811 : NOTE THAT BACK SPACE, CAR RET, BELL AND LINE FEED ARE HANDLED ;
4812 : AS COMMANDS RATHER THAN AS DISPLAYABLE GRAPHICS
4813 : (BL) = FOREGROUND COLOR FOR CHAR WRITE IF CURRENTLY IN A ;
4814 : GRAPHICS MODE
4815 : EXIT
4816 : ALL REGISTERS SAVED
4817 :-----
4818 ASSUME CS:CODE,DS:DATA
4819 WRITE_TTY PROC NEAR
4820 PUSH AX ; SAVE REGISTERS
4821 PUSH AX ; SAVE CHAR TO WRITE
4822 MOV AH,3
4823 MOV BH,ACTIVE_PAGE ; GET THE CURRENT ACTIVE PAGE
4824 INT 10H ; READ THE CURRENT CURSOR POSITION
4825 POP AX ; RECOVER CHAR
4826
4827 :----- DX NOW HAS THE CURRENT CURSOR POSITION
4828
4829 CMP AL,8 ; IS IT A BACKSPACE
4830 JE UB ; BACK SPACE
4831 CMP AL,0DH ; IS IT CARRIAGE RETURN
4832 JE U9 ; CAR RET
4833 CMP AL,0AH ; IS IT A LINE FEED
4834 JE U1 ; LINE FEED
4835 CMP AL,07H ; IS IT A BELL
4836 JE U11 ; BELL
4837
4838 :----- WRITE THE CHAR TO THE SCREEN
4839
4840
4841 MOV AH,10 ; WRITE CHAR ONLY
4842 MOV CX,1 ; ONLY ONE CHAR
4843 INT 10H ; WRITE THE CHAR
4844
4845 :----- POSITION THE CURSOR FOR NEXT CHAR
4846
4847 INC DL
4848 CMP DL,BYTE PTR CRT_COLS ; TEST FOR COLUMN OVERFLOW
4849 JNZ U7 ; SET CURSOR
4850 MOV DL,0 ; COLUMN FOR CURSOR
4851 CMP DH,24
4852 JNZ U6 ; SET_CURSOR_INC
4853
4854 :----- SCROLL REQUIRED
4855
4856 U1:
4857 MOV AH,2
4858 INT 10H ; SET THE CURSOR
4859
4860 :----- DETERMINE VALUE TO FILL WITH DURING SCROLL
4861
4862 MOV AL,CRT_MODE ; GET THE CURRENT MODE
4863 CMP AL,4
4864 JC U2 ; READ-CURSOR
4865 CMP AL,7
4866 MOV BH,0 ; FILL WITH BACKGROUND
4867 JNE U3 ; SCROLL-UP
4868 U2:
4869 MOV AH,8 ; READ-CURSOR
4870 INT 10H ; READ CHAR/ATTR AT CURRENT CURSOR
4871 MOV BH,AH ; STORE IN BH
4872 U3:
4873 MOV AX,601H ; SCROLL-UP
4874 SUB CX,CX ; SCROLL ONE LINE
4875 MOV DH,24 ; UPPER LEFT CORNER
4876 DL,BYTE PTR CRT_COLS ; LOWER RIGHT ROW
4877 DEC DL ; LOWER RIGHT COLUMN
4878 U4:
4879 INT 10H ; VIDEO-CALL-RETURN
4880 U5:
4881 POP AX ; SCROLL UP THE SCREEN
4882 JMP VIDEO_RETURN ; TTY-RETURN
4883 U6:
4884 INC DH ; RESTORE THE CHARACTER
4885 U7:
4886 MOV AH,2 ; RETURN TO CALLER
4887 JMP U4 ; SET-CURSOR-INC
4888 ; SET-CURSOR
4889 :----- BACK SPACE FOUND
4890
4891 U8:
4892 CMP DL,0 ; ALREADY AT END OF LINE
4893 JE U7 ; SET CURSOR
4894 DEC DL ; NO -- JUST MOVE IT BACK
4895 JMP U7 ; SET CURSOR
4896
4897 :----- CARRIAGE RETURN FOUND
4898
4899 U9:
4900 MOV DL,0 ; MOVE TO FIRST COLUMN
4901 JMP U7 ; SET_CURSOR
4902
4903 :----- LINE FEED FOUND
4904
4905 U10:
4906 CMP U6,24 ; BOTTOM OF SCREEN
4907 JNE U6 ; YES, SCROLL THE SCREEN
4908 JMP U1 ; NO, JUST SET THE CURSOR

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LOC OBJECT          LINE SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82
4909
4910 ;----- BELL FOUND
4911
F78D                4912 U11:
F78D B302           4913     MOV     BL,2           ; SET UP COUNT FOR BEEP
F78F E81602         4914     CALL    BEEP          ; SOUND THE POD BELL
F792 E8BB           4915     JMP     US              ; TTY_RETURN
4916 WRITE_TTY      4916     ENDP
4917 ;-----
4918 ; LIGHT PEN
4919 ; THIS ROUTINE TESTS THE LIGHT PEN SWITCH AND THE LIGHT
4920 ; PEN TRIGGER. IF BOTH ARE SET, THE LOCATION OF THE LIGHT
4921 ; PEN IS DETERMINED. OTHERWISE, A RETURN WITH NO
4922 ; INFORMATION IS MADE.
4923 ; ON EXIT
4924 ; (AH) = 0 IF NO LIGHT PEN INFORMATION IS AVAILABLE
4925 ; BX,CX,DX ARE DESTROYED
4926 ; (AH) = 1 IF LIGHT PEN IS AVAILABLE
4927 ; (DH,DL) = ROW,COLUMN OF CURRENT LIGHT PEN
4928 ; POSITION
4929 ; (CH) = RASTER POSITION
4930 ; (BX) = BEST GUESS AT PIXEL HORIZONTAL POSITION
4931 ;-----
4932     ASSUME CS:CODE,DS:DATA
4933     SUBTRACT TABLE
4934 V1 LABEL BYTE
4935     DB     3,3,5,5,3,3,3,4 ;

F794
F794 03
F795 03
F796 05
F797 05
F798 03
F799 03
F79A 03
F79B 04
F79C

4936 READ_LPEN      PROC    NEAR
4937
4938 ;----- WAIT FOR LIGHT PEN TO BE DEPRESSED
4939
F79C B400           4940     MOV     AH,0           ; SET NO LIGHT PEN RETURN CODE
F79E 8B166300       4941     MOV     DX,ADDR_6845  ; GET BASE ADDRESS OF 6845
F7A2 83C206        4942     ADD     DX,DX          ; POINT TO STATUS REGISTER
F7A5 EC            4943     IN     AL,DX          ; GET STATUS REGISTER
F7A6 A804          4944     TEST    AL,4           ; TEST LIGHT PEN SWITCH
F7A8 757E          4945     JNZ     V6             ; NOT SET, RETURN
4946
4947 ;----- NOW TEST FOR LIGHT PEN TRIGGER
4948
F7AA A802          4949     TEST    AL,2           ; TEST LIGHT PEN TRIGGER
F7AC 7503          4950     JNZ     V7A          ; RETURN WITHOUT RESETTING TRIGGER
F7AE E98100        4951     JMP     V7
4952
4953 ;----- TRIGGER HAS BEEN SET, READ THE VALUE IN
4954
F7B1
F7B1 B410          4955     V7A:
4956     MOV     AH,16           ; LIGHT PEN REGISTERS ON 6845
4957
4958 ;----- INPUT REGS POINTED TO BY AH, AND CONVERT TO ROW COLUMN IN DX
4959
F7B3 8B166300       4960     MOV     DX,ADDR_6845  ; ADDRESS REGISTER FOR 6845
F7B7 8AC4          4961     MOV     AL,AH          ; REGISTER TO READ
F7B9 EE            4962     OUT    DX,AL          ; SET IT UP
F7BA 42            4963     INC     DX             ; DATA REGISTER
F7BB EC            4964     IN     AL,DX          ; GET THE VALUE
F7BC 8AE8          4965     MOV     CH,AL          ; SAVE IN CX
F7BE 4A            4966     DEC     DX             ; ADDRESS REGISTER
F7BF FEC4          4967     INC     AH             ;
F7C1 8AC4          4968     MOV     AL,AH          ;
F7C3 EE            4969     OUT    DX,AL          ; SECOND DATA REGISTER
F7C4 42            4970     INC     DX             ; POINT TO DATA REGISTER
F7C5 EC            4971     IN     AL,DX          ; GET SECOND DATA VALUE
F7C6 8AE5          4972     MOV     AH,CX          ; AX HAS INPUT VALUE
4973
4974 ;----- AX HAS THE VALUE READ IN FROM THE 6845
4975
F7C8 8A1E4900       4976     MOV     BL,CRT_MODE
F7CC 2AFF          4977     SUB     BH,BH          ; MODE VALUE TO BX
F7CE 2E8A9F94F7    4978     MOV     BL,CS:V1[BX]  ; DETERMINE AMOUNT TO SUBTRACT
F7D3 2BC3          4979     SUB     AX,BX          ; TAKE IT AWAY
F7D5 8B1E4E00       4980     MOV     BX,CRT_START
F7D9 D1EB          4981     SHR     BX,1
F7DB 2BC3          4982     SUB     AX,BX
F7DD 7902          4983     JNS     V2             ; IF POSITIVE, DETERMINE MODE
F7DF 2BC0          4984     SUB     AX,AX          ; <0 PLAYS A3 0
4985
4986 ;----- DETERMINE MODE OF OPERATION
4987
F7E1
F7E1 B103           4988     V2:
4989     MOV     CL,3           ; DETERMINE MODE
F7E3 803E490004     4990     CMP     CRT_MODE,4    ; SET *8 SHIFT COUNT
F7E5 722A          4991     JB     V4             ; DETERMINE IF GRAPHICS OR ALPHA
F7EA 803E490007     4992     CMP     CRT_MODE,7    ; ALPHA_PEN
F7EF 7423          4993     JE     V4             ; ALPHA_PEN
4994
4995 ;----- GRAPHICS MODE
4996
F7F1 B228          4997     MOV     DL,40          ; DIVISOR FOR GRAPHICS
F7F3 F6F2          4998     DIV    DL             ; DETERMINE ROW(AL) AND COLUMN(AH)
4999     ; AL RANGE 0-99, AH RANGE 0-39
5000
5001 ;----- DETERMINE GRAPHIC ROW POSITION
5002
F7F5 8AE8          5003     MOV     CH,AL          ; SAVE ROW VALUE IN CH
F7F7 02ED          5004     ADD     CH,CH          ; *2 FOR EVEN/ODD FIELD
5005     MOV     BL,AH          ; *2 COLUMN VALUE TO BX
F7F9 8ADC          5006     SUB     BH,BH          ; MULTIPLY BY 8 FOR MEDIUM RES
F7FB 2AFF          5007     CMP     CRT_MODE,6    ; DETERMINE MEDIUM OR HIGH RES
F802 7504          5008     JNE     V3             ; NOT HIGH RES
F804 B104          5009     MOV     CL,4           ; SHIFT VALUE FOR HIGH RES
F806 D0E4          5010     SAL    AH,1           ; COLUMN VALUE TIMES 2 FOR HIGH RES
F808                5011     V3:
F808 D3E3          5012     SHL    BX,CL          ; MULTIPLY *16 FOR HIGH RES

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5013
5014 ;----- DETERMINE ALPHA CHAR POSITION
5015
F80A 8AD4 5016      MOV     DL,AH      ; COLUMN VALUE FOR RETURN
F80C 8AF0 5017      MOV     DH,AL      ; ROW VALUE
F80E D0EE 5018      SHR     DH,1      ; DIVIDE BY 4
F810 D0EE 5019      SHR     DH,1      ; FOR VALUE IN 0-24 RANGE
F812 EB12 5020      JMP     SHORT V5   ; LIGHT_PEN_RETURN_SET
5021
5022 ;----- ALPHA MODE ON LIGHT PEN
5023
F814 5024 V4:      ; ALPHA PEN
F814 F6364A00 5025     DIV     BYTE PTR CRT_COLS ; DETERMINE ROW,COLUMN VALUE
F818 8AF0 5026     MOV     DH,AL      ; ROWS TO DH
F81A 8AD4 5027     MOV     DL,AH      ; COLS TO DL
F81C D2E0 5028     SAL     AL,CL      ; MULTIPLY ROWS * 8
F81E 8AE8 5029     MOV     CH,AL      ; GET RASTER VALUE TO RETURN REG
F820 8ADC 5030     MOV     BL,AH      ; COLUMN VALUE
F822 32FF 5031     XOR     BH,BH      ; TO BX
F824 D3E3 5032     SAL     BX,CL
F826 5033 V5:      ; LIGHT PEN RETURN SET
F826 B401 5034     MOV     AH,1      ; INDICATE EVERTHING SET
F828 5035 V6:      ; LIGHT PEN RETURN
F828 52 5036     PUSH    DX      ; SAVE RETURN VALUE (IN CASE)
F829 8B166300 5037     MOV     DX,ADDR_6845 ; GET BASE ADDRESS
F82D 83C207 5038     ADD     DX,7      ; POINT TO RESET PARM
F830 EE 5039     OUT     DX,AL      ; ADDRESS, NOT DATA, IS IMPORTANT
F831 5A 5040     POP     DX      ; RECOVER VALUE
F832 5041 V7:      ; RETURN_NO_RESET
F832 5F 5042     POP     DI
F833 5E 5043     POP     SI
F834 1F 5044     POP     DS
F835 1F 5045     POP     DS      ; DISCARD SAVED BX,CX,DX
F836 1F 5046     POP     DS
F837 1F 5047     POP     DS
F838 07 5048     POP     ES
F839 CF 5049     IRET
5050 READ_LPEN  ENDP

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5051 -----
5052 :--- INT 12 -----
5053 : MEMORY_SIZE_DET
5054 : THIS ROUTINE DETERMINES THE AMOUNT OF MEMORY IN THE SYSTEM
5055 : AS REPRESENTED BY THE SWITCHES ON THE PLANAR. NOTE THAT THE
5056 : SYSTEM MAY NOT BE ABLE TO USE I/O MEMORY UNLESS THERE IS A FULL
5057 : COMPLEMENT OF 64K BYTES ON THE PLANAR.
5058 : INPUT
5059 : NO REGISTERS
5060 : THE MEMORY_SIZE VARIABLE IS SET DURING POWER ON DIAGNOSTICS
5061 : ACCORDING TO THE FOLLOWING HARDWARE ASSUMPTIONS:
5062 : PORT 60 BITS 3,2 = 00 - 16K BASE RAM
5063 : 01 - 32K BASE RAM
5064 : 10 - 48K BASE RAM
5065 : 11 - 64K BASE RAM
5066 : PORT 62 BITS 3-0 INDICATE AMOUNT OF I/O RAM IN 32K INCREMENTS
5067 : E.G., 0000 - NO RAM IN I/O CHANNEL
5068 : 0010 - 64K RAM IN I/O CHANNEL, ETC.
5069 : OUTPUT
5070 : (AX) = NUMBER OF CONTIGUOUS 1K BLOCKS OF MEMORY
5071 -----
5072 : ASSUME CS:CODE,DS:DATA
5073 : ORG 0F841H
5074 MEMORY_SIZE_DET PROC FAR
5075 : STI
5076 : PUSH DS
5077 : CALL DDS
5078 : MOV AX, MEMORY_SIZE
5079 : POP DS
5080 : IRET
5081 MEMORY_SIZE_DET ENDP
5082 -----
5083 :--- INT 11 -----
5084 : EQUIPMENT DETERMINATION
5085 : THIS ROUTINE ATTEMPTS TO DETERMINE WHAT OPTIONAL
5086 : DEVICES ARE ATTACHED TO THE SYSTEM.
5087 : INPUT
5088 : NO REGISTERS
5089 : THE EQUIP_FLAG VARIABLE IS SET DURING THE POWER ON
5090 : DIAGNOSTICS USING THE FOLLOWING HARDWARE ASSUMPTIONS:
5091 : PORT 60 = LOW ORDER BYTE OF EQUIPMENT
5092 : PORT 3FA = INTERRUPT ID REGISTER OF 8250
5093 : BITS 7-3 ARE ALWAYS 0
5094 : PORT 378 = OUTPUT PORT OF PRINTER -- 8255 PORT THAT
5095 : CAN BE READ AS WELL AS WRITTEN
5096 : OUTPUT
5097 : (AX) IS SET, BIT SIGNIFICANT, TO INDICATE ATTACHED I/O
5098 : BIT 15,14 = NUMBER OF PRINTERS ATTACHED
5099 : BIT 13 NOT USED
5100 : BIT 12 = GAME I/O ATTACHED
5101 : BIT 11,10,9 = NUMBER OF RS232 CARDS ATTACHED
5102 : BIT 8 UNUSED
5103 : BIT 7,6 = NUMBER OF DISKETTE DRIVES
5104 : 00=1, 01=2, 10=3, 11=4 ONLY IF BIT 0 = 1
5105 : BIT 5,4 = INITIAL VIDEO MODE
5106 : 00 - UNUSED
5107 : 01 - 40X25 BW USING COLOR CARD
5108 : 10 - 80X25 BW USING COLOR CARD
5109 : 11 - 80X25 BW USING BW CARD
5110 : BIT 3,2 = PLANAR RAM SIZE (00=16K,01=32K,10=48K,11=64K)
5111 : BIT 1 NOT USED
5112 : BIT 0 = IPL FROM DISKETTE -- THIS BIT INDICATES THAT
5113 : THERE ARE DISKETTE DRIVES ON THE SYSTEM
5114 :
5115 : NO OTHER REGISTERS AFFECTED
5116 -----
5117 : ASSUME CS:CODE,DS:DATA
5118 : ORG 0F84DH
5119 EQUIPMENT PROC FAR
5120 : STI
5121 : PUSH DS
5122 : CALL DDS
5123 : MOV AX,EQUIP_FLAG
5124 : POP DS
5125 : IRET
5126 EQUIPMENT ENDP
5127 -----
5128 :--- INT 15 -----
5129 : DUMMY CASSETTE IO ROUTINE-RETURNS 'INVALID CMD' IF THE ROUTINE IS
5130 : IS EVER CALLED BY ACCIDENT (AH=86H, CARRY FLAG=1)
5131 -----
5132 : ORG 0F859H
5133 CASSETTE_IO PROC FAR
5134 : STC
5135 : MOV AH,86H
5136 : RET 2
5137 CASSETTE_IO ENDP

```

F841
F841 FB
F842 IE
F843 E81302
F846 A11300
F849 IF
F84A CF

F84D
F84D
F84D FB
F84E IE
F84F E80702
F852 A11000
F855 IF
F856 CF

F859
F859
F859 F9
F85A B486
F85C CA0200

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5138
5139
5140 ; NON-MASKABLE INTERRUPT ROUTINE:
5141 ; THIS ROUTINE WILL PRINT A PARITY CHECK 1 OR 2 MESSAGE ;
5142 ; AND ATTEMPT TO FIND THE STORAGE LOCATION CONTAINING THE ;
5143 ; BAD PARITY. IF FOUND, THE SEGMENT ADDRESS WILL BE ;
5144 ; PRINTED. IF NO PARITY ERROR CAN BE FOUND (INTERMITTANT ;
5145 ; READ PROBLEM) ?????<- WILL BE PRINTED WHERE THE ADDRESS ;
5146 ; WOULD NORMALLY GO. ;
5147 ; IF ADDRESS IN ERROR IS IN THE I/O EXPANSION BOX, THE ;
5148 ; ADDRESS WILL BE FOLLOWED BY A '(E)', IF IN SYSTEM UNIT, ;
5149 ; A '(S)' WILL FOLLOW THE ADDRESS ;
-----
5150
F85F 5151 NMI_INT PROC NEAR
5152 ASSUME DS,DATA
F85F 50 5153 PUSH AX ; SAVE ORIG CONTENTS OF AX
F860 E462 5154 IN AL,PORT_C ; PARITY CHECK?
F862 A8C0 5155 TEST AL,000H ; PARITY CHECK?
F864 7503 5156 JNZ NMI ; NO, EXIT FROM ROUTINE
F866 E98700 5157 JMP D14
F869 5158 NMI_1:
F869 BA4000 5159 MOV DX,DATA
F86C 8EDA 5160 MOV DS,DX
F86E BE15F990 5161 MOV SI,OFFSET D1 ; ADDR OF ERROR MSG
F8A0 A840 5162 AL,40H ; I/O PARITY CHECK
F874 7504 5163 JNZ D13 ; DISPLAY ERROR MSG
F876 BE25F990 5164 MOV SI,OFFSET D2 ; MUST BE PLANAR
F87A B400 5165 D13: MOV AH,0 ; INIT AND SET MODE FOR VIDEO
F87C A04900 5167 MOV AL,CRT_MODE
F87F CD10 5168 INT 10H ; CALL VIDEO IO PROCEDURE
F881 E84601 5169 CALL P_MSG ; PRINT ERROR MSG
5170
5171 ;----- SEE IF LOCATION THAT CAUSED PARITY CHECK CAN BE FOUND
5172
F884 B000 5173 MOV AL,00H ; DISABLE TRAP
F886 E6A0 5174 OUT 0A0H,AL
F888 E461 5175 IN AL,PORT_B
F88A 0C30 5176 OR AL,00110000B ; TOGGLE PARITY CHECK ENABLES
F88C E661 5177 PORT_B,AL
F88E 24CF 5178 AND AL,1001111B
F890 E661 5179 OUT PORT_B,AL
F892 8B1E1300 5180 MOV BX,MEMORY_SIZE ; GET MEMORY SIZE WORD
F896 FC 5181 CLD ; SET DIR FLAG TO INCREMENT
F897 2BD2 5182 SUB DX,DX ; POINT DX AT START OF MEM
F899 5183 NMI_LOOP:
F899 8EDA 5184 MOV DS,DX
F89B BEC2 5185 MOV ES,DX
F89D B90040 5186 MOV CX,4000H ; SET FOR 16KB SCAN
F8A0 2BF6 5187 SUB SI,SI ; SET SI TO BE REALTIME TO
; START OF ES
F8A2 F3 5188 REP LODSB ; READ 16KB OF MEMORY
F8A3 AC
F8A4 E462 5190 IN AL,PORT_C ; SEE IF PARITY CHECK HAPPENED
F8A6 24C0 5191 AND AL,11000000B
F8A8 7512 5192 JNZ PRT_NMI ; GO PRINT ADDRESS IF IT DID
F8AA 81C20004 5193 ADD DX,0400H ; POINT TO NEXT 16K BLOCK
F8AE 83EB10 5194 SUB BX,16D
F8B1 75E2 5195 JNZ NMI_LOOP
F8B3 BE35F990 5196 MOV SI,OFFSET D2A1 ; PRINT ROW OF ????? IF PARITY
F8B7 E81001 5197 CALL P_MSG ; CHECK COULD NOT BE RE-CREATED
F8BA FA 5198 CLI
F8BC F4 5199 HLT ; HALT SYSTEM
F8BC 5200 PRT_NMI:
F8BC 8CDA 5201 MOV DX,DS
F8BE E81907 5202 CALL PRT_SEG ; PRINT SEGMENT VALUE
F8C1 BA1302 5203 MOV DX,0E13H
F8C4 B000 5204 MOV AL,00 ; DISABLE EXPANSION BOX
F8C6 EE 5205 OUT DX,AL ; (CAN'T WRITE TO MEM)
F8C7 B028 5206 MOV AL,' '
F8C9 E8D000 5207 CALL PRT_HEX
F8CC B85AA5 5208 MOV AX,0A55AH
F8CF 8BC8 5209 MOV CX,AX
F8D1 2BDB 5210 SUB BX,BX
F8D3 8907 5211 MOV [BX],AX ; WRITE A WORD TO SEGMENT THAT
F8D5 90 5212 NOP
F8D6 90 5213 NOP
F8D7 8B07 5214 MOV AX,[BX] ; HAD THE ERROR
F8D9 3BC1 5215 CMP AX,CX ; IS IT THERE?
F8DB 7407 5216 JE SYS_BOX_ERR ; YES- MUST BE SYS UNIT
F8DD B045 5217 MOV AL,'E' ; NO-MUST BE IN EXP. BOX
F8DF E8B400 5218 CALL PRT_HEX
F8E2 EB05 5219 JMP SHORT HLT_NMI
F8E4 5220 SYS_BOX_ERR:
F8E4 B053 5221 MOV AL,'S'
F8E6 E8B300 5222 CALL PRT_HEX
F8E9 5223 HLT_NMI:
F8E9 B029 5224 MOV AL,')'
F8EB E8AE00 5225 CALL PRT_HEX
F8EE FA 5226 CLI ; HALT SYSTEM
F8EF F4 5227 HLT
F8F0 5228 D14: MOV AX ; RESTORE ORIG CONTENTS OF AX
F8F0 58 5229 POP AX
F8F1 CF 5230 IRET
5231 NMI_INT ENDP
5232
-----
5233 ;
5234 ; ROS CHECKSUM SUBROUTINE
5235 ;
5236
F8F2 5236 ROS_CHECKSUM PROC NEAR ; NEXT ROS MODULE
F8F2 B90020 5237 MOV CX,8192 ; NUMBER OF BYTES TO ADD
F8F5 5238 ROS_CHECKSUM_CNT: ; ENTRY FOR OPTIONAL ROS TEST
F8F5 32C0 5239 XOR AL,AL
F8F7 5240 C26: ADD AL,DS:[BX]
F8F7 0207 5241 INC BX ; POINT TO NEXT BYTE
F8F9 43 5242 LOOP C26 ; ADD ALL BYTES IN ROS MODULE
F8FA E2FB 5243 INC BX ; SUM = 0?
F8FC 0AC0 5244 OR AL,AL
F8FE C3 5245 RET
5246 ROS_CHECKSUM ENDP

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LOC OBJECT          LINE SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82
5247 ;-----
5248 ; MESSAGE AREA FOR POST
5249 ;-----
F8FF 313031        5250 E0 DB '101',13,10 ; SYSTEM BOARD ERROR
F902 0D
F903 0A
F904 20323031     5251 E1 DB ' 201',13,10 ; MEMORY ERROR
F908 0D
F909 0A
F90A 524F4D      5252 F3A DB 'ROM',13,10 ; ROM CHECKSUM ERROR
F90D 0D
F90E 0A
F90F 31383031    5253 F3C DB '1801',13,10 ; EXPANSION IO BOX ERROR
F913 0D
F914 0A
F915 50415249545920 5254 D1 DB '*PARITY CHECK 2',13,10
      434845434B2032
F923 0D
F924 0A
F925 50415249545920 5255 D2 DB '*PARITY CHECK 1',13,10
      434845434B2031
F933 0D
F934 0A
F935 3F3F3F3F3F 5256 D2A DB '?????',13,10
F93A 0D
F93B 0A

5257 ;-----
5258 ; BLINK LED PROCEDURE FOR MFG RUN-IN TESTS
5259 ; IF LED IS ON, TURN IT OFF. IF OFF, TURN ON.
5260 ;-----
5261 ;-----
5262 ASSUME DS:DATA
5263 BLINK_INT PROC NEAR
5264 STI
5265 PUSH AX ; SAVE AX REG CONTENTS
5266 IN AL,PORT_B ; READ CURRENT VAL OF PORT B
5267 MOV AH,AL
5268 NOT AL ; FLIP ALL BITS
5269 AND AL,01000000B ; ISOLATE CONTROL BIT
5270 AND AH,10111111B ; MASK OUT OF ORIGINAL VAL
5271 OR AL,AH ; OR NEW CONTROL BIT IN
5272 OUT PORT_B,AL
5273 MOV AL,E01
5274 OUT INTA00,AL
5275 POP AX ; RESTORE AX REG
5276 IRET
5277 BLINK_INT ENDP
5278
5279 ;-----
5280 ; THIS ROUTINE CHECKSUMS OPTIONAL ROM MODULES AND
5281 ; IF CHECKSUM IS OK, CALLS INIT/TEST CODE IN MODULE
5282 ;-----
5283 ROM_CHECK PROC NEAR
5284 MOV AX,DATA ; POINT ES TO DATA AREA
5285 MOV ES,AX
5286 SUB AL,AH ; ZERO OUT AH
5287 MOV AL,[BX+2] ; GET LENGTH INDICATOR
5288 MOV CL,09H ; MULTIPLY BY 512
5289 SHL AX,CL
5290 MOV CX,AX ; SET COUNT
5291 PUSH CX ; SAVE COUNT
5292 MOV CX,4 ; ADJUST
5293 SHR AX,CL ; SET POINTER TO NEXT MODULE
5294 ADD DX,AX ; RETRIEVE COUNT
5295 POP CX ; DO CHECKSUM
5296 CALL ROM_CHECKSUM_CNT
5297 JZ ROM_CHECK_1 ; POST CHECKSUM ERROR
5298 ROM_CALL ; AND EXIT
5299 JMP ROM_CHECK_END
5300 ROM_CHECK_1:
5301 PUSH DX ; SAVE POINTER
5302 MOV ES:10_ROM_INIT,0003H ; LOAD OFFSET
5303 MOV ES:10_ROM_SEG,DS ; LOAD SEGMENT
5304 CALL DWORD PTR ES:10_ROM_INIT ; CALL INIT./TEST ROUTINE
5305 POP DX
5306 ROM_CHECK_END:
5307 RET ; RETURN TO CALLER
5308 ROM_CHECK ENDP
5309
5310 ;-----
5311 ; CONVERT AND PRINT ASCII CODE
5312 ; AL MUST CONTAIN NUMBER TO BE CONVERTED.
5313 ; AX AND BX DESTROYED.
5314 ;-----
5315 XPC_BYTE PROC NEAR
5316 PUSH AX ; SAVE FOR LOW NIBBLE DISPLAY
5317 MOV CL,4 ; SHIFT COUNT
5318 SHR AL,CL ; NYBBLE SWAP
5319 CALL XLAT_PR ; DO THE HIGH NIBBLE DISPLAY
5320 POP AX ; RECOVER THE NIBBLE
5321 AND AL,0FH ; ISOLATE TO LOW NIBBLE
5322 ; FALL INTO LOW NIBBLE CONVERSION
5323 XLAT_PR PROC NEAR ; CONVERT 00-0F TO ASCII CHARACTER
5324 ADD AL,090H ; ADD FIRST CONVERSION FACTOR
5325 MOV PROC ; ADJUST FOR NUMERIC AND ALPHA RANGE
5326 ADC AL,040H ; ADD CONVERSION AND ADJUST LOW NIBBLE
5327 DAA ; ADJUST HIGH NIBBLE TO ASCII RANGE
5328 PRT_HEX PROC NEAR ; DISPLAY CHARACTER IN AL
5329 MOV AH,14
5330 MOV BH,0
5331 INT 10H ; CALL VIDEO_10
5332 RET
5333 PRT_HEX ENDP
5334 XLAT_PR ENDP
5335 XPC_BYTE ENDP
5336
5337 F4 LABEL ; PRINTER SOURCE TABLE
5338 F4 LABEL
5339 DW 38CH
5340 DW 37BH
5341 DW 27BH
5342 F4E LABEL WORD
5343

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SECTION 5

LOC OBJECT

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5343 ;-----
5344 ; THIS SUBROUTINE WILL PRINT A MESSAGE ON THE DISPLAY ;
5345 ; ;
5346 ; ENTRY REQUIREMENTS: ;
5347 ; SI = OFFSET(ADDRESS) OF MESSAGE BUFFER ;
5348 ; CX = MESSAGE BYTE COUNT ;
5349 ; MAXIMUM MESSAGE LENGTH IS 36 CHARACTERS ;
5350 ;-----
F9A9 E8EE 5351 E_MSG PROC NEAR
F9AB E81C00 5352 MOV BP,SI ; SET BP NON-ZERO TO FLAG ERR
F9AE IE 5353 CALL P_MSG ; PRINT MESSAGE
F9AF E8A700 5354 PUSH DS
F9B2 A01000 5355 CALL DDS
F9B5 2401 5356 MOV AL,BYTE PTR EQUIP_FLAG ; LOOP/HALT ON ERROR
F9B7 750F 5357 AND AL,01H ; SWITCH ON?
F9B9 5358 JNZ G12 ; NO - RETURN
F9BB FA 5359 MFG_HALT: CL I ; YES - HALT SYSTEM
F9BA B089 5360 MOV AL,89H
F9BC E663 5361 OUT CMD_PORT,AL
F9BE B085 5362 MOV AL,T0000101B ; DISABLE KB
F9C0 E661 5363 OUT PORT_B,AL
F9C2 A01500 5364 MOV AL,MFG_ERR_FLAG ; RECOVER ERROR INDICATOR
F9C5 E660 5365 OUT PORT_A,AL ; SET INTO 8255 REG
F9C7 F4 5366 HLT ; HALT SYS
F9C8 5367 G12:
F9C9 C3 5368 POP DS ; WRITE_MSG:
5370 RET
5371 E_MSG ENDP
5372
5373 P_MSG PROC NEAR
5374 GT2A:
F9CA 5375 MOV AL,CS:[SI] ; PUT CHAR IN AL
F9CB 2E8A04 5376 INC SI ; POINT TO NEXT CHAR
F9CD 46 5377 PUSH AX ; SAVE PRINT CHAR
F9CE 50 5378 CALL PRT_HEX ; CALL VIDEO IO
F9CF E8CAFF 5379 POP AX ; RECOVER PRINT CHAR
F9D2 58 5380 CMP AL,10 ; WAS IT LINE FEED?
F9D3 3C0A 5381 JNE G12A ; NO,KEEP PRINTING STRING
F9D5 75F3 5382 RET
F9D7 C3 5383 P_MSG ENDP
5384
5385 ;-----
5386 ; INITIAL RELIABILITY TEST -- SUBROUTINES ;
5387 ;-----
5388 ; ASSUME CS:CODE,DS:DATA ;
5389 ;-----
5390 ; SUBROUTINES FOR POWER ON DIAGNOSTICS ;
5391 ;-----
5392 ; THIS PROCEDURE WILL ISSUE ONE LONG TONE (3 SECS) AND ONE OR ;
5393 ; MORE SHORT TONES (1 SEC) TO INDICATE A FAILURE ON THE PLANAR ;
5394 ; BOARD, A BAD RAM MODULE, OR A PROBLEM WITH THE CRT. ;
5395 ; ENTRY PARAMETERS: ;
5396 ; DH = NUMBER OF LONG TONES TO BEEP. ;
5397 ; DL = NUMBER OF SHORT TONES TO BEEP. ;
5398 ;-----
F9D8 5399 ERR_BEEP PROC NEAR
F9D9 9C 5400 PUSHF
F9DA IE 5401 CL I ; SAVE FLAGS
F9DB E81B00 5402 PUSH DS ; DISABLE SYSTEM INTERRUPTS
F9DE 0AF6 5403 CALL DDS ; SAVE DS REG CONTENTS
F9E0 7414 5404 OR DH,DH ; ANY LONG ONES TO BEEP
F9E2 8306 5405 JZ G3 ; NO, DO THE SHORT ONES
F9E4 E82100 5406 MOV BL,6 ; LONG BEEP:
F9E7 E2FE 5407 CALL G2 ; COUNTER FOR BEEPS
F9E9 FECE 5408 ; DO THE BEEP
F9EB 75F5 5409 G2: LOOP G2 ; DELAY BETWEEN BEEPS
F9ED 803E120001 5410 DEC DH ; ANY MORE TO DO
F9F2 7502 5411 JNZ G1 ; DO IT
F9F4 EB03 5412 CMP MFG_TST,1 ; MFG TEST MODE?
F9F6 B301 5413 JNE G3 ; YES - CONTINUE BEEPING SPEAKER
F9F8 E80D00 5414 JMP MFG_HALT ; STOP BLINKING LED
F9FB 5415 G3: ; SHORT BEEP:
F9FD E2FE 5416 MOV BL,1 ; COUNTER FOR A SHORT BEEP
F9FF FECA 5417 CALL BEEP ; DO THE SOUND
FA01 75F5 5418 JNZ G3 ; DELAY BETWEEN BEEPS
FA01 E2FE 5419 G5: LOOP G5 ; LONG DELAY BEFORE RETURN
FA03 5420 DEC DL ; DONE WITH SHORTS
FA05 1F 5421 JNZ G3 ; DO SOME MORE
FA06 9D 5422 G6: LOOP G6
FA07 C3 5423 POP DS ; RESTORE ORIG CONTENTS OF DS
5424 POPF ; RESTORE FLAGS TO ORIG SETTINGS
5425 RET ; RETURN TO CALLER
5426 ERR_BEEP ENDP
5427
5428 ;----- ROUTINE TO SOUND BEEPER
5429 BEEP PROC NEAR
5430 MOV AL,10110110B ; SEL TIM 2,LSB,MSB,BINARY
5431 OUT TIMER+3,AL ; WRITE THE TIMER MODE REG
5432 MOV AX,533H ; DIVISOR FOR 1000 HZ
5433 OUT TIMER+2,AL ; WRITE TIMER 2 CNT - LSB
5434 MOV AL,AH
5435 OUT TIMER+2,AL ; WRITE TIMER 2 CNT - MSB
5436 IN AL,PORT_B ; GET CURRENT SETTING OF PORT
5437 MOV AH,AL ; SAVE THAT SETTINGH
5438 OR AL,03 ; TURN SPEAKER ON
5439 OUT PORT_B,AL
5440 SUB CX,CX ; SET CNT TO WAIT 500 MS
5441 G7:
5442 LOOP G7 ; DELAY BEFORE TURNING OFF
5443 DEC BL ; DELAY CNT EXPIRED?
5444 JNZ G7 ; NO - CONTINUE BEEPING SPK
5445 MOV AL,AH ; RECOVER VALUE OF PORT
5446 OUT PORT_B,AL
5447 RET ; RETURN TO CALLER
5448 BEEP ENDP
5449

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LOC OBJECT

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5454
5455 -----
5456 : THIS PROCEDURE WILL SEND A SOFTWARE RESET TO THE KEYBOARD.
5457 : SCAN CODE 'AA' SHOULD BE RETURNED TO THE CPU.
5458 -----
FA2A KBD_RESET PROC NEAR
5460 ASSUME DS:ABS0
FA2A B008 5461 MOV AL,08H ; SET KBD CLK LINE LOW
FA2C E661 5462 MOV PORT_B,AL ; WRITE 8255 PORT B
FA2E B95629 5463 OUT CX,10582 ; HOLD KBD CLK LOW FOR 20 MS
FA31 5464 G8:
FA31 E2FE 5465 LOOP G8 ; LOOP FOR 20 MS
FA33 B0CB 5466 MOV AL,0CBH ; SET CLK, ENABLE LINES HIGH
FA35 E661 5467 OUT PORT_B,AL
FA37 5468 SP_TEST:
FA37 B048 5469 MOV AL,4BH ; ENTRY FOR MANUFACTURING TEST 2
FA39 E661 5470 OUT PORT_B,AL ; SET KBD CLK HIGH, ENABLE LOW
FA3B B0FD 5471 MOV AL,0FDH ; ENABLE KEYBOARD INTERRUPTS
FA3D E621 5472 OUT INTA01,AL ; WRITE 8259 IMR
FA3F C6066B0400 5473 MOV DATA_AREA[OFFSET INTR_FLAG] ; RESET INTERRUPT INDICATOR
FA44 FB 5474 STI ; ENABLE INTERRUPTS
FA45 2BC9 5475 SUB CX,CX ; SETUP INTERRUPT TIMEOUT CNT
FA47 5476 G9:
FA47 F6066B0402 5477 TEST DATA_AREA[OFFSET INT_R_FLAG],02H ; DID A KEYBOARD INTERRU OCCUR?
FA4C 7502 5478 G10 JNZ ; YES READ SCAN CODE RETURNED
FA4E E2F7 5479 LOOP ; NO - LOOP TILL TIMEOUT
FA50 5480 G10:
FA50 E460 5481 IN AL,PORT_A ; READ KEYBOARD SCAN CODE
FA52 8A08 5482 MOV BL,AX ; SAVE SCAN CODE JUST READ
FA54 B0CB 5483 MOV AL,0CBH ; CLEAR KEYBOARD
FA56 E661 5484 OUT PORT_B,AL
FA58 C3 5485 RET ; RETURN TO CALLER
-----
KBD_RESET ENDP
-----
FA59 5488 DDS PROC NEAR
FA59 50 5489 AX ; SAVE AX
FA5A B84000 5490 MOV AX,DATA
FA5D 8BD8 5491 MOV DS,AX ; SET SEGMENT
FA5F 58 5492 POP AX ; RESTORE AX
FA60 C3 5493 RET
5494 DDS ENDP
5495
5496 -----
5497 : CHARACTER GENERATOR GRAPHICS FOR 320X200 AND 640X200 GRAPHICS :
5498 -----
FA6E 5499 ORG 0FA6EH
FA6E 5500 CRT_CHAR_GEN LABEL BYTE
FA6E 0000000000000000 5501 DB 000H,000H,030H,000H,000H,000H,000H,000H ; D_00
FA76 7E81A581BD99817E 5502 DB 07EH,081H,0A5H,081H,0BDH,099H,081H,07EH ; D_01
FA7E 7EFDFFBF3C7FFF7E 5503 DB 07EH,0FFH,0DBH,0FFH,0C3H,0E7H,0FFH,07EH ; D_02
FA86 6CFEFFF7C381000 5504 DB 06CH,0FEH,0FEH,0FEH,07CH,038H,010H,000H ; D_03
FA8E 10387CFE7C381000 5505 DB 010H,038H,07CH,0FEH,07CH,038H,010H,000H ; D_04
FA96 387C38FEF7C387C 5506 DB 038H,07CH,038H,0FEH,07CH,038H,07CH,07CH ; D_05
FA9E 1010387CFE7C387C 5507 DB 010H,010H,038H,07CH,0FEH,07CH,038H,07CH ; D_06
FAA6 0000183C3C180000 5508 DB 000H,000H,018H,03CH,03CH,018H,000H,000H ; D_07
FAAE FFFFE7C3C7FFFF 5509 DB 0FFH,0FFH,0FFH,0C3H,0C3H,0E7H,0FFH,07EH ; D_08
FAB6 003C664242663000 5510 DB 000H,03CH,066H,042H,042H,066H,03CH,000H ; D_09
FABE FFC3998BDB093CFF 5511 DB 0FFH,0C3H,099H,0BDH,0BDH,099H,0C3H,0FFH ; D_0A
FAC6 0F070F7DCCCCC78 5512 DB 0FFH,007H,00FH,07DH,0CCH,0CCH,0CCH,078H ; D_0B
FACC 3C6666663C187E18 5513 DB 03CH,066H,066H,066H,03CH,018H,07EH,018H ; D_0C
FAD6 3F333F303070F0E 5514 DB 03FH,033H,03FH,030H,030H,070H,0F0H,0E0H ; D_0D
FADE 7F637F63637E6C0 5515 DB 07FH,063H,07FH,063H,063H,067H,0E6H,0C0H ; D_0E
FAE6 995A3E71325A99 5516 DB 099H,05AH,03CH,0E7H,07H,03CH,05AH,099H ; D_0F
FAEE 80E0F8FF8E08000 5517 DB 080H,0E0H,0F8H,0FEH,0F8H,0E0H,080H,000H ; D_10
FAF6 020E3E3E3E0E0200 5518 DB 002H,00EH,03EH,0FEH,03EH,00EH,002H,000H ; D_11
FABE 183C7E18187E3C18 5519 DB 018H,03CH,07EH,018H,018H,07EH,03CH,018H ; D_12
FB06 6666666666006600 5520 DB 066H,066H,0FEH,066H,066H,000H,066H,000H ; D_13
FB0E 7FDBDB7B181B1800 5521 DB 07FH,0DBH,0DBH,07BH,018H,018H,018H,000H ; D_14
FB16 3E6386C6C38C78 5522 DB 03EH,063H,038H,06EH,06CH,06CH,038H,078H ; D_15
FB1E 000000007E7E7E00 5523 DB 000H,000H,000H,000H,07EH,07EH,07EH,000H ; D_16
FB26 183C7E187E3C18FF 5524 DB 018H,03CH,07EH,018H,07EH,03CH,018H,0FFH ; D_17
FB2E 183C7E1818181800 5525 DB 018H,03CH,07EH,018H,07EH,03CH,018H,000H ; D_18
FB36 181818187E3C1800 5526 DB 018H,018H,018H,018H,07EH,03CH,018H,000H ; D_19
FB3E 00180F0C180000 5527 DB 000H,018H,00CH,0FEH,00CH,018H,000H,000H ; D_1A
FB46 003060FE60300000 5528 DB 000H,030H,060H,0FEH,060H,030H,000H,000H ; D_1B
FB4E 0000C0C0C0FE0000 5529 DB 000H,000H,0C0H,0C0H,0C0H,0FEH,000H,000H ; D_1C
FB56 002466FF66240000 5530 DB 000H,024H,066H,0FFH,066H,024H,000H,000H ; D_1D
FB5E 00183C7E7FFF0000 5531 DB 000H,018H,03CH,07EH,0FFH,0FFH,000H,000H ; D_1E
FB66 0000000000000000 5532 DB 000H,0FFH,0FFH,07EH,03CH,018H,000H,000H ; D_1F
FB6E 0000000000000000 5533 DB 000H,000H,000H,000H,000H,000H,000H,000H ; SP_D_20
FB76 3078783000300000 5534 DB 030H,078H,078H,030H,030H,000H,030H,000H ; D_21
FB7E 6C6C6C0000000000 5535 DB 06CH,06CH,06CH,000H,000H,000H,000H ; D_22
FB86 6C6CFE6CFE6C6C00 5536 DB 06CH,06CH,0FEH,06CH,06CH,06CH,000H ; D_23
FB8E 307CC078CFC83000 5537 DB 030H,07CH,0C0H,078H,0C0H,0F8H,030H,000H ; D_24
FB96 00C6CC183066C600 5538 DB 000H,0C6H,0CCH,018H,030H,066H,0C6H,000H ; PER_CENT_D_25
FB9E 386C3876DC0C7600 5539 DB 038H,06CH,038H,0FEH,0DCCH,0CCH,078H,000H ; D_26
FBA6 6060C00000000000 5540 DB 060H,060H,0C0H,000H,000H,000H,000H ; D_27
FBAE 1830606060301800 5541 DB 018H,030H,060H,060H,060H,030H,018H,000H ; D_28
FBB6 6030181818306000 5542 DB 060H,030H,018H,018H,018H,030H,060H,000H ; D_29
FBBE 0063CFF3C6600000 5543 DB 006H,03CH,0FFH,0C6H,066H,03CH,000H,000H ; D_2A
FBC6 003030FC30300000 5544 DB 000H,030H,030H,0FCH,030H,030H,000H,000H ; D_2B
FBC6 000000000000303060 5545 DB 000H,000H,000H,000H,000H,030H,030H,060H ; D_2C
FBD6 000000FC00000000 5546 DB 000H,000H,000H,0FCH,000H,000H,000H,000H ; D_2D
FBD6 0000000000303000 5547 DB 000H,000H,000H,000H,000H,000H,000H,000H ; D_2E
FBE6 060C183060C08000 5548 DB 06EH,0CCH,018H,030H,060H,0CCH,080H,000H ; D_2F
FBE6 7CC6EDEF667C00 5549 DB 07CH,0C6H,06EH,06EH,06EH,06EH,07CH,000H ; D_30
FBF6 3070303030FC0000 5550 DB 030H,070H,030H,030H,030H,030H,0FCH,000H ; D_31
FBFE 78CC0C380CFFC700 5551 DB 078H,0CCH,0CCH,038H,0CCH,0CCH,0CCH,000H ; D_32
FC06 78CC0C380CFFC700 5552 DB 078H,0CCH,0CCH,038H,0CCH,0CCH,0CCH,078H,000H ; D_33
FC0E 1C3C6C0CFFC0E1E00 5553 DB 01CH,03CH,06CH,0CCH,0FEH,0CCH,01EH,000H ; D_34
FC16 FCC0F0C0C07800 5554 DB 0FCH,0CCH,0FCH,0FCH,0CCH,0CCH,0F8H,000H ; D_35
FC1E 386C0CFC8CC0C7800 5555 DB 038H,06CH,0CCH,0F8H,0CCH,03CH,078H,000H ; D_36
FC26 FCC0C1830303000 5556 DB 0FCH,0CCH,0CCH,018H,030H,030H,030H,000H ; D_37
FC2E 78CC0C78CC0C7800 5557 DB 078H,0CCH,0CCH,078H,0CCH,0CCH,078H,000H ; D_38
FC36 78CC0C780C187000 5558 DB 078H,0CCH,0CCH,07CH,0CCH,018H,070H,000H ; D_39
FC3E 003030000000303000 5559 DB 000H,030H,030H,000H,000H,030H,030H,000H ; D_40
FC46 0030300000003060 5560 DB 000H,030H,030H,000H,000H,030H,030H,060H ; D_41
FC4E 183060C060301800 5561 DB 018H,030H,060H,0CCH,060H,030H,018H,000H ; <D_3C
FC56 000F000F00C00000 5562 DB 000H,00FH,0FCH,000H,000H,0FCH,000H,000H ; <D_3D
FC5E 6030180C18306000 5563 DB 060H,030H,018H,0CCH,000H,030H,000H,000H ; D_42
FC66 78CC0C1830003000 5564 DB 078H,0CCH,0CCH,018H,030H,000H,030H,000H ; ?D_3F

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SECTION 5

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FC6E 7CC6DEDEDEC07800 5565 DB 07CH,0C6H,0DEH,0DEH,0C0H,07BH,000H * D_40
FC76 3078CCCCFC000000 5566 DB 030H,078H,0CCH,0CCH,0FCH,0CCH,0CCH,000H A D_41
FC7E FC66667C6666F000 5567 DB 0FCH,066H,066H,07CH,066H,066H,0FCH,000H B D_42
FC86 3C66C0C0C0C663C00 5568 DB 03CH,066H,0CCH,0CCH,0CCH,066H,03CH,000H C D_43
FC8E FB6C6666666CF800 5569 DB 0F8H,06CH,066H,066H,066H,06CH,0F8H,000H D D_44
FC96 FE262F786862FE00 5570 DB 0FEH,062H,068H,078H,068H,062H,0FEH,000H E D_45
FC9E FE262F786862FE00 5571 DB 0FEH,062H,068H,078H,068H,062H,0FEH,000H E D_45
FC9E FC66C0C0C663C000 5572 DB 03CH,066H,0CCH,0CCH,0CCH,066H,03CH,000H C D_43
FC9E FC66C0C0C663C000 5573 DB 0CCH,0CCH,0CCH,0CCH,0FCH,0CCH,0CCH,000H H D_48
FCB6 7830303030307800 5574 DB 078H,030H,030H,030H,030H,030H,078H,000H I D_49
FCB6 1E0C0C0C0C0C0C00 5575 DB 01EH,0C0H,0C0H,0C0H,0C0H,0C0H,01EH,000H J D_50
FCCE E6666C786C66E000 5576 DB 066H,066H,06CH,078H,06CH,066H,066H,000H D D_48
FCCE F06060606266FE00 5577 DB 0F0H,060H,060H,060H,062H,066H,0F0H,000H L D_4C
FCDE C6E6FEFED6C6C600 5578 DB 0C6H,06EH,0FEH,0FEH,06EH,0C6H,0C6H,000H M D_4D
FCDE C6E6FEFED6C6C600 5579 DB 0C6H,06EH,0FEH,0FEH,06EH,0C6H,0C6H,000H M D_4D
FCFE 386C6C6C6C6C63800 5580 DB 038H,06CH,0C6H,0C6H,0C6H,06CH,038H,000H O D_4F
FCFE FC66667C6060F000 5581 DB 0FCH,066H,066H,07CH,060H,060H,0FCH,000H P D_50
FCF4 78CCCC0C0C781C00 5582 DB 078H,0CCH,0CCH,0CCH,0DCH,078H,01CH,000H Q D_51
FCF6 FC66667C6C66E000 5583 DB 0C6H,066H,066H,07CH,066H,066H,066H,000H R D_52
FC0E 78CCE0701CC87800 5584 DB 078H,0CCH,0E0H,070H,01CH,0CCH,078H,000H S D_53
FC0E FC84303030307800 5585 DB 0FCH,0B4H,030H,030H,030H,030H,0FCH,000H T D_54
FD16 CCCCC0C0C0C0C000 5586 DB 0CCH,0CCH,0CCH,0CCH,0CCH,0CCH,0CCH,000H V D_56
FD1E CCCCC0C0C0C0C000 5587 DB 0CCH,0CCH,0CCH,0CCH,0CCH,078H,030H,000H V D_56
FD26 C6C6C6D6FEEEC600 5588 DB 0C6H,0C6H,0C6H,0D6H,0FEH,0EEH,0C6H,000H W D_57
FD26 C6C6C638386C6C00 5589 DB 0C6H,0C6H,06CH,038H,038H,06CH,0C6H,000H X D_58
FD36 CCCCC07830307800 5590 DB 0CCH,0CCH,0CCH,078H,030H,030H,078H,000H Y D_59
FD36 FEC68C183266FE00 5591 DB 0FEH,0C6H,08CH,018H,032H,066H,0FEH,000H Z D_5A
FD46 7860606060607800 5592 DB 078H,060H,060H,060H,060H,060H,078H,000H I D_5B
FD4E C060301800602000 5593 DB 0C0H,060H,030H,018H,0CCH,006H,0C0H,000H BACKSLASH D_5C
FD56 7818181818187800 5594 DB 078H,018H,018H,018H,018H,018H,078H,000H I D_5D
FD5E 10386C0000000000 5595 DB 010H,038H,06CH,0C6H,000H,000H,000H,000H CIRCUMFLEX D_5E
FD66 00000000000000FF 5596 DB 000H,000H,000H,000H,000H,000H,000H,000H D_5F
FD6E 3030180000000000 5597 DB 030H,030H,018H,000H,000H,000H,000H,000H T D_60
FD76 0000780C7CCC7600 5598 DB 000H,000H,078H,0CCH,0CCH,0CCH,078H,000H LOWER CASE A D_61
FD7E E060607C6666C000 5599 DB 0E0H,060H,060H,07CH,066H,066H,0DCH,000H L.C. B D_62
FD86 00078CCCC0C7800 5600 DB 000H,000H,078H,0CCH,0CCH,0CCH,078H,000H L.C. C D_63
FD8E 1C0C0C7CCCC7600 5601 DB 01CH,0C0H,0C0H,07CH,0CCH,0CCH,078H,000H L.C. D D_64
FD96 00078CFCFC07800 5602 DB 000H,000H,078H,0CCH,0FCH,0CCH,078H,000H L.C. E D_65
FD9E 386C60606060F000 5603 DB 038H,06CH,060H,0F0H,060H,060H,0F0H,000H L.C. F D_66
FDA6 00078CCCC70CF8 5604 DB 000H,000H,076H,0CCH,0CCH,07CH,0CCH,0F8H L.C. G D_67
FDAE E06060766666E000 5605 DB 0E0H,060H,066H,076H,066H,066H,066H,000H L.C. H D_68
FDB6 3000703030307800 5606 DB 030H,000H,070H,030H,030H,030H,078H,000H L.C. I D_69
FDBE 0C000C0C0C0C0C00 5607 DB 0CCH,000H,0CCH,0CCH,0CCH,0CCH,0CCH,078H L.C. J D_6A
FDC6 E0606066786C6E00 5608 DB 0E0H,060H,066H,06CH,078H,06CH,066H,000H L.C. K D_6B
FDC6 7030303030307800 5609 DB 070H,030H,0CCH,030H,030H,078H,000H L.C. L D_6C
FDD6 000C0CFEFD6C600 5610 DB 000H,000H,0CCH,0FEH,0FEH,0D6H,0C6H,000H L.C. M D_6D
FDE6 000F8CCCC0C00000 5611 DB 000H,000H,0F8H,0CCH,0CCH,0CCH,0CCH,000H L.C. N D_6E
FDE6 00078CCCC0C7800 5612 DB 000H,000H,078H,0CCH,0CCH,0CCH,078H,000H L.C. O D_6F
FDEE 000006667C60F0 5613 DB 000H,000H,078H,0CCH,0CCH,0CCH,060H,000H L.C. P D_70
FDF6 00078CCCC7C0C1E 5614 DB 000H,000H,076H,0CCH,0CCH,07CH,0CCH,01EH L.C. Q D_71
FDFE 0000DC76660F000 5615 DB 000H,000H,0DCH,076H,066H,060H,0F0H,000H L.C. R D_72
FDE6 0007TC0780CF800 5616 DB 000H,000H,07CH,0C0H,078H,06CH,0F8H,000H L.C. S D_73
FE0E 10307C0303041800 5617 DB 01CH,030H,030H,0CCH,038H,06CH,0C6H,000H L.C. T D_74
FE16 0000CCCC0C0C7600 5618 DB 000H,000H,0CCH,0CCH,0CCH,0CCH,076H,000H L.C. U D_75
FE1E 0000CCCC0C783000 5619 DB 000H,000H,0CCH,0CCH,0CCH,078H,030H,000H L.C. V D_76
FE26 0000C6D6FEEFC000 5620 DB 000H,000H,0C6H,0D6H,0FEH,0FEH,06CH,000H L.C. W D_77
FE2E 0000C6D6FEEFC000 5621 DB 000H,000H,0C6H,0C6H,038H,06CH,0C6H,000H L.C. X D_78
FE36 0000CCCC0C70CF8 5622 DB 000H,000H,0CCH,0CCH,0CCH,07CH,0CCH,0F8H L.C. Y D_79
FE3E 0000F983064FC00 5623 DB 000H,000H,0F9H,098H,030H,064H,0FCH,000H L.C. Z D_7A
FE46 1C3030E030301C00 5624 DB 01CH,030H,030H,0E0H,030H,030H,01CH,000H { D_7B
FE4E 1818180018181800 5625 DB 018H,018H,018H,000H,018H,018H,018H,000H { D_7C
FE56 E030301C3030E000 5626 DB 0E0H,030H,030H,01CH,030H,030H,0E0H,000H } D_7D
FE5E 76D0C00000000000 5627 DB 076H,0DCH,000H,000H,000H,000H,000H,000H TILDE D_7E
FE66 0010386C6C6CFE00 5628 DB 000H,010H,038H,06CH,0C6H,0C6H,0FEH,000H DELTA D_7F
5629
5630 ; --- INT 1A ---
5631 ; TIME_OF_DAY
5632 ; THIS ROUTINE ALLOWS THE CLOCK TO BE SET/READ
5633
5634 ; INPUT
5635 ; (AH) = 0 READ THE CURRENT CLOCK SETTING
5636 ; RETURNS CX = HIGH PORTION OF COUNT
5637 ; DX = LOW PORTION OF COUNT
5638 ; AL = 0 IF TIMER HAS NOT PASSED
5639 ; 24 HOURS SINCE LAST READ
5640 ; >= 1 IF ON ANOTHER DAY
5641 ; (AH) = 1 SET THE CURRENT CLOCK
5642 ; CX = HIGH PORTION OF COUNT
5643 ; DX = LOW PORTION OF COUNT
5644 ; NOTE: COUNTS OCCUR AT THE RATE OF
5645 ; 1193180/65536 COUNTS/SEC
5646 ; (OR ABOUT 18.2 PER SECOND -- SEE EQUATES BELOW)
5647
5648 ; --- ASSUME CS:CODE,DS:DATA ---
5649 TIME_OF_DAY PROC FAR
5650 ; INTERRUPTS BACK ON
5651 ; SAVE SEGMENT
5652 PUSH DS
5653 CALL DDS
5654 OR AH,AH
5655 JZ T2
5656 DEC AH
5657 JZ T3
5658 T1: STI
5659 ; SET TIME
5660 ; TOD RETURN
5661 POP DS
5662 IRET
5663 ; INTERRUPTS BACK ON
5664 ; RECOVER SEGMENT
5665 ; RETURN TO CALLER
5666 READ TIME
5667 T2: CLI
5668 ; NO TIMER INTERRUPTS WHILE READING
5669 MOV AL,TIMER_OFL
5670 MOV TIMER_OFL,0
5671 ; GET OVERFLOW, AND RESET THE FLAG
5672 CX,TIMER_HIGH
5673 MOV DX,TIMER_LOW
5674 JMP T1
5675 T3: TOD RETURN
5676 ; SET TIME
5677 MOV TIMER_HIGH,CX
5678 MOV TIMER_OFL,0
5679 ; RESET OVERFLOW
5680 JMP T1
5681 ; TOD RETURN
5682 TIME_OF_DAY ENDP

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5676
5677 ;-----
5678 ; THIS ROUTINE HANDLES THE TIMER INTERRUPT FROM ;
5679 ; CHANNEL 0 OF THE 8253 TIMER. INPUT FREQUENCY ;
5680 ; IS 1.19318 MHZ AND THE DIVISOR IS 65536, RESULTING ;
5681 ; IN APPROX. 18.2 INTERRUPTS EVERY SECOND. ;
5682 ;
5683 ; THE INTERRUPT HANDLER MAINTAINS A COUNT OF INTERRUPTS ;
5684 ; SINCE POWER ON TIME, WHICH MAY BE USED TO ESTABLISH ;
5685 ; TIME OF DAY. ;
5686 ; THE INTERRUPT HANDLER ALSO DECREMENTS THE MOTOR ;
5687 ; CONTROL COUNT OF THE DISKETTE, AND WHEN IT EXPIRES, ;
5688 ; WILL TURN OFF THE DISKETTE MOTOR, AND RESET THE ;
5689 ; MOTOR RUNNING FLAGS. ;
5690 ; THE INTERRUPT HANDLER WILL ALSO INVOKE A USER ROUTINE ;
5691 ; THROUGH INTERRUPT ICH AT EVERY TIME TICK. THE USER ;
5692 ; MUST CODE A ROUTINE AND PLACE THE CORRECT ADDRESS IN ;
5693 ; THE VECTOR TABLE. ;
5694 ;-----
FEA5          5695          ORG      0FEA5H
FEA5          5696          TIMER_INT  PROC      FAR
FEA5 FB       5697          STI          DS          ; INTERRUPTS BACK ON
FEA6 1E       5698          PUSH         DS
FEA7 50       5699          PUSH         AX
FEA8 52       5700          PUSH         DX          ; SAVE MACHINE STATE
FEA9 EBADF8   5701          CALL          DDS
FEAC FF06C00  5702          INC          TIMER_LOW ; INCREMENT TIME
FEB0 7504     5703          JNZ          T4          ; TEST_DAY
FEB2 FF06E00  5704          INC          TIMER_HIGH ; INCREMENT HIGH WORD OF TIME
FEB6          5705          T4:          ; TEST_DAY
FEB6 8336E0018 5706          CMP          TIMER_HIGH,018H ; TEST FOR COUNT EQUALING 24 HOURS
FEBB 7515     5707          JNZ          T5          ; DISKETTE_CTL
FEBD 813E6C00B000 5708          CMP          TIMER_LOW,0B0H
FEC3 750D     5709          JNZ          T5          ; DISKETTE_CTL
5710
5711 ;----- TIMER HAS GONE 24 HOURS
5712
FEC5 2BC0     5713          SUB          AX,AX
FEC7 A36E00   5714          MOV          TIMER_HIGH,AX
FECA A36C00   5715          MOV          TIMER_LOW,AX
FECD C06700001 5716          MOV          TIMER_OFL,1
5717
5718 ;----- TEST FOR DISKETTE TIME OUT
5719
FED2          5720          T5:          ; DISKETTE_CTL
FED2 FE0E4000 5721          DEC          MOTOR_COUNT
FED6 750B     5722          JNZ          T6          ; RETURN IF COUNT NOT OUT
FED8 80263F00F0 5723          AND          MOTOR_STATUS,0F0H ; TURN OFF MOTOR RUNNING BITS
FEDD B00C     5724          MOV          AL,0CH
FEDF BAF203   5725          MOV          DX,03F2H ; FDC_CTL_PORT
FEE2 EE       5726          OUT          DX,AL ; TURN OFF THE MOTOR
FEE3          5727          T6:          ; TIMER_RET:
FEE3 CD1C     5728          INT          1CH ; TRANSFER CONTROL TO A USER ROUTINE
FEE5 B020     5729          MOV          AL,E01
FEE7 E620     5730          OUT          020H,AL ; END OF INTERRUPT TO 8259
FEE9 5A       5731          POP          DX
FEEA 58       5732          POP          AX
FEEB 1F       5733          POP          DS          ; RESET MACHINE STATE
FEEC CF       5734          IRET          ; RETURN FROM INTERRUPT
5735          TIMER_INT  ENDP
5736

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5737 ;-----
5738 ; THESE ARE THE VECTORS WHICH ARE MOVED INTO      ;
5739 ; THE 3084 INTERRUPT AREA DURING POWER ON.      ;
5740 ; ONLY THE OFFSETS ARE DISPLAYED HERE, CODE     ;
5741 ; SEGMENT WILL BE ADDED FOR ALL OF THEM, EXCEPT ;
5742 ; WHERE NOTED.                                   ;
5743 ;-----
5744         ASSUME  CS:CODE
FEF3      ORG     0FEF3H
FEF4      VECTOR_TABLE LABEL WORD          ; VECTOR TABLE FOR MOVE TO INTERRUPTS
FEF5      DW     OFFSET TIMER_INT          ; INTERRUPT 8
FEF6      DW     OFFSET KB_INT            ; INTERRUPT 9
FEF7      DW     OFFSET DIT               ; INTERRUPT A
FEF8      DW     OFFSET DII              ; INTERRUPT B
FEF9      DW     OFFSET DIII             ; INTERRUPT C
FEFA      DW     OFFSET DII              ; INTERRUPT D
FEFB      DW     OFFSET DISK_INT         ; INTERRUPT E
FF00      DW     OFFSET DII              ; INTERRUPT F
FF01      DW     OFFSET VIDEO_IO         ; INTERRUPT 10H
FF02      DW     OFFSET EQUIPMENT        ; INTERRUPT 11H
FF03      DW     OFFSET MEMORY_SIZE_DET  ; INTERRUPT 12H
FF04      DW     OFFSET DISKETTE_IO      ; INTERRUPT 13H
FF05      DW     OFFSET RS232_IO         ; INTERRUPT 14H
FF06      DW     CASSETTE_IO            ; INTERRUPT 15H(FORMER CASSETTE IO)
FF07      DW     OFFSET KEYBOARD_IO      ; INTERRUPT 16H
FF08      DW     OFFSET PRINTER_IO       ; INTERRUPT 17H
FF09      DW     00000H                  ; INTERRUPT 18H
FF0A      DW     0F600H                  ; MUST BE INSERTED INTO TABLE LATER
FF0B      DW     00000H
FF0C      DW     00000H
FF0D      DW     00000H
FF0E      DW     00000H
FF0F      DW     00000H
FF10      DW     00000H
FF11      DW     00000H
FF12      DW     00000H
FF13      DW     00000H
FF14      DW     00000H
FF15      DW     00000H
FF16      DW     00000H
FF17      DW     00000H
FF18      DW     00000H
FF19      DW     00000H
FF1A      DW     00000H
FF1B      DW     00000H
FF1C      DW     00000H
FF1D      DW     00000H
FF1E      DW     00000H
FF1F      DW     00000H
FF20      DW     00000H
FF21      DW     00000H
FF22      DW     00000H
FF23      DW     00000H
FF24      DW     00000H
FF25      DW     00000H
FF26      DW     00000H
FF27      DW     00000H
FF28      DW     00000H
FF29      DW     00000H
FF2A      DW     00000H
FF2B      DW     00000H
FF2C      DW     00000H
FF2D      DW     00000H
FF2E      DW     00000H
FF2F      DW     00000H
FF30      DW     00000H
FF31      DW     00000H
FF32      DW     00000H
FF33      DW     00000H
FF34      DW     00000H
FF35      DW     00000H
FF36      DW     00000H
FF37      DW     00000H
FF38      DW     00000H
FF39      DW     00000H
FF3A      DW     00000H
FF3B      DW     00000H
FF3C      DW     00000H
FF3D      DW     00000H
FF3E      DW     00000H
FF3F      DW     00000H
FF40      DW     00000H
FF41      DW     00000H
FF42      DW     00000H
FF43      DW     00000H
FF44      DW     00000H
FF45      DW     00000H
FF46      DW     00000H
FF47      DW     00000H
FF48      DW     00000H
FF49      DW     00000H
FF4A      DW     00000H
FF4B      DW     00000H
FF4C      DW     00000H
FF4D      DW     00000H
FF4E      DW     00000H
FF4F      DW     00000H
FF50      DW     00000H
FF51      DW     00000H
FF52      DW     00000H
FF53      DW     00000H
FF54      DW     00000H
FF55      DW     00000H
FF56      DW     00000H
FF57      DW     00000H
FF58      DW     00000H
FF59      DW     00000H
FF5A      DW     00000H
FF5B      DW     00000H
FF5C      DW     00000H
FF5D      DW     00000H
FF5E      DW     00000H
FF5F      DW     00000H
FF60      DW     00000H
FF61      DW     00000H
FF62      DW     00000H
FF63      DW     00000H
FF64      DW     00000H
FF65      DW     00000H
FF66      DW     00000H
FF67      DW     00000H
FF68      DW     00000H
FF69      DW     00000H
FF6A      DW     00000H
FF6B      DW     00000H
FF6C      DW     00000H
FF6D      DW     00000H
FF6E      DW     00000H
FF6F      DW     00000H
FF70      DW     00000H
FF71      DW     00000H
FF72      DW     00000H
FF73      DW     00000H
FF74      DW     00000H
FF75      DW     00000H
FF76      DW     00000H
FF77      DW     00000H
FF78      DW     00000H
FF79      DW     00000H
FF7A      DW     00000H
FF7B      DW     00000H
FF7C      DW     00000H
FF7D      DW     00000H
FF7E      DW     00000H
FF7F      DW     00000H
FF80      DW     00000H
FF81      DW     00000H
FF82      DW     00000H
FF83      DW     00000H
FF84      DW     00000H
FF85      DW     00000H
FF86      DW     00000H
FF87      DW     00000H
FF88      DW     00000H
FF89      DW     00000H
FF8A      DW     00000H
FF8B      DW     00000H
FF8C      DW     00000H
FF8D      DW     00000H
FF8E      DW     00000H
FF8F      DW     00000H
FF90      DW     00000H
FF91      DW     00000H
FF92      DW     00000H
FF93      DW     00000H
FF94      DW     00000H
FF95      DW     00000H
FF96      DW     00000H
FF97      DW     00000H
FF98      DW     00000H
FF99      DW     00000H
FF9A      DW     00000H
FF9B      DW     00000H
FF9C      DW     00000H
FF9D      DW     00000H
FF9E      DW     00000H
FF9F      DW     00000H
FFA0      DW     00000H
FFA1      DW     00000H
FFA2      DW     00000H
FFA3      DW     00000H
FFA4      DW     00000H
FFA5      DW     00000H
FFA6      DW     00000H
FFA7      DW     00000H
FFA8      DW     00000H
FFA9      DW     00000H
FFAA      DW     00000H
FFAB      DW     00000H
FFAC      DW     00000H
FFAD      DW     00000H
FFAE      DW     00000H
FFAF      DW     00000H
FFB0      DW     00000H
FFB1      DW     00000H
FFB2      DW     00000H
FFB3      DW     00000H
FFB4      DW     00000H
FFB5      DW     00000H
FFB6      DW     00000H
FFB7      DW     00000H
FFB8      DW     00000H
FFB9      DW     00000H
FFBA      DW     00000H
FFBB      DW     00000H
FFBC      DW     00000H
FFBD      DW     00000H
FFBE      DW     00000H
FFBF      DW     00000H
FFC0      DW     00000H
FFC1      DW     00000H
FFC2      DW     00000H
FFC3      DW     00000H
FFC4      DW     00000H
FFC5      DW     00000H
FFC6      DW     00000H
FFC7      DW     00000H
FFC8      DW     00000H
FFC9      DW     00000H
FFCA      DW     00000H
FFCB      DW     00000H
FFCC      DW     00000H
FFCD      DW     00000H
FFCE      DW     00000H
FFCF      DW     00000H
FFD0      DW     00000H
FFD1      DW     00000H
FFD2      DW     00000H
FFD3      DW     00000H
FFD4      DW     00000H
FFD5      DW     00000H
FFD6      DW     00000H
FFD7      DW     00000H
FFD8      DW     00000H
FFD9      DW     00000H
FFDA      DW     00000H
FFDB      DW     00000H
FFDC      DW     00000H
FFDD      DW     00000H
FFDE      DW     00000H
FFDF      DW     00000H
FFE0      DW     00000H
FFE1      DW     00000H
FFE2      DW     00000H
FFE3      DW     00000H
FFE4      DW     00000H
FFE5      DW     00000H
FFE6      DW     00000H
FFE7      DW     00000H
FFE8      DW     00000H
FFE9      DW     00000H
FFEA      DW     00000H
FFEB      DW     00000H
FFEC      DW     00000H
FFED      DW     00000H
FFEE      DW     00000H
FFEF      DW     00000H
FFF0      DW     00000H
FFF1      DW     00000H
FFF2      DW     00000H
FFF3      DW     00000H
FFF4      DW     00000H
FFF5      DW     00000H
FFF6      DW     00000H
FFF7      DW     00000H
FFF8      DW     00000H
FFF9      DW     00000H
FFFA      DW     00000H
FFFB      DW     00000H
FFFC      DW     00000H
FFFD      DW     00000H
FFFE      DW     00000H
FFFF      DW     00000H

```

```

5821 :-- INT 5
5822 : THIS LOGIC WILL BE INVOKED BY INTERRUPT 05H TO PRINT THE
5823 : SCREEN. THE CURSOR POSITION AT THE TIME THIS ROUTINE IS INVOKED
5824 : WILL BE SAVED AND RESTORED UPON COMPLETION. THE ROUTINE IS
5825 : INTENDED TO RUN WITH INTERRUPTS ENABLED. IF A SUBSEQUENT
5826 : 'PRINT SCREEN' KEY IS DEPRESSED DURING THE TIME THIS ROUTINE
5827 : IS PRINTING IT WILL BE IGNORED.
5828 : ADDRESS 50:0 CONTAINS THE STATUS OF THE PRINT SCREEN:
5829 :
5830 : 50:0 =0 EITHER PRINT SCREEN HAS NOT BEEN CALLED
5831 : OR UPON RETURN FROM A CALL THIS INDICATES
5832 : A SUCCESSFUL OPERATION.
5833 : =1 PRINT SCREEN IS IN PROGRESS
5834 : =255 ERROR ENCOUNTERED DURING PRINTING
-----
FF54 5836 ASSUME CS:CODE,DS:XXDATA
FF54 5837 ORG OFF54AH
FF54 FB 5838 PRINT_SCREEN PROC FAR
FF54 5839 STI
FF54 5840 PUSH DS ; MUST RUN WITH INTERRUPTS ENABLED
FF54 5841 PUSH AX ; MUST USE 50:0 FOR DATA AREA STORAGE
FF54 5842 PUSH BX
FF54 5843 PUSH CX ; WILL USE THIS LATER FOR CURSOR LIMITS
FF54 5844 PUSH DX ; WILL HOLD CURRENT CURSOR POSITION
FF54 B85000 5845 MOV AX,XXDATA ; HEX 50
FF54 8ED8 5846 MOV DS,AX
FF54 803E0000 5847 CMP STATUS_BYTE,1 ; SEE IF PRINT ALREADY IN PROGRESS
FF54 745F 5848 JZ EXIT ; JUMP IF PRINT ALREADY IN PROGRESS
FF54 C6060000 5849 MOV STATUS_BYTE,1 ; INDICATE PRINT NOW IN PROGRESS
FF68 B40F 5850 MOV AH,15 ; WILL REQUEST THE CURRENT SCREEN MODE
FF6D CD10 5851 INT 10H ; [AH]=MODE
5852 ; [BH]=NUMBER COLUMNS/LINE
5853 ; [BH]=VISUAL PAGE
-----
5854 :
5855 : AT THIS POINT WE KNOW THE COLUMNS/LINE ARE IN
5856 : [AX] AND THE PAGE IF APPLICABLE IS IN[BH] THE STACK
5857 : HAS DS,AX,BX,CX,DX PUSHED. [A] HAS VIDEO MODE
-----
FF6F BACC 5858 MOV CL,AH ; WILL MAKE USE OF [CX] REGISTER TO
FF71 B519 5859 MOV CH,25 ; CONTROL ROW & COLUMNS
FF73 E85500 5861 CALL CRLF ; CARRIAGE RETURN LINE FEED ROUTINE
FF76 51 5862 PUSH CX ; SAVE SCREEN BOUNDS
FF77 B403 5863 MOV AH,3 ; WILL NOW READ THE CURSOR.
FF79 CD10 5864 INT 10H ; AND PRESERVE THE POSITION
FF7B 59 5865 POP CX ; RECALL SCREEN BOUNDS
FF7C 52 5866 PUSH DX ; RECALL [BH]=VISUAL PAGE
FF7D 33D2 5867 XOR DX,DX ; WILL SET CURSOR POSITION TO [0,0]
-----
5868 :
5869 : THE LOOP FROM PR110 TO THE INSTRUCTION PRIOR TO PR120
5870 : IS THE LOOP TO READ EACH CURSOR POSITION FROM THE
5871 : SCREEN AND PRINT.
-----
FF7F B402 5873 PR110: MOV AH,2 ; TO INDICATE CURSOR SET REQUEST
FF81 CD10 5874 INT 10H ; NEW CURSOR POSITION ESTABLISHED
FF83 B408 5876 MOV AH,8 ; TO INDICATE READ CHARACTER
FF85 CD10 5877 INT 10H ; CHARACTER NOW IN [AL]
FF87 0AC0 5878 OR AL,AL ; SEE IF VALID CHAR
FF89 7502 5879 JNZ PR115 ; JUMP IF VALID CHAR
FF8B B020 5880 MOV AL,' ' ; MAKE A BLANK
FF8D 52 5881 PR115: PUSH DX ; SAVE CURSOR POSITION
FF8E 33D2 5883 XOR DX,DX ; INDICATE PRINTER 1
FF90 32E4 5884 XOR AH,AH ; TO INDICATE PRINT CHAR IN [AL]
FF92 CD17 5885 INT 17H ; PRINT THE CHARACTER
FF94 5A 5886 POP DX ; RECALL CURSOR POSITION
FF95 F6C425 5887 TEST AH,25H ; TEST FOR PRINTER ERROR
FF98 7521 5888 JNZ ERR10 ; JUMP IF ERROR DETECTED
FF9A FEC2 5889 INC DL ; ADVANCE TO NEXT COLUMN
FF9C 3ACA 5890 CMP CL,DL ; SEE IF AT END OF LINE
FF9E 75DF 5891 JNZ PR110 ; IF NOT PROCEED
FFA0 32D2 5892 XOR DL,DL ; BACK TO COLUMN 0
FFA2 8AE2 5893 MOV AH,DL ; [AH]=0
FFA4 52 5894 PUSH DX ; SAVE NEW CURSOR POSITION
FFA5 E82300 5895 CALL CRLF ; LINE FEED CARRIAGE RETURN
FFA8 5A 5896 POP DX ; RECALL CURSOR POSITION
FFA9 FEC6 5897 INC DH ; ADVANCE TO NEXT LINE
FFAB 3AEE 5898 CMP CH,DH ; FINISHED?
FFAD 75D0 5899 JNZ PR110 ; IF NOT CONTINUE
FFAF 5A 5900 PR120: POP DX ; RECALL CURSOR POSITION
FFB0 B402 5901 MOV AH,2 ; TO INDICATE CURSOR SET REQUEST
FFB2 CD10 5903 INT 10H ; CURSOR POSITION RESTORED
FFB4 C6060000 5904 MOV STATUS_BYTE,0 ; INDICATE FINISHED
FFB9 E80A 5905 JMP SHORT EXIT ; EXIT THE ROUTINE
FFBB 5A 5906 ERR10: POP DX ; GET CURSOR POSITION
FFBB 5A 5907 MOV AH,2 ; TO REQUEST CURSOR SET
FFBE CD10 5909 INT 10H ; CURSOR POSITION RESTORED
FFC0 5910 ERR20: MOV STATUS_BYTE,0FFH ; INDICATE ERROR
FFC5 C6060000FF 5911 EXIT: MOV STATUS_BYTE,0FFH ; INDICATE ERROR
FFC5 5A 5913 POP DX ; RESTORE ALL THE REGISTERS USED
FFC6 59 5914 POP CX
FFC7 5B 5915 POP BX
FFC8 58 5916 POP AX
FFC9 1F 5917 POP DS
FFCA CF 5918 IRET
5919 PRINT_SCREEN ENDP
5920
5921 :----- CARRIAGE RETURN, LINE FEED SUBROUTINE
5922
FFCB 5923 CRLF PROC NEAR
FFCB 33D2 5924 DX,DX ; PRINTER 0
FFCD 32E4 5925 XOR AH,AH ; WILL NOW SEND INITIAL LF,CR
5926 ; TO PRINTER
FFCF B00A 5927 MOV AL,120 ; LF
FFD1 CD17 5928 INT 17H ; SEND THE LINE FEED
FFD3 32E4 5929 XOR AH,AH ; NOW FOR THE CR
FFD5 B00D 5930 MOV AL,150 ; CR
FFD7 CD17 5931 INT 17H ; SEND THE CARRIAGE RETURN
FFD9 C3 5932 RET
5933 CRLF ENDP

```

SECTION 5

LOC OBJECT

LINE SOURCE (BIOS FOR THE IBM PERSONAL COMPUTER XT) 11/08/82

```

5934
5935 ;-----
5936 ; PRINT A SEGMENT VALUE TO LOOK LIKE A 20 BIT ADDRESS ;
5937 ; DX MUST CONTAIN SEGMENT VALUE TO BE PRINTED ;
5938 ;-----
FFDA      5939 PRT_SEG PROC    NEAR
FFDA 8AC6 5940     MOV     AL,DIH
FFDC EBACF9 5941     CALL    XPC_BYTE ;GET MSB
FFDF 8AC2 5942     MOV     AL,DL ;LSB
FFE1 E8A7F9 5943     CALL    XPC_BYTE
FFE4 B030 5944     MOV     AL,'0' ; PRINT A '0 '
FFE6 EBB3F9 5945     CALL    PRT_HEX
FFE9 B020 5946     MOV     AL,' ' ;SPACE
FFEB E8AEF9 5947     CALL    PRT_HEX
FFEE C3 5948     RET
          5949 PRT_SEG ENDP
          5950
----      5951 CODE    ENDS
          5952
          5953 ;-----
          5954 ; POWER ON RESET VECTOR ;
          5955 ;-----
----      5956 VECTOR SEGMENT AT 0FFFFH
          5957
          5958 ;---- POWER ON RESET
          5959
0000 EA5BE00F0 5960     JMP     RESET
          5961
0005 31312F30382F38 5962     DB     '11/08/82' ; RELEASE MARKER
          32
----      5963 VECTOR ENDS
          5964     END

```

SECTION 6. INSTRUCTION SET

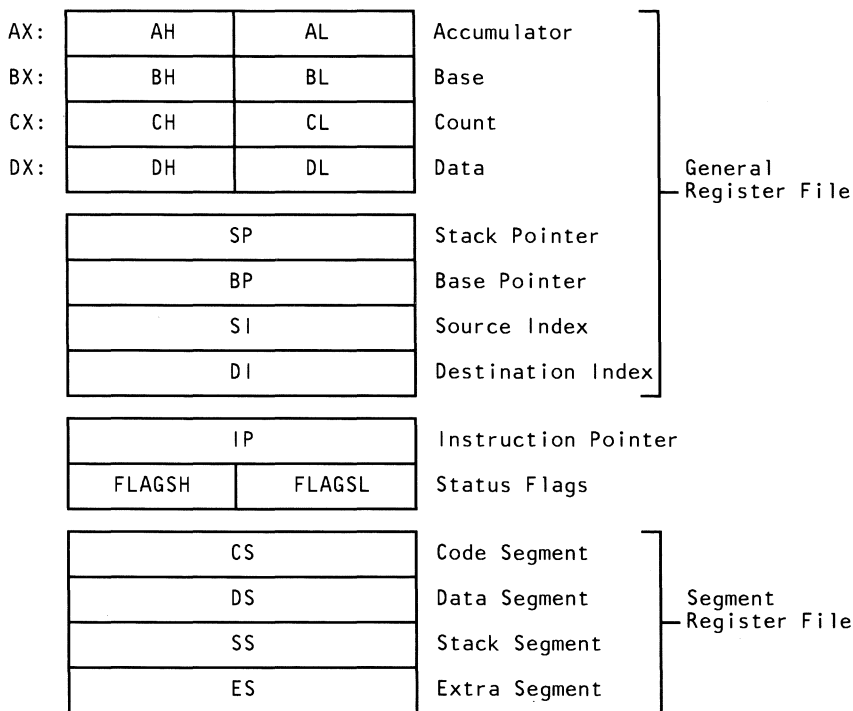
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Notes:

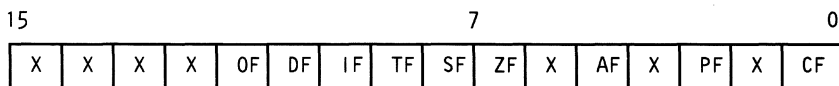
8088 Register Model

Notes:

if d = 1 then "to"; if d = 0 then "from"
 if w = 1 then word instruction; if w = 0 then byte instruction
 if s:w = 01 then 16 bits of immediate data from the operand
 if s:w = 11 then an immediate data byte is signed extended to form the 16-bit operand
 if v = 0 the "count" = 1; if v = 1 the "count" is in (CL) or (CX)
 x = don't care
 z is used for string primitives for comparison with ZF FLAG
 AL = 8-bit accumulator
 AX = 16-bit accumulator
 CX = Count register
 DS = Data segment
 ES = Extra segment
 Above/below refers to unsigned value
 Greater = more positive;
 Less = less positive (more negative) signed values



Instructions which reference the flag register file as a 16-bit object, use the symbol FLAGS to represent the file:



X = Don't Care

AF: Auxiliary Carry - BCD	}	8080 Flags
CF: Carry Flag		
PF: Parity Flag		
SF: Sign Flag		
ZF: Zero Flag		
DF: Direction Flag	}	8088 Flags
IF: Interrupt Enable Flag		
OF: Overflow Flag (CF + SF)		
TF: Trap-Single Step Flag		

Operand Summary

reg Field Bit Assignments

16-Bit [w = 1]	8-Bit [w = 0]	Segment
000 AX	000 AL	00 ES
001 CX	001 CL	01 CS
010 DX	010 DL	10 SS
011 BX	011 BL	11 DS
100 SP	100 AH	
101 BP	101 CH	
110 SI	110 DH	
111 DI	111 BH	

Second Instruction Byte Summary

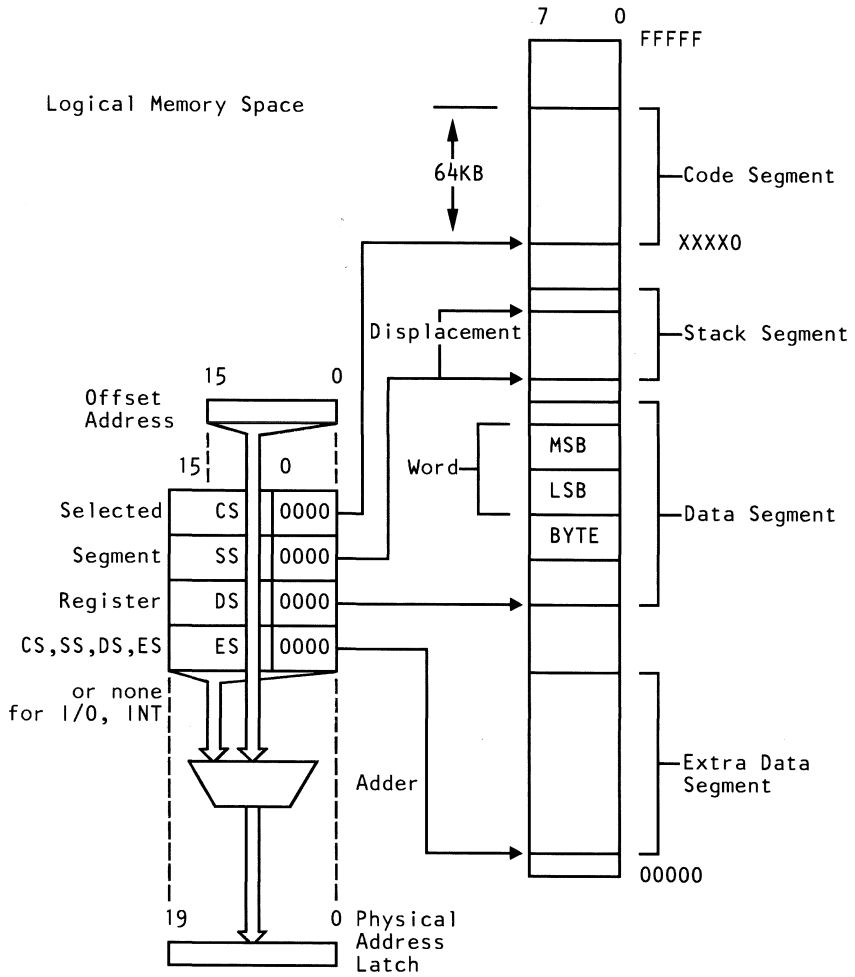
mod	xxx	r/m
-----	-----	-----

mod | Displacement

00	DISP = 0*, disp-low and disp-high are absent
01	DISP = disp-low sign-extended to 16-bits, disp-high is absent
10	DISP = disp-high: disp-low
11	r/m is treated as a "reg" field

DISP follows 2nd byte of instruction (before data if required)
 *except if mod=00 and r/m=110 then EA=disp-high: disp-low.

Memory Segmentation Model



Segment Override Prefix

001reg110

Use of Segment Override

Operand Register	Default	With Override Prefix
IP (Code Address)	CS	Never
SP (Stack Address)	SS	Never
BP (Stack Address or Stack Marker)	SS	BP + DS or ES, or CS
SI or DI (not including strings)	DS	ES, SS, or CS
SI (Implicit Source Address for strings)	DS	ES, SS, or CS
DI (Implicit Destination Address for strings)	ES	Never

8088 Instruction Set

Data Transfer

MOV = Move

Register/Memory to/from Register

100010dw	mod reg r/m
----------	-------------

Immediate to Register/Memory

1100011w	mod 000 r/m	data	data if w = 1
----------	-------------	------	---------------

Immediate to Register

1011wreg	data	data if w = 1
----------	------	---------------

Memory to Accumulator

1010000w	addr-low	addr-high
----------	----------	-----------

Accumulator to Memory

1010001w	addr-low	addr-high
----------	----------	-----------

Register/Memory to Segment Register

10001110	mod 0 reg r/m
----------	---------------

Segment Register to Register/Memory

10001100	mod 0 reg r/m
----------	---------------

PUSH = Push

Register/Memory

11111111	mod 110 r/m
----------	-------------

Register

01010 reg

Segment Register

000 reg 110

POP = Pop

Register/Memory

10001111	mod 000 r/m
----------	-------------

Register

01011reg

Segment Register

000 reg 111

XCHG = Exchange

Register/Memory with Register

1000011w	mod reg r/m
----------	-------------

Register with Accumulator

10010reg

IN = Input to AL/AX from

Fixed Port

1110010w	port
----------	------

Variable Port

1110110w

OUT = Output from AL/AX to

Fixed Port

1110011w	port
----------	------

Variable Port (DX)

1110110w

XLAT = Translate Byte to AL

11010111

LEA = Load EA to Register

10001101	mod reg r/m
----------	-------------

LDS = Load Pointer to DS

11000101	mod reg r/m
----------	-------------

LES = Load Pointer to ES

11000100	mod reg r/m
----------	-------------

LAHF = Load AH with Flags

10011111

SAHF = Store AH with Flags

10011110

PUSHF = Push Flags

10011100

POPF = Pop Flags

10011101

Arithmetic

ADD = Add

Register/Memory with Register to Either

000000dw	mod reg r/m
----------	-------------

Immediate to Register Memory

100000sw	mod 000 r/m	data	data if s:w = 01
----------	-------------	------	------------------

Immediate to Accumulator

0000010w	data	data if w = 1
----------	------	---------------

ADC = Add with Carry

Register/Memory with Register to Either

000100dw	mod reg r/m
----------	-------------

Immediate to Register/Memory

100000sw	mod 010 r/m	data	data if s:w = 01
----------	-------------	------	------------------

Immediate to Accumulator

0001010w	data	data if w = 1
----------	------	---------------

INC = Increment

Register/Memory

1111111w	mod 000 r/m
----------	-------------

Register

01000reg

AAA = ASCII Adjust for Add

00110111

DAA = Decimal Adjust for Add

00100111

SUB = Subtract

Register/Memory and Register to Either

001010dw	mod reg r/m
----------	-------------

Immediate from Register/Memory

100000sw	mod 101 r/m	data	data if s:w = 01
----------	-------------	------	------------------

Immediate from Accumulator

0010110w	data	data if w = 1
----------	------	---------------

SBB = Subtract with Borrow

Register/Memory and Register to Either

000110dw	mod reg r/m
----------	-------------

Immediate from Register/Memory

100000sw	mod 011 r/m	data	data if s:w = 01
----------	-------------	------	------------------

Immediate to Accumulator

0001110w	data	data if w = 1
----------	------	---------------

DEC = Decrement

Register/Memory

1111111w	mod 001 r/m
----------	-------------

Register

01001reg

NEG = Change Sign

1111011w	mod 011 r/m
----------	-------------

CMP = Compare

Register/Memory and Register

001110dw	mod reg r/m
----------	-------------

Immediate with Register/Memory

100000sw	mod 111 r/m	data	data if s:w = 01
----------	-------------	------	------------------

Immediate with Accumulator

0011110w	data	data if w = 1
----------	------	---------------

AAS = ASCII Adjust for Subtract

00111111

DAS = Decimal Adjust for Subtract

00101111

MUL = Multiply (Unsigned)

1111011w	mod 100 r/m
----------	-------------

IMUL = Integer Multiply (Signed)

1111011w	mod 101 r/m
----------	-------------

AAM = ASCII Adjust for Multiply

11010100	00001010
----------	----------

DIV = Divide (Unsigned)

1111011w	mod 110 r/m
----------	-------------

IDIV = Integer Divide (Signed)

1111011w	mod 111 r/m
----------	-------------

AAD = ASCII Adjust for Divide

11010101	00001010
----------	----------

CBW = Convert Byte to Word

10011000

CWD = Convert Word to Double Word

10011001

Logic

Shift/Rotate Instructions

NOT = Invert Register/Memory

1111011w	mod 010 r/m
----------	-------------

SHL/SAL = Shift Logical/Arithmetic Left

110100vw	mod 100 r/m
----------	-------------

SHR = Shift Logical Right

110100vw	mod 101 r/m
----------	-------------

SAR = Shift Arithmetic Right

110100vw	mod 111 r/m
----------	-------------

ROL = Rotate Left

110100vw	mod 000 r/m
----------	-------------

ROR = Rotate Right

110100vw	mod 001 r/m
----------	-------------

RCL = Rotate through Carry Left

110100vw	mod 010 r/m
----------	-------------

RCR = Rotate through Carry Right

110100vw	mod 011 r/m
----------	-------------

AND = And

Register/Memory and Register to Either

001000dw	mod reg r/m
----------	-------------

Immediate to Register/Memory

1000000w	mod 100 r/m	data	data if w = 1
----------	-------------	------	---------------

Immediate to Accumulator

0010010w	data	data if w = 1
----------	------	---------------

TEST = AND Function to Flags; No Result

Register/Memory and Register

1000010w	mod reg r/m
----------	-------------

Immediate Data and Register/Memory

1111011w	mod 000 r/m	data	data if w = 1
----------	-------------	------	---------------

Immediate Data and Accumulator

1010100w	data	data if w = 1
----------	------	---------------

OR = Or

Register/Memory and Register to Either

000010dw	mod reg r/m
----------	-------------

Immediate to Register/Memory

1000000w	mod 001 r/m	data	data if w = 1
----------	-------------	------	---------------

Immediate to Accumulator

0000110w	data	data if w = 1
----------	------	---------------

XOR = Exclusive OR

Register/Memory and Register to Either

001100dw	mod reg r/m
----------	-------------

Immediate to Register/Memory

1000000w	mod 110 r/m	data	data if w = 1
----------	-------------	------	---------------

Immediate to Accumulator

0011010w	data	data if w = 1
----------	------	---------------

String Manipulation

REP = Repeat

1111001z

MOVS = Move String

1010010w

CMPS = Compare String

1010011w

SCAS = Scan String

1010111w

LODS = Load String

1010110w

STOS = Store String

1010101w

Control Transfer

CALL = Call

Direct within Segment

11101000	disp-low	disp-high
----------	----------	-----------

Indirect within Segment

11111111	mod 010 r/m
----------	-------------

Direct Intersegment

10011010	offset-low	offset-high
----------	------------	-------------

seg-low	seg-high
---------	----------

Indirect Intersegment

11111111	mod 011 r/m
----------	-------------

JMP = Unconditional Jump

Direct within Segment-Short

11101011	disp
----------	------

Indirect within Segment

11111111	mod 100 r/m
----------	-------------

Direct Intersegment

11101010	offset-low	offset-high
----------	------------	-------------

seg-low	seg-high
---------	----------

Indirect Intersegment

11111111	mod 101 r/m
----------	-------------

RET = Return from Call

Within Segment

1100011

Within Segment Adding Immediate to SP

1100010	data-low	data-high
---------	----------	-----------

Intersegment

11001011

Intersegment Adding Immediate to SP

1100010	data-low	data-high
---------	----------	-----------

JE/JZ = Jump on Equal/Zero

01110100	disp
----------	------

JL/JNGE = Jump on Less/Not Greater, or Equal

01111100	disp
----------	------

JLE/JNG = Jump on Less, or Equal/Not Greater

01111110	disp
----------	------

JB/JNAE = Jump on Below/Not Above, or Equal

01110010	disp
----------	------

JBE/JNA = Jump on Below, or Equal/Not Above

01110110	disp
----------	------

JP/JPE = Jump on Parity/Parity Even

01111010	disp
----------	------

JO = Jump on Overflow

01110000	disp
----------	------

JS = Jump on Sign

01111000	disp
----------	------

JNE/JNZ = Jump on Not Equal/Not Zero

01110101	disp
----------	------

JNL/JGE = Jump on Not Less/Greater, or Equal

01111101	disp
----------	------

JNLE/JG = Jump on Not Less, or Equal/Greater

01111111	disp
----------	------

JNB/JAE = Jump on Not Below/Above, or Equal

01110011	disp
----------	------

JNBE/JA = Jump on Not Below, or Equal/Above

01110111	disp
----------	------

JNP/JPO = Jump on Not Parity/Parity Odd

01111011	disp
----------	------

JNO = Jump on Not Overflow

01110001	disp
----------	------

JNS = Jump on Not Sign

01111001	disp
----------	------

LOOP = Loop CX Times

11100010	disp
----------	------

LOOPZ/LOOPE = Loop while Zero/Equal

11100001	disp
----------	------

LOOPNZ/LOOPNE = Loop while Not Zero/Not Equal

11100000	disp
----------	------

JCXZ = Jump on CX Zero

11100011	disp
----------	------

8088 Instruction Set Matrix

	0	1	2	3	4	5	6	7
HI 0	ADD b,b,r/m	ADD w,f,r/m	ADD b,t,r/m	ADD w,t,r/m	ADD b,ia	ADD w,ia	PUSH ES	POP ES
1	ADC b,f,r/m	ADC w,f,r/m	ADC b,t,r/m	ADC w,t,r/m	ADC b,i	ADC w,i	PUSH SS	POP SS
2	AND b,f,r/m	AND w,f,r/m	AND b,t,r/m	AND w,t,r/m	AND b,i	AND w,i	DEG =ES	DAA
3	XOR b,f,r/m	XOR w,f,r/m	XOR b,t,r/m	XOR w,t,r/m	XOR b,i	XOR w,i	SEG =S+	AAA
4	INC AX	INC CX	INC DX	INC BX	INC SP	INC BP	INC SI	INC DI
5	PUSH AX	PUSH CX	PUSH DX	PUSH BX	PUSH SP	PUSH BP	PUSH SI	PUSH DI
6								
7	JO	JNO	JB/ JNAE	JNB/ JAE	JE/ JZ	JNE/ JNZ	JBE/ JNA	JNBE/ JA
8	Immed b,r/m	Immed w,r/m	Immed b,r/m	Immed is,r/m	TEST b,r/m	TEST w,r/m	XCHG b,r/m	XCHG w,r/m
9	NOP	XCHG CX	XCHG DX	XCHG BX	XCHG SP	XCHG BP	XCHG SI	XCHG DI
A	MOV m AL	MOV m AL	MOV AL m	MOV AL m	MOV b	MOV w	CMPS b	CMPS w
B	MOV i AL	MOV i CL	MOV i DL	MOV i BL	MOV i AH	MOV i CH	MOV i DH	MOV i BH
C			RET (I+SP)	RET	LES	LDS	MOV b,i,r/m	MOV w,i,r/m
D	Shift b	Shift w	Shift b,v	Shift w,v	AAM	AAD		XLAT
E	LOOPNZ/ LOOPNE	LOOPZ/ LOOPPE	LOOP	JCXZ	IN b	IN w	OUT b	OUT w
F	LOCK		REP	REP z	HLT	CMC	Grp 1 b,r/m	Grp 1 w,r/m

b = byte operation

d = direct

f = from CPU reg

i = immediate

ia = immed. to accum.

id = direct

is = immed. byte, sign ext.

l = long ie. intersegment

m = memory

r/m = EA is second byte

si = short intersegment

t = to CPU reg

v = variable

w = word operation

z = zero

sr = segment register

	8	9	A	B	C	D	E	F
HI 0	OR b,f,r/m	w,f,r/m	OR b,t,r/m	OR w,t,r/m	OR b,i	OR w,i	PUSH CS	
1	SBB b,f,r/m	SBB w,f,r/m	SBB b,t,r/m	SBB w,t,r/m	SBB b,i	SBB w,i	PUSH DS	POP DS
2	SUB b,f,r/m	SUB w,f,r/m	SUB b,t,r/m	SUB w,t,r/m	SUB b,i	SUB w,i	SEG= CS	DAS
3	CMP b,f,r/m	CMP w,f,r/m	CMP b,t,r/m	CMP w,t,r/m	CMP b,i	CMP w,i	SEG= CS	AAS
4	DEC AX	DEC CX	DEC DX	DEC BX	DEC SP	DEC BP	DEC SI	DEC DI
5	POP AX	POP CX	POP DX	POP BX	POP SP	POP BP	POP SI	POP DI
6								
7	JS	JNS	JP/ JPE	JNP/ JPO	JL/ JNGE	JNL/ JGE	JLE/ JNG	JNLE/ JG
8	MOV b,f,r/m	MOV w,f,r/m	MOV b,t,r/m	MOV w,t,r/m	MOV sr,t,r/m	LEA	MOV sr,f,r/m	POP r/m
9	CBW	CWD CX	CALL l,d	WAIT BX	PUSHF SP	POPF BP	SAHF SI	LAHF DI
A	TEST b,i	TEST w,i	STOS b	STOS w	LODS b	LODS w	SCAS b	SCAS w
B	MOV i AX	MOV i CX	MOV i DX	MOV i BX	MOV i SP	MOV i BP	MOV i SI	MOV i DI
C			RET l,(I+SP)	RET l	INT Type 3	INT (Any)	INTO	IRET
D	ESC 0	ESC 1	ESC 2	ESC 3	ESC 4	ESC 5	ESC 6	ESC 7
E	CALL d	JMP d	JMP l,d	JMP si,d	IN v,b	IN v,w	OUT v,b	OUT v,w
F	CLC	STC	CLI	STI	CLD	STD	Grp 2 b,r/m	Grp 3 w,r/m

where:

mod r/m	000	001	010	011	100	101	110	111
Immed	ADD	OR	ADC	SBB	AND	SUB	XOR	CMP
Shift	ROL	ROR	RCL	RCR	SHL/SAL	SHR	--	SAR
Grp 1	TEST	--	NOT	NEG	MUL	IMUL	DIV	DIV
Grp 2	INC	DEC	CALL id	CALL l,id	JMP id	JMP l,id	PUSH	--

8088 Conditional Transfer Operations

Instruction	Condition	Interpretation
JE or JZ	ZF = 1	"equal" or "zero"
JL or JNGE	(SF xor OF) = 1	"less" or "not greater or equal"
JLE or JNG	((SF xor OF) or ZF) = 1	"less or equal" or "not greater"
JB or JNAE or JC	CF = 1	"below" or "not above or equal"
JBE or JNA	(CF or ZF) = 1	"below or equal" or "not above"
JP or JPE	PF = 1	"parity" or "parity even"
JO	OF = 1	"overflow"
JS	SF = 1	"sign"
JNE or JNZ	ZF = 0	"not equal" or "not zero"
JNL or JGE	(SF xor OF) = 0	"not less" or "greater or equal"
JNLE or JG	((SF xor OF) or ZF) = 0	"not less or equal" or "greater"
JNB or JAE or JNC	CF = 0	"not below" or "above or equal"
JNBE or JA	(CF or ZF) = 0	"not below or equal" or "above"
JNP or JPO	PF = 0	"not parity" or "parity odd"
JNO	OF = 0	"not overflow"
JNS	SF = 0	"not sign"

"Above" and "below" refer to the relation between two unsigned values, while "greater" and "less" refer to the relation between two signed values.

INT = Interrupt

Type Specified

11001101	Type
----------	------

Type 3

11001100

INTO = Interrupt on Overflow

11001110

IRET = Interrupt Return

11001111

Processor Control

CLC = Clear Carry

11111000

STC = Set Carry

11111001

CMC = Complement Carry

11110101

NOP = No Operation

10010000

CLD = Clear Direction

11111100

STD = Set Direction

11111101

CLI = Clear Interrupt

11111010

STI = Set Interrupt

11111011

HLT = Halt

11110100

WAIT = Wait

10011011

LOCK = Bus lock prefix

11110000

ESC = Escape (to 8087)

11011xxx

mod xxx r/m

8087 Coprocessor Instruction Set

The following is an instruction set summary for the 8087 coprocessor. In the following, the bit pattern for escape is 11011.

MF = Memory format	r/m	Operand Address
00 - 32-bit Real	000	(BX) + (SI) + DISP
01 - 32-bit Integer	001	(BX) + (DI) + DISP
10 - 64-bit Real	010	(BP) + (SI) + DISP
11 - 64-bit Integer	011	(BP) + (DI) + DISP
	100	(SI) + DISP
	101	(DI) + DISP
	110	(BP) + DISP*
	100	(BX) + DISP

DISP follows 2nd byte of instruction (before data if required)
 *except if mod=00 and r/m=110 then EA=disp-high: disp-low.

Data Transfer

FLD = Load

Integer/Real Memory to ST(0)

escape MF 1	mod 000 r/m	disp-low	disp-high
-------------	-------------	----------	-----------

Long Integer Memory to ST(0)

escape 111	mod 101 r/m	disp-low	disp-high
------------	-------------	----------	-----------

Temporary Real Memory to ST(0)

escape 011	mod 101 r/m	disp-low	disp-high
------------	-------------	----------	-----------

BCD Memory to ST(0)

escape 111	mod 100 r/m	disp-low	disp-high
------------	-------------	----------	-----------

ST(i) to ST(0)

escape 001	11000ST(i)
------------	------------

FST = Store

ST(0) to Integer/Real Memory

escape MF 1	mod 010 r/m	disp-low	disp-high
-------------	-------------	----------	-----------

ST(0) to ST(i)

escape 101	11010 ST(i)
------------	-------------

FSTP = Store and Pop

ST(0) to Integer/Real Memory

escape MF 1	mod 011 r/m	disp-low	disp-high
-------------	-------------	----------	-----------

ST(0) to Long Integer Memory

escape 111	mod 111 r/m	disp-low	disp-high
------------	-------------	----------	-----------

ST(0) to Temporary Real Memory

escape 011	mod 111 r/m	disp-low	disp-high
------------	-------------	----------	-----------

ST(0) to BCD Memory

escape 111	mod 110 r/m	disp-low	disp-high
------------	-------------	----------	-----------

ST(0) to ST(i)

escape 101	11011 ST(i)
------------	-------------

FXCH = Exchange ST(i) and ST(0)

escape 001	11001 ST(i)
------------	-------------

Comparison

FCOM = Compare

Integer/Real Memory to ST(0)

escape MF 0	mod 010 r/m	disp-low	disp-high
-------------	-------------	----------	-----------

ST(i) to ST(0)

escape 000	11010 ST(i)
------------	-------------

FCOMP = Compare and Pop

Integer/Real Memory to ST(0)

escape MF 0	mod 011 r/m	disp-low	disp-high
-------------	-------------	----------	-----------

ST(i) to ST(0)

escape 000	11010 ST(i)
------------	-------------

FCOMPP = Compare ST(i) to ST(0) and Pop Twice

escape 110	11011001
------------	----------

FTST = Test ST(0)

escape 001	11100100
------------	----------

FXAM = Examine ST(0)

escape 001	11100101
------------	----------

Arithmetic

FADD = Addition

Integer/Real Memory with ST(0)

escape MF 0	mod 000 r/m	disp-low	disp-high
-------------	-------------	----------	-----------

ST(i) and ST(0)

escape dP0	11000 ST(i)
------------	-------------

FSUB = Subtraction

Integer/Real Memory with ST(0)

escape MF 0	mod 10R r/m	disp-low	disp-high
-------------	-------------	----------	-----------

ST(i) and ST(0)

escape dP0	1110R r/m
------------	-----------

FMUL = Multiplication

Integer/Real Memory with ST(0)

escape MF 0	mod 001 r/m	disp-low	disp-high
-------------	-------------	----------	-----------

ST(i) and ST(0)

escape dP0	11001 r/m
------------	-----------

FDIV = Division

Integer/Real Memory with ST(0)

escape MF 0	mod 11R r/m	disp-low	disp-high
-------------	-------------	----------	-----------

ST(i) and ST(0)

escape dP0	1111R r/m
------------	-----------

FSQRT = Square Root of ST(0)

escape 001	11111010
------------	----------

FSCALE = Scale ST(0) by ST(1)

escape 001	11111101
------------	----------

FPREM = Partial Remainder of ST(0) ÷ ST(1)

escape 001	11111000
------------	----------

FRNDINT = Round ST(0) to Integer

escape 001	11111100
------------	----------

FXTRACT = Extract Components of ST(0)

escape 001	11110100
------------	----------

FABS = Absolute Value of ST(0)

escape 001	11100001
------------	----------

FCHS = Change Sign of ST(0)

escape 001	11100000
------------	----------

Transcendental

FPTAN = Partial Tangent of ST(0)

escape 001	11110010
------------	----------

FPATAN = Partial Arctangent of ST(0) ÷ ST(1)

escape 001	11110011
------------	----------

F2XM1 = $2^{ST(0)} - 1$

escape 001	11110000
------------	----------

FYL2X = ST(1) x Log₂ [ST(0)]

escape 001	11110001
------------	----------

FYL2XP1 = ST(1) x Log₂ [ST(0) + 1]

escape 001	11111001
------------	----------

Constants

FLDZ = Load + 0.0 into ST(0)

escape 001	11101110
------------	----------

FLD1 = Load + 1.0 into ST(0)

escape 001	11101000
------------	----------

FLDP1 = Load π into ST(0)

escape 001	11101011
------------	----------

FLDL2T = Load $\text{Log}_2 10$ into ST(0)

escape 001	11101001
------------	----------

FLDLG2 = Load $\text{Log}_{10} 2$ into ST(0)

escape 001	11101100
------------	----------

FLDLN2 = Load $\text{Log}_e 2$ into ST(0)

escape 001	11101101
------------	----------

Processor Control

FINIT = Initialize NDP

escape 011	11100011
------------	----------

FENI = Enable Interrupts

escape 011	11100000
------------	----------

FDISI = Disable Interrupts

escape 011	11100001
------------	----------

FLDCW = Load Control Word

escape 001	mod101 r/m	disp-low	disp-high
------------	------------	----------	-----------

FSTCW = Store Control Word

escape 001	mod111 r/m	disp-low	disp-high
------------	------------	----------	-----------

FSTSW = Store Status Word

escape 101	mod111 r/m	disp-low	disp-high
------------	------------	----------	-----------

FCLEX = Clear Exceptions

escape 011	11100010
------------	----------

FSTENV = Store Environment

escape 001	mod110 r/m	disp-low	disp-high
------------	------------	----------	-----------

FLDENV = Load Environment

escape 001	mod100 r/m	disp-low	disp-high
------------	------------	----------	-----------

FSAVE = Save State

escape 101	mod110 r/m	disp-low	disp-high
------------	------------	----------	-----------

FRSTOR = Restore State

escape 101	mod100 r/m	disp-low	disp-high
------------	------------	----------	-----------

FINCSTP = Increment Stack Pointer

escape 001	11110111
------------	----------

FDECSTP = Decrement Stack Pointer

escape 001	11110110
------------	----------

FFREE = Free ST(i)

escape 001	11000ST(i)
------------	------------

FNOP = No Operation

escape 001	11010000
------------	----------

FWAIT = CPU Wait for NDP

10011011

Notes:

ST(0) = Current Stack top

ST(i) = i^{th} register below Stack top

d = Destination

0—Destination is ST(0)

1—Destination is ST(i)

P = POP

0—No Pop

1—Pop ST(0)

R = Reverse

0—Destination (op) Source

1—Source (op) Destination

For **FSQRT**: $-0 \leq \text{ST}(0) \leq +\infty$

For **FSCALE**: $-2^{15} \leq \text{ST}(1) < +2^{15}$ and ST(1) interger

For **F2XM1**: $0 \leq \text{ST}(0) \leq 2^{-1}$

For **FYL2X**: $0 < \text{St}(0) < \infty - \infty < \text{ST}(1) < +\infty$

For **FYL2XP1**: $0 < |\text{ST}(0)| < (2-\sqrt{2})/2 - \infty < \text{ST}(1) < \infty$

For **FPTAN**: $0 \leq \text{ST}(0) < \pi/4$

For **FPATAN**: $0 \leq \text{ST}(0) < \text{ST}(1) < +\infty$

Notes:

SECTION 7. CHARACTERS, KEYSTROKES, AND COLORS

Character Codes	7-3
Quick Reference	7-14

Notes:

Character Codes

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
00	0	Blank (Null)	Ctrl 2		Black	Black	Non-Display
01	1	☺	Ctrl A		Black	Blue	Underline
02	2	☹	Ctrl B		Black	Green	Normal
03	3	♥	Ctrl C		Black	Cyan	Normal
04	4	♦	Ctrl D		Black	Red	Normal
05	5	♣	Ctrl E		Black	Magenta	Normal
06	6	♠	Ctrl F		Black	Brown	Normal
07	7	●	Ctrl G		Black	Light Grey	Normal
08	8	•	Ctrl H, Backspace, Shift Backspace		Black	Dark Grey	Non-Display
09	9	○	Ctrl I		Black	Light Blue	High Intensity Underline
0A	10	◉	Ctrl J, Ctrl ←		Black	Light Green	High Intensity
0B	11	♂	Ctrl K		Black	Light Cyan	High Intensity
0C	12	♀	Ctrl L		Black	Light Red	High Intensity
0D	13	♪	Ctrl M, ←, Shift ←		Black	Light Magenta	High Intensity
0E	14	♫	Ctrl N		Black	Yellow	High Intensity
0F	15	☼	Ctrl O		Black	White	High Intensity
10	16	▶	Ctrl P		Blue	Black	Normal
11	17	◀	Ctrl Q		Blue	Blue	Underline
12	18	↕	Ctrl R		Blue	Green	Normal
13	19	!!	Ctrl S		Blue	Cyan	Normal
14	20	¶	Ctrl T		Blue	Red	Normal
15	21	§	Ctrl U		Blue	Magenta	Normal
16	22	■	Ctrl V		Blue	Brown	Normal
17	23	↕	Ctrl W		Blue	Light Grey	Normal

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
18	24	↑	Ctrl X		Blue	Dark Grey	High Intensity
19	25	↓	Ctrl Y		Blue	Light Blue	High Intensity Underline
1A	26	→	Ctrl Z		Blue	Light Green	High Intensity
1B	27	←	Ctrl [, Esc, Shift Esc, Ctrl Esc		Blue	Light Cyan	High Intensity
1C	28	└─	Ctrl \		Blue	Light Red	High Intensity
1D	29	↔	Ctrl]		Blue	Light Magenta	High Intensity
1E	30	▲	Ctrl 6		Blue	Yellow	High Intensity
1F	31	▼	Ctrl —		Blue	White	High Intensity
20	32	Blank Space	Space Bar, Shift, Space, Ctrl Space, Alt Space		Green	Black	Normal
21	33	!	!	Shift	Green	Blue	Underline
22	34	”	”	Shift	Green	Green	Normal
23	35	#	#	Shift	Green	Cyan	Normal
24	36	\$	\$	Shift	Green	Red	Normal
25	37	%	%	Shift	Green	Magenta	Normal
26	38	&	&	Shift	Green	Brown	Normal
27	39	,	,		Green	Light Grey	Normal
28	40	((Shift	Green	Dark Grey	High Intensity
29	41))	Shift	Green	Light Blue	High Intensity Underline
2A	42	*	*	Note 1	Green	Light Green	High Intensity
2B	43	+	+	Shift	Green	Light Cyan	High Intensity
2C	44	,	,		Green	Light Red	High Intensity
2D	45	-	-		Green	Light Magenta	High Intensity
2E	46	.	.	Note 2	Green	Yellow	High Intensity

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
2F	47	/	/		Green	White	High Intensity
30	48	0	0	Note 3	Cyan	Black	Normal
31	49	1	1	Note 3	Cyan	Blue	Underline
32	50	2	2	Note 3	Cyan	Green	Normal
33	51	3	3	Note 3	Cyan	Cyan	Normal
34	52	4	4	Note 3	Cyan	Red	Normal
35	53	5	5	Note 3	Cyan	Magenta	Normal
36	54	6	6	Note 3	Cyan	Brown	Normal
37	55	7	7	Note 3	Cyan	Light Grey	Normal
38	56	8	8	Note 3	Cyan	Dark Grey	High Intensity
39	57	9	9	Note 3	Cyan	Light Blue	High Intensity Underline
3A	58	:	:	Shift	Cyan	Light Green	High Intensity
3B	59	;	;		Cyan	Light Cyan	High Intensity
3C	60	<	<	Shift	Cyan	Light Red	High Intensity
3D	61	=	=		Cyan	Light Magenta	High Intensity
3E	62	>	>	Shift	Cyan	Yellow	High Intensity
3F	63	?	?	Shift	Cyan	White	High Intensity
40	64	@	@	Shift	Red	Black	Normal
41	65	A	A	Note 4	Red	Blue	Underline
42	66	B	B	Note 4	Red	Green	Normal
43	67	C	C	Note 4	Red	Cyan	Normal
44	68	D	D	Note 4	Red	Red	Normal
45	69	E	E	Note 4	Red	Magenta	Normal
46	70	F	F	Note 4	Red	Brown	Normal
47	71	G	G	Note 4	Red	Light Grey	Normal
48	72	H	H	Note 4	Red	Dark Grey	High Intensity
49	73	I	I	Note 4	Red	Light Blue	High Intensity Underline
4A	74	J	J	Note 4	Red	Light Green	High Intensity

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
4B	75	K	K	Note 4	Red	Light Cyan	High Intensity
4C	76	L	L	Note 4	Red	Light Red	High Intensity
4D	77	M	M	Note 4	Red	Light Magenta	High Intensity
4E	78	N	N	Note 4	Red	Yellow	High Intensity
4F	79	O	O	Note 4	Red	White	High Intensity
50	80	P	P	Note 4	Magenta	Black	Normal
51	81	Q	Q	Note 4	Magenta	Blue	Underline
52	82	R	R	Note 4	Magenta	Green	Normal
53	83	S	S	Note 4	Magenta	Cyan	Normal
54	84	T	T	Note 4	Magenta	Red	Normal
55	85	U	U	Note 4	Magenta	Magenta	Normal
56	86	V	V	Note 4	Magenta	Brown	Normal
57	87	W	W	Note 4	Magenta	Light Grey	Normal
58	88	X	X	Note 4	Magenta	Dark Grey	High Intensity
59	89	Y	Y	Note 4	Magenta	Light Blue	High Intensity Underline
5A	90	Z	Z	Note 4	Magenta	Light Green	High Intensity
5B	91	[[Magenta	Light Cyan	High Intensity
5C	92	\	\		Magenta	Light Red	High Intensity
5D	93]]		Magenta	Light Magenta	High Intensity
5E	94	^	^	Shift	Magenta	Yellow	High Intensity
5F	95	—	—	Shift	Magenta	White	High Intensity
60	96	·	·		Brown	Black	Normal
61	97	a	a	Note 5	Brown	Blue	Underline
62	98	b	b	Note 5	Brown	Green	Normal
63	99	c	c	Note 5	Brown	Cyan	Normal
64	100	d	d	Note 5	Brown	Red	Normal
65	101	e	e	Note 5	Brown	Magenta	Normal
66	102	f	f	Note 5	Brown	Brown	Normal

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
67	103	g	g	Note 5	Brown	Light Grey	Normal
68	104	h	h	Note 5	Brown	Dark Grey	High Intensity
69	105	i	i	Note 5	Brown	Light Blue	High Intensity Underline
6A	106	j	j	Note 5	Brown	Light Green	High Intensity
6B	107	k	k	Note 5	Brown	Light Cyan	High Intensity
6C	108	l	l	Note 5	Brown	Light Red	High Intensity
6D	109	m	m	Note 5	Brown	Light Magenta	High Intensity
6E	110	n	n	Note 5	Brown	Yellow	High Intensity
6F	111	o	o	Note 5	Brown	White	High Intensity
70	112	p	p	Note 5	Light Grey	Black	Reverse Video
71	113	q	q	Note 5	Light Grey	Blue	Underline
72	114	r	r	Note 5	Light Grey	Green	Normal
73	115	s	s	Note 5	Light Grey	Cyan	Normal
74	116	t	t	Note 5	Light Grey	Red	Normal
75	117	u	u	Note 5	Light Grey	Magenta	Normal
76	118	v	v	Note 5	Light Grey	Brown	Normal
77	119	w	w	Note 5	Light Grey	Light Grey	Normal
78	120	x	x	Note 5	Light Grey	Dark Grey	Reverse Video
79	121	y	y	Note 5	Light Grey	Light Blue	High Intensity Underline
7A	122	z	z	Note 5	Light Grey	Light Green	High Intensity
7B	123	{	{	Shift	Light Grey	Light Cyan	High Intensity
7C	124			Shift	Light Grey	Light Red	High Intensity
7D	125	}	}	Shift	Light Grey	Light Magenta	High Intensity
7E	126	~	~	Shift	Light Grey	Yellow	High Intensity
7F	127	△	Ctrl ←		Light Grey	White	High Intensity

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
**** 80 to FF Hex are Flashing in both Color & IBM Monochrome ****							
80	128	Ç	Alt 128	Note 6	Black	Black	Non-Display
81	129	ü	Alt 129	Note 6	Black	Blue	Underline
82	130	é	Alt 130	Note 6	Black	Green	Normal
83	131	â	Alt 131	Note 6	Black	Cyan	Normal
84	132	ã	Alt 132	Note 6	Black	Red	Normal
85	133	à	Alt 133	Note 6	Black	Magenta	Normal
86	134	â	Alt 134	Note 6	Black	Brown	Normal
87	135	ç	Alt 135	Note 6	Black	Light Grey	Normal
88	136	ê	Alt 136	Note 6	Black	Dark Grey	Non-Display
89	137	ë	Alt 137	Note 6	Black	Light Blue	High Intensity Underline
8A	138	è	Alt 138	Note 6	Black	Light Green	High Intensity
8B	139	ï	Alt 139	Note 6	Black	Light Cyan	High Intensity
8C	140	î	Alt 140	Note 6	Black	Light Red	High Intensity
8D	141	ì	Alt 141	Note 6	Black	Light Magenta	High Intensity
8E	142	Ä	Alt 142	Note 6	Black	Yellow	High Intensity
8F	143	Å	Alt 143	Note 6	Black	White	High Intensity
90	144	É	Alt 144	Note 6	Blue	Black	Normal
91	145	æ	Alt 145	Note 6	Blue	Blue	Underline
92	146	Æ	Alt 146	Note 6	Blue	Green	Normal
93	147	ô	Alt 147	Note 6	Blue	Cyan	Normal
94	148	ö	Alt 148	Note 6	Blue	Red	Normal
95	149	ò	Alt 149	Note 6	Blue	Magenta	Normal
96	150	û	Alt 150	Note 6	Blue	Brown	Normal
97	151	ù	Alt 151	Note 6	Blue	Light Grey	Normal
98	152	ÿ	Alt 152	Note 6	Blue	Dark Grey	High Intensity
99	153	Ö	Alt 153	Note 6	Blue	Light Blue	High Intensity Underline
9A	154	Ü	Alt 154	Note 6	Blue	Light Green	High Intensity

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
9B	155	¢	Alt 155	Note 6	Blue	Light Cyan	High Intensity
9C	156	£	Alt 156	Note 6	Blue	Light Red	High Intensity
9D	157	¥	Alt 157	Note 6	Blue	Light Magenta	High Intensity
9E	158	Pt	Alt 158	Note 6	Blue	Yellow	High Intensity
9F	159	f	Alt 159	Note 6	Blue	White	High Intensity
A0	160	á	Alt 160	Note 6	Green	Black	Normal
A1	161	í	Alt 161	Note 6	Green	Blue	Underline
A2	162	ó	Alt 162	Note 6	Green	Green	Normal
A3	163	ú	Alt 163	Note 6	Green	Cyan	Normal
A4	164	ñ	Alt 164	Note 6	Green	Red	Normal
A5	165	Ñ	Alt 165	Note 6	Green	Magenta	Normal
A6	166	<u>a</u>	Alt 166	Note 6	Green	Brown	Normal
A7	167	<u>o</u>	Alt 167	Note 6	Green	Light Grey	Normal
A8	168	¿	Alt 168	Note 6	Green	Dark Grey	High Intensity
A9	169	┌	Alt 169	Note 6	Green	Light Blue	High Intensity Underline
AA	170	└	Alt 170	Note 6	Green	Light Green	High Intensity
AB	171	½	Alt 171	Note 6	Green	Light Cyan	High Intensity
AC	172	¼	Alt 172	Note 6	Green	Light Red	High Intensity
AD	173	ı	Alt 173	Note 6	Green	Light Magenta	High Intensity
AE	174	<<	Alt 174	Note 6	Green	Yellow	High Intensity
AF	175	>>	Alt 175	Note 6	Green	White	High Intensity
B0	176	⋮	Alt 176	Note 6	Cyan	Black	Normal
B1	177	⋮	Alt 177	Note 6	Cyan	Blue	Underline
B2	178	⋮	Alt 178	Note 6	Cyan	Green	Normal
B3	179		Alt 179	Note 6	Cyan	Cyan	Normal
B4	180	▬	Alt 180	Note 6	Cyan	Red	Normal
B5	181	▬	Alt 181	Note 6	Cyan	Magenta	Normal
B6	182	▬	Alt 182	Note 6	Cyan	Brown	Normal

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
D1	209		Alt 209	Note 6	Magenta	Blue	Underline
D2	210		Alt 210	Note 6	Magenta	Green	Normal
D3	211		Alt 211	Note 6	Magenta	Cyan	Normal
D4	212		Alt 212	Note 6	Magenta	Red	Normal
D5	213		Alt 213	Note 6	Magenta	Magenta	Normal
D6	214		Alt 214	Note 6	Magenta	Brown	Normal
D7	215		Alt 215	Note 6	Magenta	Light Grey	Normal
D8	216		Alt 216	Note 6	Magenta	Dark Grey	High Intensity
D9	217		Alt 217	Note 6	Magenta	Light Blue	High Intensity Underline
DA	218		Alt 218	Note 6	Magenta	Light Green	High Intensity
DB	219		Alt 219	Note 6	Magenta	Light Cyan	High Intensity
DC	220		Alt 220	Note 6	Magenta	Light Red	High Intensity
DD	221		Alt 221	Note 6	Magenta	Light Magenta	High Intensity
DE	222		Alt 222	Note 6	Magenta	Yellow	High Intensity
DF	223		Alt 223	Note 6	Magenta	White	High Intensity
E0	224	α	Alt 224	Note 6	Brown	Black	Normal
E1	225	β	Alt 225	Note 6	Brown	Blue	Underline
E2	226	Γ	Alt 226	Note 6	Brown	Green	Normal
E3	227	π	Alt 227	Note 6	Brown	Cyan	Normal
E4	228	Σ	Alt 228	Note 6	Brown	Red	Normal
E5	229	σ	Alt 229	Note 6	Brown	Magenta	Normal
E6	230	μ	Alt 230	Note 6	Brown	Brown	Normal
E7	231	τ	Alt 231	Note 6	Brown	Light Grey	Normal
E8	232	Φ	Alt 232	Note 6	Brown	Dark Grey	High Intensity
E9	233	θ	Alt 233	Note 6	Brown	Light Blue	High Intensity Underline
EA	234	Ω	Alt 234	Note 6	Brown	Light Green	High Intensity
EB	235	δ	Alt 235	Note 6	Brown	Light Cyan	High Intensity

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
EC	236	∞	Alt 236	Note 6	Brown	Light Red	High Intensity
ED	237	ϕ	Alt 237	Note 6	Brown	Light Magenta	High Intensity
EE	238	€	Alt 238	Note 6	Brown	Yellow	High Intensity
EF	239	∩	Alt 239	Note 6	Brown	White	High Intensity
F0	240	≡	Alt 240	Note 6	Light Grey	Black	Reverse Video
F1	241	±	Alt 241	Note 6	Light Grey	Blue	Underline
F2	242	≥	Alt 242	Note 6	Light Grey	Green	Normal
F3	243	≤	Alt 243	Note 6	Light Grey	Cyan	Normal
F4	244	∫	Alt 244	Note 6	Light Grey	Red	Normal
F5	245	∫	Alt 245	Note 6	Light Grey	Magenta	Normal
F6	246	÷	Alt 246	Note 6	Light Grey	Brown	Normal
F7	247	≈	Alt 247	Note 6	Light Grey	Light Grey	Normal
F8	248	○	Alt 248	Note 6	Light Grey	Dark Grey	Reverse Video
F9	249	●	Alt 249	Note 6	Light Grey	Light Blue	High Intensity Underline
FA	250	●	Alt 250	Note 6	Light Grey	Light Green	High Intensity
FB	251	√	Alt 251	Note 6	Light Grey	Light Cyan	High Intensity
FC	252	ⁿ	Alt 252	Note 6	Light Grey	Light Red	High Intensity
FD	253	²	Alt 253	Note 6	Light Grey	Light Magenta	High Intensity
FE	254	■	Alt 254	Note 6	Light Grey	Yellow	High Intensity
FF	255	BLANK	Alt 255	Note 6	Light Grey	White	High Intensity

Notes:

1. Asterisk (*) can be typed using two methods: press the (PrtSc/*) key or, in the shift mode, press the 8 key.
2. Period (.) can be typed using two methods: press the . key or, in the shift or Num Lock mode, press the Del key.
3. Numeric characters 0-9 can be typed using two methods: press the numeric keys on the top row of the keyboard or, in the shift or Num Lock mode, press the numeric keys in the keypad portion of the keyboard.
4. Uppercase alphabetic characters (A-Z) can be typed in two modes: the shift mode or the Caps Lock mode.
5. Lowercase alphabetic characters (a-z) can be typed in two modes: in the normal mode or in Caps Lock and shift mode combined.
6. The three digits after the Alt key must be typed from the numeric keypad. Character codes 001-255 may be entered in this fashion (with Caps Lock activated, character codes 97-122 will display uppercase).

Quick Reference

DECIMAL VALUE	➡	0	16	32	48	64	80	96	112
⬇	HEXA-DECIMAL VALUE	0	1	2	3	4	5	6	7
0	0	BLANK (NULL)	▶	BLANK (SPACE)	0	@	P	'	p
1	1	😊	◀	!	1	A	Q	a	q
2	2	😬	↕		2	B	R	b	r
3	3	♥	!!	#	3	C	S	c	s
4	4	♦	¶	\$	4	D	T	d	t
5	5	♣	§	%	5	E	U	e	u
6	6	♠	▬	&	6	F	V	f	v
7	7	•	↕	'	7	G	W	g	w
8	8	●	↑	(8	H	X	h	x
9	9	○	↓)	9	I	Y	i	y
10	A	◉	→	*	:	J	Z	j	z
11	B	♂	←	+	;	K	[k	{
12	C	♀	└	,	<	L	\	l	
13	D	🎵	↔	—	=	M]	m	}
14	E	🎵	▲	.	>	N	^	n	~
15	F	☀	▼	/	?	O	_	o	△

DECIMAL VALUE	➡	128	144	160	176	192	208	224	240
⬇	HEXA-DECIMAL VALUE	8	9	A	B	C	D	E	F
0	0	Ç	É	á	⋮			∞	≡
1	1	ü	æ	í	⋮			β	±
2	2	é	Æ	ó	⋮			Γ	≥
3	3	â	ô	ú				π	≤
4	4	ä	ö	ñ				Σ	∫
5	5	à	ò	Ñ				σ	∫
6	6	å	û	à				μ	÷
7	7	ç	ù	ó				γ	≈
8	8	ê	ÿ	¿				Φ	°
9	9	ë	Ö	┘				Θ	•
10	A	è	Ü	┘				Ω	•
11	B	ï	¢	½				δ	√
12	C	î	£	¼				∞	n
13	D	ì	¥	¡				φ	²
14	E	Ä	℞	«				€	■
15	F	Å	f	»				∩	BLANK 'FF'

Notes:

Glossary

This glossary includes terms and definitions from the *IBM Vocabulary for Data Processing, Telecommunications, and Office Systems*, GC20-1699.

μ . Prefix micro; 0.000 001.

μ s. Microsecond; 0.000 001 second.

A. Ampere.

ac. Alternating current.

accumulator. A register in which the result of an operation is formed.

active high. Designates a signal that has to go high to produce an effect. Synonymous with positive true.

active low. Designates a signal that has to go low to produce an effect. Synonymous with negative true.

adapter. An auxiliary device or unit used to extend the operation of another system.

address bus. One or more conductors used to carry the binary-coded address from the processor throughout the rest of the system.

algorithm. A finite set of well-defined rules for the solution of a problem in a finite number of steps.

all points addressable (APA). A mode in which all points of a displayable image can be controlled by the user.

alphanumeric. Synonym for alphanumeric.

alphanumeric (A/N). Pertaining to a character set that contains letters, digits, and usually other characters, such as punctuation marks. Synonymous with alphameric.

alternating current (ac). A current that periodically reverses its direction of flow.

American National Standard Code for Information Interchange (ASCII). The standard code, using a coded character set consisting of 7-bit coded characters (8 bits including parity check), used for information exchange between data processing systems, data communication systems, and associated equipment. The ASCII set consists of control characters and graphic characters.

ampere (A). The basic unit of electric current.

A/N. Alphanumeric

analog. (1) Pertaining to data in the form of continuously variable physical quantities. (2) Contrast with digital.

AND. A logic operator having the property that if P is a statement, Q is a statement, R is a statement,..., then the AND of P, Q, R,...is true if all statements are true, false if any statement is false.

AND gate. A logic gate in which the output is 1 only if all inputs are 1.

AND operation. The boolean operation whose result has the boolean value 1, if and only if, each operand has the boolean value 1. Synonymous with conjunction.

APA. All points addressable.

ASCII. American National Standard Code for Information Interchange.

assemble. To translate a program expressed in an assembler language into a computer language.

assembler. A computer program used to assemble.

assembler language. A computer-oriented language whose instructions are usually in one-to-one correspondence with computer instructions.

asynchronous transmission. (1) Transmission in which the time of occurrence of the start of each character, or block of characters, is arbitrary; once started, the time of occurrence of each signal representing a bit within a character, or block, has the same relationship to significant instants of a fixed time frame. (2) Transmission in which each information character is individually transmitted (usually timed by the use of start elements and stop elements).

audio frequencies. Frequencies that can be heard by the human ear (approximately 15 hertz to 20,000 hertz).

auxiliary storage. (1) A storage device that is not main storage. (2) Data storage other than main storage; for example, storage on magnetic disk. (3) Contrast with main storage.

BASIC. Beginner's all-purpose symbolic instruction code.

basic input/output system (BIOS). The feature of the IBM Personal Computer that provides the level control of the major I/O devices, and relieves the programmer from concern about hardware device characteristics.

baud. (1) A unit of signaling speed equal to the number of discrete conditions or signal events per second. For example, one baud equals one bit per second in a train of binary signals, one-half dot cycle per second in Morse code, and one 3-bit value per second in a train of signals each of which can assume one of eight different states. (2) In asynchronous transmission, the unit of modulation rate corresponding to one unit of interval per second; that is, if the duration of the unit interval is 20 milliseconds, the modulation rate is 50 baud.

BCC. Block-check character.

beginner's all-purpose symbolic instruction code (BASIC). A programming language with a small repertoire of commands and a simple syntax, primarily designed for numeric applications.

binary. (1) Pertaining to a selection, choice, or condition that has two possible values or states. (2) Pertaining to a fixed radix numeration system having a radix of 2.

binary digit. (1) In binary notation, either of the characters 0 or 1. (2) Synonymous with bit.

binary notation. Any notation that uses two different characters, usually the binary digits 0 and 1.

binary synchronous communications (BSC). A uniform procedure, using a standardized set of control characters and control character sequences for synchronous transmission of binary-coded data between stations.

BIOS. Basic input/output system.

bit. Synonym for binary digit

bits per second (bps). A unit of measurement representing the number of discrete binary digits transmitted by a device in one second.

block. (1) A string of records, a string of words, or a character string formed for technical or logic reasons to be treated as an entity. (2) A set of things, such as words, characters, or digits, treated as a unit.

block-check character (BCC). In cyclic redundancy checking, a character that is transmitted by the sender after each message block and is compared with a block-check character computed by the receiver to determine if the transmission was successful.

boolean operation. (1) Any operation in which each of the operands and the result take one of two values. (2) An operation that follows the rules of boolean algebra.

bootstrap. A technique or device designed to bring itself into a desired state by means of its own action; for example, a machine routine whose first few instructions are sufficient to bring the rest of itself into the computer from an input device.

bps. Bits per second.

BSC. Binary synchronous communications.

buffer. (1) An area of storage that is temporarily reserved for use in performing an input/output operation, into which data is read or from which data is written. Synonymous with I/O area. (2) A portion of storage for temporarily holding input or output data.

bus. One or more conductors used for transmitting signals or power.

byte. (1) A sequence of eight adjacent binary digits that are operated upon as a unit. (2) A binary character operated upon as a unit. (3) The representation of a character.

C. Celsius.

capacitor. An electronic circuit component that stores an electric charge.

Cartesian coordinates. A system of coordinates for locating a point on a plane by its distance from each of two intersecting lines, or in space by its distance from each of three mutually perpendicular planes.

CAS. Column address strobe.

cathode ray tube (CRT). A vacuum tube in which a stream of electrons is projected onto a fluorescent screen producing a luminous spot. The location of the spot can be controlled.

cathode ray tube display (CRT display). (1) A CRT used for displaying data. For example, the electron beam can be controlled to form alphanumeric data by use of a dot matrix. (2) Synonymous with monitor.

CCITT. International Telegraph and Telephone Consultative Committee.

Celsius (C). A temperature scale. Contrast with Fahrenheit (F).

central processing unit (CPU). Term for processing unit.

channel. A path along which signals can be sent; for example, data channel, output channel.

character generator. (1) In computer graphics, a functional unit that converts the coded representation of a graphic character into the shape of the character for display. (2) In word processing, the means within equipment for generating visual characters or symbols from coded data.

character set. (1) A finite set of different characters upon which agreement has been reached and that is considered complete for some purpose. (2) A set of unique representations called characters. (3) A defined collection of characters.

characters per second (cps). A standard unit of measurement for the speed at which a printer prints.

check key. A group of characters, derived from and appended to a data item, that can be used to detect errors in the data item during processing.

clipping. In computer graphics, removing parts of a display image that lie outside a window.

closed circuit. A continuous unbroken circuit; that is, one in which current can flow. Contrast with open circuit.

CMOS. Complementary metal oxide semiconductor.

code. (1) A set of unambiguous rules specifying the manner in which data may be represented in a discrete form. Synonymous with coding scheme. (2) A set of items, such as abbreviations, representing the members of another set. (3) To represent data or a computer program in a symbolic form that can be accepted by a data processor. (4) Loosely, one or more computer programs, or part of a computer program.

coding scheme. Synonym for code.

collector. An element in a transistor toward which current flows.

color cone. An arrangement of the visible colors on the surface of a double-ended cone where lightness varies along the axis of

the cone, and hue varies around the circumference. Lightness includes both the intensity and saturation of color.

column address strobe (CAS). A signal that latches the column addresses in a memory chip.

compile. (1) To translate a computer program expressed in a problem-oriented language into a computer-oriented language. (2) To prepare a machine-language program from a computer program written in another programming language by making use of the overall logic structure of the program, or generating more than one computer instruction for each symbolic statement, or both, as well as performing the function of an assembler.

complement. A number that can be derived from a specified number by subtracting it from a second specified number.

complementary metal oxide semiconductor (CMOS). A logic circuit family that uses very little power. It works with a wide range of power supply voltages.

computer. A functional unit that can perform substantial computation, including numerous arithmetic operations or logic operations, without human intervention during a run.

computer instruction code. A code used to represent the instructions in an instruction set. Synonymous with machine code.

computer program. A sequence of instructions suitable for processing by a computer.

computer word. A word stored in one computer location and capable of being treated as a unit.

configuration. (1) The arrangement of a computer system or network as defined by the nature, number, and the chief characteristics of its functional units. More specifically, the term configuration may refer to a hardware configuration or a software configuration. (2) The devices and programs that make up a system, subsystem, or network.

conjunction. Synonym for AND operation.

contiguous. Touching or joining at the edge or boundary; adjacent.

control character. A character whose occurrence in a particular context initiates, modifies, or stops a control operation.

control operation. An action that affects the recording, processing, transmission, or interpretation of data; for example, starting or stopping a process, carriage return, font change, rewind, and end of transmission.

control storage. A portion of storage that contains microcode.

coordinate space. In computer graphics, a system of Cartesian coordinates in which an object is defined.

cps. Characters per second.

CPU. Central processing unit.

CRC. Cyclic redundancy check.

CRT. Cathode ray tube.

CRT display. Cathode ray tube display.

CTS. Clear to send. Associated with modem control.

cursor. (1) In computer graphics, a movable marker that is used to indicate position on a display. (2) A displayed symbol that acts as a marker to help the user locate a point in text, in a system command, or in storage. (3) A movable spot of light on the screen of a display device, usually indicating where the next character is to be entered, replaced, or deleted.

cyclic redundancy check (CRC). (1) A redundancy check in which the check key is generated by a cyclic algorithm. (2) A system of error checking performed at both the sending and receiving station after a block-check character has been accumulated.

cylinder. (1) The set of all tracks with the same nominal distance from the axis about which the disk rotates. (2) The

tracks of a disk storage device that can be accessed without repositioning the access mechanism.

daisy-chained cable. A type of cable that has two or more connectors attached in series.

data. (1) A representation of facts, concepts, or instructions in a formalized manner suitable for communication, interpretation, or processing by human or automatic means. (2) Any representations, such as characters or analog quantities, to which meaning is, or might be assigned.

data base. A collection of data that can be immediately accessed and operated upon by a data processing system for a specific purpose.

data processing system. A system that performs input, processing, storage, output, and control functions to accomplish a sequence of operations on data.

data transmission. Synonym for transmission.

dB. Decibel.

dBa. Adjusted decibels.

dc. Direct current.

debounce. (1) An electronic means of overcoming the make/break bounce of switches to obtain one smooth change of signal level. (2) The elimination of undesired signal variations caused by mechanically generated signals from contacts.

decibel. (1) A unit that expresses the ratio of two power levels on a logarithmic scale. (2) A unit for measuring relative power.

decoupling capacitor. A capacitor that provides a low impedance path to ground to prevent common coupling between circuits.

Deutsche Industrie Norm (DIN). (1) German Industrial Norm. (2) The committee that sets German dimension standards.

digit. (1) A graphic character that represents an integer; for example, one of the characters 0 to 9. (2) A symbol that

represents one of the non-negative integers smaller than the radix. For example, in decimal notation, a digit is one of the characters 0 to 9.

digital. (1) Pertaining to data in the form of digits.
(2) Contrast with analog.

DIN. Deutsche Industrie Norm.

DIN connector. One of the connectors specified by the DIN committee.

DIP. Dual in-line package.

DIP switch. One of a set of small switches mounted in a dual in-line package.

direct current (dc). A current that always flows in one direction.

direct memory access (DMA). A method of transferring data between main storage and I/O devices that does not require processor intervention.

disable. To stop the operation of a circuit or device.

disabled. Pertaining to a state of a processing unit that prevents the occurrence of certain types of interruptions. Synonymous with masked.

disk. Loosely, a magnetic disk.

diskette. A thin, flexible magnetic disk and a semirigid protective jacket, in which the disk is permanently enclosed. Synonymous with flexible disk.

diskette drive. A device for storing data on and retrieving data from a diskette.

display. (1) A visual presentation of data. (2) A device for visual presentation of information on any temporary character imaging device. (3) To present data visually. (4) See cathode ray tube display.

display attribute. In computer graphics, a particular property that is assigned to all or part of a display; for example, low intensity, green color, blinking status.

display element. In computer graphics, a basic graphic element that can be used to construct a display image; for example, a dot, a line segment, a character.

display group. In computer graphics, a collection of display elements that can be manipulated as a unit and that can be further combined to form larger groups.

display image. In computer graphics, a collection of display elements or display groups that are represented together at any one time in a display space.

display space. In computer graphics, that portion of a display surface available for a display image. The display space may be all or part of a display surface.

display surface. In computer graphics, that medium on which display images may appear; for example, the entire screen of a cathode ray tube.

DMA. Direct memory access.

dot matrix. (1) In computer graphics, a two-dimensional pattern of dots used for constructing a display image. This type of matrix can be used to represent characters by dots. (2) In word processing, a pattern of dots used to form characters. This term normally refers to a small section of a set of addressable points; for example, a representation of characters by dots.

dot printer. Synonym for matrix printer.

dot-matrix character generator. In computer graphics, a character generator that generates character images composed of dots.

drawing primitive. A group of commands that draw defined geometric shapes.

DSR. Data set ready. Associated with modem control.

DTR. In the IBM Personal Computer, data terminal ready. Associated with modem control.

dual in-line package (DIP). A widely used container for an integrated circuit. DIPs have pins in two parallel rows. The pins are spaced 1/10 inch apart. See also DIP switch.

duplex. (1) In data communication, pertaining to a simultaneous two-way independent transmission in both directions.
(2) Contrast with half-duplex.

duty cycle. In the operation of a device, the ratio of on time to idle time. Duty cycle is expressed as a decimal or percentage.

dynamic memory. RAM using transistors and capacitors as the memory elements. This memory requires a refresh (recharge) cycle every few milliseconds. Contrast with static memory.

EBCDIC. Extended binary-coded decimal interchange code.

ECC. Error checking and correction.

edge connector. A terminal block with a number of contacts attached to the edge of a printed-circuit board to facilitate plugging into a foundation circuit.

EIA. Electronic Industries Association.

electromagnet. Any device that exhibits magnetism only while an electric current flows through it.

enable. To initiate the operation of a circuit or device.

end of block (EOB). A code that marks the end of a block of data.

end of file (EOF). An internal label, immediately following the last record of a file, signaling the end of that file. It may include control totals for comparison with counts accumulated during processing.

end-of-text (ETX). A transmission control character used to terminate text.

end-of-transmission (EOT). A transmission control character used to indicate the conclusion of a transmission, which may have included one or more texts and any associated message headings.

end-of-transmission-block (ETB). A transmission control character used to indicate the end of a transmission block of data when data is divided into such blocks for transmission purposes.

EOB. End of block.

EOF. End of file.

EOT. End-of-transmission.

EPROM. Erasable programmable read-only memory.

erasable programmable read-only memory (EPROM). A PROM in which the user can erase old information and enter new information.

error checking and correction (ECC). The detection and correction of all single-bit errors, plus the detection of double-bit and some multiple-bit errors.

ESC. The escape character.

escape character (ESC). A code extension character used, in some cases, with one or more succeeding characters to indicate by some convention or agreement that the coded representations following the character or the group of characters are to be interpreted according to a different code or according to a different coded character set.

ETB. End-of-transmission-block.

ETX. End-of-text.

extended binary-coded decimal interchange code (EBCDIC). A set of 256 characters, each represented by eight bits.

F. Fahrenheit.

Fahrenheit (F). A temperature scale. Contrast with Celsius (C).

falling edge. Synonym for negative-going edge.

FCC. Federal Communications Commission.

fetch. To locate and load a quantity of data from storage.

FF. The form feed character.

field. (1) In a record, a specified area used for a particular category of data. (2) In a data base, the smallest unit of data that can be referred to.

field-programmable logic sequencer (FPLS). An integrated circuit containing a programmable, read-only memory that responds to external inputs and feedback of its own outputs.

FIFO (first-in-first out). A queuing technique in which the next item to be retrieved is the item that has been in the queue for the longest time.

fixed disk drive. In the IBM Personal Computer, a unit consisting of nonremovable magnetic disks, and a device for storing data on and retrieving data from the disks.

flag. (1) Any of various types of indicators used for identification. (2) A character that signals the occurrence of some condition, such as the end of a word. (3) Deprecated term for mark.

flexible disk. Synonym for diskette.

flip-flop. A circuit or device containing active elements, capable of assuming either one of two stable states at a given time.

font. A family or assortment of characters of a given size and style; for example, 10 point Press Roman medium.

foreground. (1) In multiprogramming, the environment in which high-priority programs are executed. (2) On a color display screen, the characters as opposed to the background.

form feed. (1) Paper movement used to bring an assigned part of a form to the printing position. (2) In word processing, a

function that advances the typing position to the same character position on a predetermined line of the next form or page.

form feed character. A control character that causes the print or display position to move to the next predetermined first line on the next form, the next page, or the equivalent.

format. The arrangement or layout of data on a data medium.

FPLS. Field-programmable logic sequencer.

frame. (1) In SDLC, the vehicle for every command, every response, and all information that is transmitted using SDLC procedures. Each frame begins and ends with a flag. (2) In data transmission, the sequence of contiguous bits bracketed by and including beginning and ending flag sequences.

g. Gram.

G. (1) Prefix giga; 1,000,000,000. (2) When referring to computer storage capacity, 1,073,741,824. ($1,073,741,824 = 2$ to the 30th power.)

gate. (1) A combinational logic circuit having one output channel and one or more input channels, such that the output channel state is completely determined by the input channel states. (2) A signal that enables the passage of other signals through a circuit.

Gb. 1,073,741,824 bytes.

general-purpose register. A register, usually explicitly addressable within a set of registers, that can be used for different purposes; for example, as an accumulator, as an index register, or as a special handler of data.

giga (G). Prefix 1,000,000,000.

gram (g). A unit of weight (equivalent to 0.035 ounces).

graphic. A symbol produced by a process such as handwriting, drawing, or printing.

graphic character. A character, other than a control character, that is normally represented by a graphic.

half-duplex. (1) In data communication, pertaining to an alternate, one way at a time, independent transmission. (2) Contrast with duplex.

hardware. (1) Physical equipment used in data processing, as opposed to programs, procedures, rules, and associated documentation. (2) Contrast with software.

head. A device that reads, writes, or erases data on a storage medium; for example, a small electromagnet used to read, write, or erase data on a magnetic disk.

hertz (Hz). A unit of frequency equal to one cycle per second.

hex. Common abbreviation for hexadecimal. Also, hexadecimal can be noted as X' '.

hexadecimal. (1) Pertaining to a selection, choice, or condition that has 16 possible different values or states. These values or states are usually symbolized by the ten digits 0 through 9 and the six letters A through F. (2) Pertaining to a fixed radix numeration system having a radix of 16.

high impedance state. A state in which the output of a device is effectively isolated from the circuit.

highlighting. In computer graphics, emphasizing a given display group by changing its attributes relative to other display groups in the same display field.

high-order position. The leftmost position in a string of characters. See also most-significant digit.

hither plane. In computer graphics, a plane that is perpendicular to the line joining the viewing reference point and the view point and that lies between these two points. Any part of an object between the hither plane and the view point is not seen. See also yon plane.

housekeeping. Operations or routines that do not contribute directly to the solution of the problem but do contribute directly to the operation of the computer.

Hz. Hertz

image. A fully processed unit of operational data that is ready to be transmitted to a remote unit; when loaded into control storage in the remote unit, the image determines the operations of the unit.

immediate instruction. An instruction that contains within itself an operand for the operation specified, rather than an address of the operand.

index register. A register whose contents may be used to modify an operand address during the execution of computer instructions.

indicator. (1) A device that may be set into a prescribed state, usually according to the result of a previous process or on the occurrence of a specified condition in the equipment, and that usually gives a visual or other indication of the existence of the prescribed state, and that may in some cases be used to determine the selection among alternative processes; for example, an overflow indicator. (2) An item of data that may be interrogated to determine whether a particular condition has been satisfied in the execution of a computer program; for example, a switch indicator, an overflow indicator.

inhibited. (1) Pertaining to a state of a processing unit in which certain types of interruptions are not allowed to occur. (2) Pertaining to the state in which a transmission control unit or an audio response unit cannot accept incoming calls on a line.

initialize. To set counters, switches, addresses, or contents of storage to 0 or other starting values at the beginning of, or at prescribed points in, the operation of a computer routine.

input/output (I/O). (1) Pertaining to a device or to a channel that may be involved in an input process, and, at a different time, in an output process. In the English language, "input/output" may be used in place of such terms as "input/output data," "input/output signal," and "input/output terminals," when such usage is clear in a given context. (2) Pertaining to a device

whose parts can be performing an input process and an output process at the same time. (3) Pertaining to either input or output, or both.

instruction. In a programming language, a meaningful expression that specifies one operation and identifies its operands, if any.

instruction set. The set of instructions of a computer, of a programming language, or of the programming languages in a programming system.

intensity. In computer graphics, the amount of light emitted at a display point

interface. A device that alters or converts actual electrical signals between distinct devices, programs, or systems.

interleave. To arrange parts of one sequence of things or events so that they alternate with parts of one or more other sequences of the same nature and so that each sequence retains its identity.

interrupt. (1) A suspension of a process, such as the execution of a computer program, caused by an event external to that process, and performed in such a way that the process can be resumed. (2) In a data transmission, to take an action at a receiving station that causes the transmitting station to terminate a transmission. (3) Synonymous with interruption.

I/O. Input/output.

I/O area. Synonym for buffer.

irrecoverable error. An error that makes recovery impossible without the use of recovery techniques external to the computer program or run.

joystick. In computer graphics, a lever that can pivot in all directions and that is used as a locator device.

k. Prefix kilo; 1000.

K. When referring to storage capacity, 1024. (1024 = 2 to the 10th power.)

KB. 1024 bytes.

key lock. A device that deactivates the keyboard and locks the cover on for security.

kg. Kilogram; 1000 grams.

kHz. Kilohertz; 1000 hertz.

kilo (k). Prefix 1000

kilogram (kg). 1000 grams.

kilohertz (kHz). 1000 hertz

latch. (1) A simple logic-circuit storage element. (2) A feedback loop in sequential digital circuits used to maintain a state.

least-significant digit. The rightmost digit. See also low-order position.

LED. Light-emitting diode.

light-emitting diode (LED). A semiconductor device that gives off visible or infrared light when activated.

load. In programming, to enter data into storage or working registers.

look-up table (LUT). (1) A technique for mapping one set of values into a larger set of values. (2) In computer graphics, a table that assigns a color value (red, green, blue intensities) to a color index.

low power Schottky TTL. A version (LS series) of TTL giving a good compromise between low power and high speed. See also transistor-transistor logic and Schottky TTL.

low-order position. The rightmost position in a string of characters. See also least-significant digit.

luminance. The luminous intensity per unit projected area of a given surface viewed from a given direction.

LUT. Look-up table.

m. (1) Prefix milli; 0.001. (2) Meter.

M. (1) Prefix mega; 1,000,000. (2) When referring to computer storage capacity, 1,048,576. (1,048,576 = 2 to the 20th power.)

mA. Milliampere; 0.001 ampere.

machine code. The machine language used for entering text and program instructions onto the recording medium or into storage and which is subsequently used for processing and printout.

machine language. (1) A language that is used directly by a machine. (2) Deprecated term for computer instruction code.

magnetic disk. (1) A flat circular plate with a magnetizable surface layer on which data can be stored by magnetic recording. (2) See also diskette.

main storage. (1) Program-addressable storage from which instructions and other data can be loaded directly into registers for subsequent execution or processing. (2) Contrast with auxiliary storage.

mark. A symbol or symbols that indicate the beginning or the end of a field, of a word, of an item of data, or of a set of data such as a file, a record, or a block.

mask. (1) A pattern of characters that is used to control the retention or elimination of portions of another pattern of characters. (2) To use a pattern of characters to control the retention or elimination of portions of another pattern of characters.

masked. Synonym for disabled.

matrix. (1) A rectangular array of elements, arranged in rows and columns, that may be manipulated according to the rules of matrix algebra. (2) In computers, a logic network in the form of an array of input leads and output leads with logic elements connected at some of their intersections.

matrix printer. A printer in which each character is represented by a pattern of dots; for example, a stylus printer, a wire printer. Synonymous with dot printer.

MB. 1,048,576 bytes.

mega (M). Prefix 1,000,000.

megahertz (MHz). 1,000,000 hertz.

memory. Term for main storage.

meter (m). A unit of length (equivalent to 39.37 inches).

MFM. Modified frequency modulation.

MHz. Megahertz; 1,000,000 hertz.

micro (μ). Prefix 0.000,001.

microcode. (1) One or more microinstructions. (2) A code, representing the instructions of an instruction set, implemented in a part of storage that is not program-addressable.

microinstruction. (1) An instruction of microcode. (2) A basic or elementary machine instruction.

microprocessor. An integrated circuit that accepts coded instructions for execution; the instructions may be entered, integrated, or stored internally.

microsecond (μ s). 0.000,001 second.

milli (m). Prefix 0.001.

milliampere (mA). 0.001 ampere.

millisecond (ms). 0.001 second.

mnemonic. A symbol chosen to assist the human memory; for example, an abbreviation such as "mpy" for "multiply."

mode. (1) A method of operation; for example, the binary mode, the interpretive mode, the alphanumeric mode. (2) The most frequent value in the statistical sense.

modeling transformation. Operations on the coordinates of an object (usually matrix multiplications) that cause the object to be rotated about any axis, translated (moved without rotating), and/or scaled (changed in size along any or all dimensions). See also viewing transformation.

modem (modulator-demodulator). A device that converts serial (bit by bit) digital signals from a business machine (or data communication equipment) to analog signals that are suitable for transmission in a telephone network. The inverse function is also performed by the modem on reception of analog signals.

modified frequency modulation (MFM). The process of varying the amplitude and frequency of the 'write' signal. MFM pertains to the number of bytes of storage that can be stored on the recording media. The number of bytes is twice the number contained in the same unit area of recording media at single density.

modulation. The process by which some characteristic of one wave (usually high frequency) is varied in accordance with another wave or signal (usually low frequency). This technique is used in modems to make business-machine signals compatible with communication facilities.

modulation rate. The reciprocal of the measure of the shortest nominal time interval between successive significant instants of the modulated signal. If this measure is expressed in seconds, the modulation rate is expressed in baud.

module. (1) A program unit that is discrete and identifiable with respect to compiling, combining with other units, and loading. (2) A packaged functional hardware unit designed for use with other components.

modulo check. A calculation performed on values entered into a system. This calculation is designed to detect errors.

modulo-N check. A check in which an operand is divided by a number N (the modulus) to generate a remainder (check digit)

that is retained with the operand. For example, in a modulo-7 check, the remainder will be 0, 1, 2, 3, 4, 5, or 6. The operand is later checked by again dividing it by the modulus; if the remainder is not equal to the check digit, an error is indicated.

modulus. In a modulo-N check, the number by which the operand is divided.

monitor. Synonym for cathode ray tube display (CRT display).

most-significant digit. The leftmost (non-zero) digit. See also high-order position.

ms. Millisecond; 0.001 second.

multiplexer. A device capable of interleaving the events of two or more activities, or capable of distributing the events of an interleaved sequence to the respective activities.

multiprogramming. (1) Pertaining to the concurrent execution of two or more computer programs by a computer. (2) A mode of operation that provides for the interleaved execution of two or more computer programs by a single processor.

n. Prefix nano; 0.000,000,001.

NAND. A logic operator having the property that if P is a statement, Q is a statement, R is a statement,..., then the NAND of P, Q, R,... is true if at least one statement is false, false if all statements are true.

NAND gate. A gate in which the output is 0 only if all inputs are 1.

nano (n). Prefix 0.000,000,001.

nanosecond (ns). 0.000,000,001 second.

negative true. Synonym for active low.

negative-going edge. The edge of a pulse or signal changing in a negative direction. Synonymous with falling edge.

non-return-to-zero change-on-ones recording (NRZI). A transmission encoding method in which the data terminal equipment changes the signal to the opposite state to send a binary 1 and leaves it in the same state to send a binary 0.

non-return-to-zero (inverted) recording (NRZI). Deprecated term for non-return-to-zero change-on-ones recording.

NOR. A logic operator having the property that if P is a statement, Q is a statement, R is a statement, ..., then the NOR of P, Q, R, ... is true if all statements are false, false if at least one statement is true.

NOR gate. A gate in which the output is 0 only if at least one input is 1.

NOT. A logical operator having the property that if P is a statement, then the NOT of P is true if P is false, false if P is true.

NRZI. Non-return-to-zero change-on-ones recording.

ns. Nanosecond; 0.000,000,001 second.

NUL. The null character.

null character (NUL). A control character that is used to accomplish media-fill or time-fill, and that may be inserted into or removed from, a sequence of characters without affecting the meaning of the sequence; however, the control of the equipment or the format may be affected by this character.

odd-even check. Synonym for parity check.

offline. Pertaining to the operation of a functional unit without the continual control of a computer.

one-shot. A circuit that delivers one output pulse of desired duration for each input (trigger) pulse.

open circuit. (1) A discontinuous circuit; that is, one that is broken at one or more points and, consequently, cannot conduct current. Contrast with closed circuit. (2) Pertaining to a no-load condition; for example, the open-circuit voltage of a power supply.

open collector. A switching transistor without an internal connection between its collector and the voltage supply. A connection from the collector to the voltage supply is made through an external (pull-up) resistor.

operand. (1) An entity to which an operation is applied. (2) That which is operated upon. An operand is usually identified by an address part of an instruction.

operating system. Software that controls the execution of programs; an operating system may provide services such as resource allocation, scheduling, input/output control, and data management.

OR. A logic operator having the property that if P is a statement, Q is a statement, R is a statement,..., then the OR of P, Q, R,... is true if at least one statement is true, false if all statements are false.

OR gate. A gate in which the output is 1 only if at least one input is 1.

output. Pertaining to a device, process, or channel involved in an output process, or to the data or states involved in an output process.

output process. (1) The process that consists of the delivery of data from a data processing system, or from any part of it. (2) The return of information from a data processing system to an end user, including the translation of data from a machine language to a language that the end user can understand.

overcurrent. A current of higher than specified strength.

overflow indicator. (1) An indicator that signifies when the last line on a page has been printed or passed. (2) An indicator that is set on if the result of an arithmetic operation exceeds the capacity of the accumulator.

overrun. Loss of data because a receiving device is unable to accept data at the rate it is transmitted.

overvoltage. A voltage of higher than specified value.

parallel. (1) Pertaining to the concurrent or simultaneous operation of two or more devices, or to the concurrent performance of two or more activities. (2) Pertaining to the concurrent or simultaneous occurrence of two or more related activities in multiple devices or channels. (3) Pertaining to the simultaneity of two or more processes. (4) Pertaining to the simultaneous processing of the individual parts of a whole, such as the bits of a character and the characters of a word, using separate facilities for the various parts. (5) Contrast with serial.

parameter. (1) A variable that is given a constant value for a specified application and that may denote the application. (2) A name in a procedure that is used to refer to an argument passed to that procedure.

parity bit. A binary digit appended to a group of binary digits to make the sum of all the digits either always odd (odd parity) or always even (even parity).

parity check. (1) A redundancy check that uses a parity bit. (2) Synonymous with odd-even check.

PEL. Picture element.

personal computer. A small home or business computer that has a processor and keyboard and that can be connected to a television or some other monitor. An optional printer is usually available.

phototransistor. A transistor whose switching action is controlled by light shining on it.

picture element (PEL). The smallest displayable unit on a display.

polling. (1) Interrogation of devices for purposes such as to avoid contention, to determine operational status, or to determine readiness to send or receive data. (2) The process whereby stations are invited, one at a time, to transmit.

port. An access point for data entry or exit.

positive true. Synonym for active high.

positive-going edge. The edge of a pulse or signal changing in a positive direction. Synonymous with rising edge.

potentiometer. A variable resistor with three terminals, one at each end and one on a slider (wiper).

power supply. A device that produces the power needed to operate electronic equipment.

printed circuit. A pattern of conductors (corresponding to the wiring of an electronic circuit) formed on a board of insulating material.

printed-circuit board. A usually copper-clad plastic board used to make a printed circuit.

priority. A rank assigned to a task that determines its precedence in receiving system resources.

processing program. A program that performs such functions as compiling, assembling, or translating for a particular programming language.

processing unit. A functional unit that consists of one or more processors and all or part of internal storage.

processor. (1) In a computer, a functional unit that interprets and executes instructions. (2) A functional unit, a part of another unit such as a terminal or a processing unit, that interprets and executes instructions. (3) Deprecated term for processing program. (4) See microprocessor.

program. (1) A series of actions designed to achieve a certain result. (2) A series of instructions telling the computer how to handle a problem or task. (3) To design, write, and test computer programs.

programmable read-only memory (PROM). A read-only memory that can be programmed by the user.

programming language. (1) An artificial language established for expressing computer programs. (2) A set of characters and rules with meanings assigned prior to their use, for writing computer programs.

programming system. One or more programming languages and the necessary software for using these languages with particular automatic data-processing equipment.

PROM. Programmable read-only memory.

propagation delay. (1) The time necessary for a signal to travel from one point on a circuit to another. (2) The time delay between a signal change at an input and the corresponding change at an output.

protocol. (1) A specification for the format and relative timing of information exchanged between communicating parties. (2) The set of rules governing the operation of functional units of a communication system that must be followed if communication is to be achieved.

pulse. A variation in the value of a quantity, short in relation to the time schedule of interest, the final value being the same as the initial value.

radio frequency (RF). An ac frequency that is higher than the highest audio frequency. So called because of the application to radio communication.

radix. (1) In a radix numeration system, the positive integer by which the weight of the digit place is multiplied to obtain the weight of the digit place with the next higher weight; for example, in the decimal numeration system the radix of each digit place is 10. (2) Another term for base.

radix numeration system. A positional representation system in which the ratio of the weight of any one digit place to the weight of the digit place with the next lower weight is a positive integer (the radix). The permissible values of the character in any digit place range from 0 to one less than the radix.

RAM. Random access memory. Read/write memory.

random access memory (RAM). Read/write memory.

RAS. In the IBM Personal Computer, row address strobe.

raster. In computer graphics, a predetermined pattern of lines that provides uniform coverage of a display space.

read. To acquire or interpret data from a storage device, from a data medium, or from another source.

read-only memory (ROM). A storage device whose contents cannot be modified. The memory is retained when power is removed.

read/write memory. A storage device whose contents can be modified. Also called RAM.

recoverable error. An error condition that allows continued execution of a program.

red-green-blue-intensity (RGBO). The description of a direct-drive color monitor that accepts input signals of red, green, blue, and intensity.

redundancy check. A check that depends on extra characters attached to data for the detection of errors. See cyclic redundancy check.

register. (1) A storage device, having a specified storage capacity such as a bit, a byte, or a computer word, and usually intended for a special purpose. (2) A storage device in which specific data is stored.

retry. To resend the current block of data (from the last EOB or ETB) a prescribed number of times, or until it is entered correctly or accepted.

reverse video. A form of highlighting a character, field, or cursor by reversing the color of the character, field, or cursor with its background; for example, changing a red character on a black background to a black character on a red background.

RF. Radio frequency.

RF modulator. The device used to convert the composite video signal to the antenna level input of a home TV.

RGBO. Red-green-blue-intensity.

rising edge. Synonym for positive-going edge.

ROM. Read-only memory.

ROM/BIOS. The ROM resident basic input/output system, which provides the level control of the major I/O devices in the computer system.

row address strobe (RAS). A signal that latches the row address in a memory chip.

RS-232C. A standard by the EIA for communication between computers and external equipment.

RTS. Request to send. Associated with modem control.

run. A single continuous performance of a computer program or routine.

saturation. In computer graphics, the purity of a particular hue. A color is said to be saturated when at least one primary color (red, blue, or green) is completely absent.

scaling. In computer graphics, enlarging or reducing all or part of a display image by multiplying the coordinates of the image by a constant value.

schematic. The representation, usually in a drawing or diagram form, of a logical or physical structure.

Schottky TTL. A version (S series) of TTL with faster switching speed, but requiring more power. See also transistor-transistor logic and low power Schottky TTL.

SDL. Shielded Data Link

SDLC. Synchronous Data Link Control.

sector. That part of a track or band on a magnetic drum, a magnetic disk, or a disk pack that can be accessed by the magnetic heads in the course of a predetermined rotational displacement of the particular device.

SERDES. Serializer/deserializer.

serial. (1) Pertaining to the sequential performance of two or more activities in a single device. In English, the modifiers serial and parallel usually refer to devices, as opposed to sequential and consecutive, which refer to processes. (2) Pertaining to the sequential or consecutive occurrence of two or more related activities in a single device or channel. (3) Pertaining to the sequential processing of the individual parts of a whole, such as the bits of a character or the characters of a word, using the same facilities for successive parts. (4) Contrast with parallel.

serializer/deserializer (SERDES). A device that serializes output from, and deserializes input to, a business machine.

setup. (1) In a computer that consists of an assembly of individual computing units, the arrangement of interconnections between the units, and the adjustments needed for the computer to operate. (2) The preparation of a computing system to perform a job or job step. Setup is usually performed by an operator and often involves performing routine functions, such as mounting tape reels. (3) The preparation of the system for normal operation.

short circuit. A low-resistance path through which current flows, rather than through a component or circuit.

signal. A variation of a physical quantity, used to convey data.

sink. A device or circuit into which current drains.

software. (1) Computer programs, procedures, and rules concerned with the operation of a data processing system. (2) Contrast with hardware.

source. The origin of a signal or electrical energy.

square wave. An alternating or pulsating current or voltage whose waveshape is square.

square wave generator. A signal generator delivering an output signal having a square waveform.

SS. Start-stop.

start bit. (1) A signal to a receiving mechanism to get ready to receive data or perform a function. (2) In a start-stop system, a signal preceding a character or block that prepares the receiving device for the reception of the code elements.

start-of-text (STX). A transmission control character that precedes a text and may be used to terminate the message heading.

start-stop system. A data transmission system in which each character is preceded by a start bit and is followed by a stop bit.

start-stop (SS) transmission. (1) Asynchronous transmission such that a group of signals representing a character is preceded by a start bit and followed by a stop bit. (2) Asynchronous transmission in which a group of bits is preceded by a start bit that prepares the receiving mechanism for the reception and registration of a character and is followed by at least one stop bit that enables the receiving mechanism to come to an idle condition pending the reception of the next character.

static memory. RAM using flip-flops as the memory elements. Data is retained as long as power is applied to the flip-flops. Contrast with dynamic memory.

stop bit. (1) A signal to a receiving mechanism to wait for the next signal. (2) In a start-stop system, a signal following a character or block that prepares the receiving device for the reception of a subsequent character or block.

storage. (1) A storage device. (2) A device, or part of a device, that can retain data. (3) The retention of data in a storage device. (4) The placement of data into a storage device.

strobe. An instrument that emits adjustable-rate flashes of light. Used to measure the speed of rotating or vibrating objects.

STX. Start-of-text.

symbol. (1) A conventional representation of a concept. (2) A representation of something by reason of relationship, association, or convention.

synchronization. The process of adjusting the corresponding significant instants of two signals to obtain the desired phase relationship between these instants.

Synchronous Data Link Control (SDLC). A protocol for management of data transfer over a data link.

synchronous transmission. (1) Data transmission in which the time of occurrence of each signal representing a bit is related to a fixed time frame. (2) Data transmission in which the sending and receiving devices are operating continuously at substantially the same frequency and are maintained, by means of correction, in a desired phase relationship.

syntax. (1) The relationship among characters or groups of characters, independent of their meanings or the manner of their interpretation and use. (2) The structure of expressions in a language. (3) The rules governing the structure of a language. (4) The relationships among symbols.

text. In ASCII and data communication, a sequence of characters treated as an entity if preceded and terminated by one STX and one ETX transmission control character, respectively.

time-out. (1) A parameter related to an enforced event designed to occur at the conclusion of a predetermined elapsed time. A time-out condition can be cancelled by the receipt of an appropriate time-out cancellation signal. (2) A time interval allotted for certain operations to occur; for example, response to polling or addressing before system operation is interrupted and must be restarted.

track. (1) The path or one of the set of paths, parallel to the reference edge on a data medium, associated with a single reading or writing component as the data medium moves past the component. (2) The portion of a moving data medium such as a drum, or disk, that is accessible to a given reading head position.

transistor-transistor logic (TTL). A popular logic circuit family that uses multiple-emitter transistors.

translate. To transform data from one language to another.

transmission. (1) The sending of data from one place for reception elsewhere. (2) In ASCII and data communication, a series of characters including headings and text. (3) The dispatching of a signal, message, or other form of intelligence by wire, radio, telephone, or other means. (4) One or more blocks or messages. For BSC and start-stop devices, a transmission is terminated by an EOT character. (5) Synonymous with data transmission.

TTL. Transistor-transistor logic.

typematic key. A keyboard key that repeats its function when held pressed.

V. Volt.

vector. In computer graphics, a directed line segment.

video. Computer data or graphics displayed on a cathode ray tube, monitor, or display.

view point. In computer graphics, the origin from which angles and scales are used to map virtual space into display space.

viewing reference point. In computer graphics, a point in the modeling coordinate space that is a defined distance from the view point.

viewing transformation. Operations on the coordinates of an object (usually matrix multiplications) that cause the view of the object to be rotated about any axis, translated (moved without rotating), and/or scaled (changed in size along any or all dimensions). Viewing transformation differs from modeling transformation in that perspective is considered. See also modeling transformation.

viewplane. The visible plane of a CRT display screen that completely contains a defined window.

viewport. In computer graphics, a predefined part of the CRT display space.

volt. The basic practical unit of electric pressure. The potential that causes electrons to flow through a circuit.

W. Watt.

watt. The practical unit of electric power.

window. (1) A predefined part of the virtual space. (2) The visible area of a viewplane.

word. (1) A character string or a bit string considered as an entity. (2) See computer word.

write. To make a permanent or transient recording of data in a storage device or on a data medium.

write precompensation. The varying of the timing of the head current from the outer tracks to the inner tracks of the diskette to keep a constant 'write' signal.

yon plane. In computer graphics, a plane that is perpendicular to the line joining the viewing reference point and the view point, and that lies beyond the viewing reference point. Any part of an object beyond the yon plane is not seen. See also hither plane.

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